NVIDIA Data Center GPU Driver version
510.85.02 (Linux) / 513.46 (Windows)

Release Notes
Table of Contents

Chapter 1. Version Highlights.............................................................................................. 1
  1.1. Software Versions.............................................................................................................. 1
  1.2. Fixed Issues.................................................................................................................... 2
  1.3. Known Issues................................................................................................................... 2
Chapter 2. Virtualization..................................................................................................... 5
Chapter 3. Hardware and Software Support.......................................................................... 7
Chapter 1. Version Highlights

This section provides highlights of the NVIDIA Data Center GPU R510 Driver (version 510.85.02 Linux and 513.46 Windows).

For changes related to the 510 release of the NVIDIA display driver, review the file “NVIDIA_Changelog” available in the .run installer packages.

- Linux driver release date: 08/02/2022
- Windows driver release date: 08/02/2022

1.1. Software Versions

For this release, the software versions are listed below.

- CUDA Toolkit 11: 11.6
  
  Note that starting with CUDA 11, individual components of the toolkit are versioned independently. For a full list of the individual versioned components (e.g. nvcc, CUDA libraries etc.), see the CUDA Toolkit Release Notes

- NVIDIA Data Center GPU Driver: 510.85.02 (Linux) / 513.46 (Windows)

- Fabric Manager: 510.85.02 (Use nv-fabricmanager -v)

- GPU VBIOS:
  - 92.00.19.00.01 (NVIDIA A100 SKU200 with heatsink for HGX A100 8-way and 4-way)
  - 92.00.19.00.02 (NVIDIA A100 SKU202 w/o heatsink for HGX A100 4-way)

- NVSwitch VBIOS: 92.10.14.00.01

- NVFlash: 5.641

Due to a revision lock between the VBIOS and driver, VBIOS versions >= 92.00.18.00.00 must use corresponding drivers >= 450.36.01. Older VBIOS versions will work with newer drivers.

For more information on getting started with the NVIDIA Fabric Manager on NVSwitch-based systems (for example, HGX A100), refer to the Fabric Manager User Guide.
1.2. Fixed Issues

- During ongoing testing, NVIDIA identified that due to an algorithm error in a very small number of hardest-to-round cases (less than 0.000007% of tested combinations), the results of 64-bit floating-point division in default round-to-nearest-even mode can differ from the IEEE754 standard by 1 least-significant bit in default round-to-nearest-even mode.

Floating-point computations have many sources of error accumulation and most algorithms will not have encountered this discrepancy. NVIDIA recommends that all developers requiring strict IEEE754 compliance update to CUDA Toolkit 11.7 Update 1 or newer.

The affected algorithm is present in both offline compilation as well as just-in-time (JIT) compilation. As JIT compilation is handled by the driver, NVIDIA recommends updating to driver version 510.85.02 when IEEE754 compliance is required and when using JIT.

This is a software algorithm fix and is not tied to specific hardware.

- Pascal GPU page faults could hit a NULL pointer dereference in the UVM driver if there was not enough system memory available to handle the faults.

- DEVID of POSTLTSSM was reused for a different table. This caused RM to parse the new table incorrectly. This was resolved by removing code which parses PostLTSSM since RISC-V devinit on PMU is currently disabled.

1.3. Known Issues

General

- `nvidia-release-upgrade` may report that not all updates have been installed and exit.

  When running the `nvidia-release-upgrade` command on DGX systems running DGX OS 4.99.x, it may exit and tell users: “Please install all available updates for your release before upgrading” even though all upgrades have been installed.

  Users who see this can run the following command:

  ```
  sudo apt install -y nvidia-fabricmanager-450/bionic-updates --allow-downgrades
  ```

  After running this, proceed with the regular upgrade steps:

  ```
  sudo apt update
  sudo apt full-upgrade -y
  sudo apt install -y nvidia-release-upgrade
  sudo nvidia-release-upgrade
  ```
When installing a driver on SLES15 or openSUSE15 that previously had an R515 driver installed, users need to run the following command afterwards to finalize the installation:

```
sudo zypper install --force nvidia-gfxG05-kmp-default
```

Without doing this, users may see the kernel objects as missing.

If you encounter an error on RHEL7 when installing with `cuda-drivers-fabricmanager` packages, use the following alternate instructions. For example:

```
new_version=510.85.02
sudo yum swap nvidia-driver-latest-dkms nvidia-driver-latest-dkms-$\{new_version\}
sudo yum install nvidia-fabric-manager-$\{new_version\}
```

By default, Fabric Manager runs as a systemd service. If using `DAEMONIZE=0` in the Fabric Manager configuration file, then the following steps may be required.

1. Disable FM service from auto starting. (`systemctl disable nvidia-fabricmanager`)
2. Once the system is booted, manually start FM process. (`/usr/bin/nv-fabricmanager -c /usr/share/nvidia/nvswitch/fabricmanager.cfg`). Note, since the process is not a daemon, the SSH/Shell prompt will not be returned (use another SSH shell for other activities or run FM as a background task).

On NVSwitch systems with Windows Server 2019 in shared NVSwitch virtualization mode, the host may hang or crash when a GPU is disabled in the guest VM. This issue is under investigation.

**GPU Performance Counters**

The use of developer tools from NVIDIA that access various performance counters requires administrator privileges. See this [note](#) for more details. For example, reading NVLink utilization metrics from `nvidia-smi` (`nvidia-smi nvlink -g 0`) would require administrator privileges.

**NoScanout Mode**

NoScanout mode is no longer supported on NVIDIA Data Center GPU products. If NoScanout mode was previously used, then the following line in the “screen” section of `/etc/X11/xorg.conf` should be removed to ensure that X server starts on data center products:

```
Option         "UseDisplayDevice" "None"
```

NVIDIA Data Center GPU products now support one display of up to 4K resolution.

**Unified Memory Support**

Some Unified Memory APIs (for example, CPU page faults) are not supported on Windows in this version of the driver. Review the CUDA Programming Guide on the system requirements for Unified Memory.
CUDA and unified memory is not supported when used with Linux power management states S3/S4.

**IMPU FRU for Volta GPUs**

The driver does not support the IPMI FRU multi-record information structure for NVLink. See the Design Guide for Tesla P100 and Tesla V100-SXM2 for more information.

**OpenCL 3.0 Known Issues**

**Device side enqueue**

- Device-Side-Enqueue related queries may return 0 values, although corresponding built-ins can be safely used by kernel. This is in accordance with conformance requirements described at https://www.khronos.org/registry/OpenCL/specs/3.0-unified/html/OpenCL_API.html#opencl-3.0-backwardscompatibility

- Shared virtual memory - the current implementation of shared virtual memory is limited to 64-bit platforms only.
Chapter 2. Virtualization

To make use of GPU passthrough with virtual machines running Windows and Linux, the hardware platform must support the following features:

- A CPU with hardware-assisted instruction set virtualization: Intel VT-x or AMD-V.
- Platform support for I/O DMA remapping.
- On Intel platforms the DMA remapper technology is called Intel VT-d.
- On AMD platforms it is called AMD IOMMU.

Support for these features varies by processor family, product, and system, and should be verified at the manufacturer’s website.

**Supported Hypervisors**

The following hypervisors are supported:

<table>
<thead>
<tr>
<th>Hypervisor</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Citrix XenServer</td>
<td>Version 6.0 and later</td>
</tr>
<tr>
<td>VMware vSphere (ESX/ESXi)</td>
<td>Version 5.1 and later.</td>
</tr>
<tr>
<td>Red Hat KVM</td>
<td>Red Hat Enterprise Linux 7 with KVM</td>
</tr>
<tr>
<td>Microsoft Hyper-V</td>
<td>Windows Server 2016 Hyper-V Generation 2</td>
</tr>
</tbody>
</table>

Data Center products now support one display of up to 4K resolution.

**Supported Graphics Cards**

The following GPUs are supported for device passthrough:

<table>
<thead>
<tr>
<th>GPU Family</th>
<th>Boards Supported</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA Ampere GPU Architecture</td>
<td>NVIDIA A100, A40, A30, A16, A10</td>
</tr>
<tr>
<td>Turing</td>
<td>NVIDIA T4</td>
</tr>
<tr>
<td>Volta</td>
<td>NVIDIA V100</td>
</tr>
<tr>
<td>Pascal</td>
<td>Tesla: P100, P40, P4</td>
</tr>
<tr>
<td>GPU Family</td>
<td>Boards Supported</td>
</tr>
<tr>
<td>------------</td>
<td>------------------</td>
</tr>
<tr>
<td>Maxwell</td>
<td>Tesla: M60, M40, M6, M4</td>
</tr>
</tbody>
</table>
Chapter 3. Hardware and Software Support

Support for these features varies by processor family, product, and system, and should be verified at the manufacturer’s website.

Supported Operating Systems for NVIDIA Data Center GPUs

The Release 510 driver is supported on the following operating systems:

- **Windows x86_64 operating systems:**
  - Microsoft Windows® Server 2022
  - Microsoft Windows® Server 2019
  - Microsoft Windows® Server 2016
  - Microsoft Windows® 11
  - Microsoft Windows® 10

- The table below summarizes the supported Linux 64-bit distributions. For a complete list of distributions, kernel versions supported, see the [CUDA Linux System Requirements](#) documentation.

<table>
<thead>
<tr>
<th>Distribution</th>
<th>x86_64</th>
<th>POWER</th>
<th>Arm64 Server</th>
</tr>
</thead>
<tbody>
<tr>
<td>OpenSUSE Leap 15.x (where y &lt;= 4)</td>
<td>Yes</td>
<td>No</td>
<td>No</td>
</tr>
<tr>
<td>Red Hat Enterprise Linux 8.y (where y &lt;= 6)</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Rocky Linux 8.y (where y &lt;= 6)</td>
<td>Yes</td>
<td>No</td>
<td>No</td>
</tr>
<tr>
<td>Red Hat Enterprise Linux / CentOS 7.y (where y &lt;= 9)</td>
<td>Yes</td>
<td>No</td>
<td>No</td>
</tr>
<tr>
<td>SUSE Linux Enterprise Server 15.y (where y &lt;= 4)</td>
<td>Yes</td>
<td>No</td>
<td>Yes (see note)</td>
</tr>
</tbody>
</table>
Note that SUSE Linux Enterprise Server (SLES) 15.3 is provided as a preview for Arm64 server since there are known issues when running some CUDA applications related to dependencies on glibc 2.27.

Supported Operating Systems and CPU Configurations for HGX A100

The Release 510 driver is validated with HGX A100 on the following operating systems and CPU configurations:

- **Linux 64-bit distributions**:
  - Red Hat Enterprise Linux 8.6 (in 4/8/16-GPU configurations)
  - Red Hat Enterprise Linux 7.9 (in 4/8/16-GPU configurations)
  - Rocky Linux 8.6 (in 4/8/16-GPU configurations)
  - CentOS Linux 7.9 (in 4/8/16-GPU configurations)
  - Ubuntu 18.04.6 LTS (in 4/8/16-GPU configurations)
  - SUSE SLES 15.4 (in 4/8/16-GPU configurations)

- **Windows 64-bit distributions**:
  - Windows Server 2019 (in 1/2/4/8-GPU configurations; 16-GPU configurations are currently not supported)
  - Windows is supported only in shared NVSwitch virtualization configurations.

- **CPU Configurations**:
  - AMD Rome in PCIe Gen4 mode
  - Intel Skylake/Cascade Lake {4-socket} in PCIe Gen3 mode

Supported Virtualization Configurations

The Release 510 driver is validated with HGX A100 on the following configurations:

- **Passthrough (full visibility of GPUs and NVSwitches to guest VMs)**:
  - 8-GPU configurations with Ubuntu 18.04.6 LTS

- **Shared NVSwitch (guest VMs only have visibility of GPUs and full NVLink bandwidth between GPUs in the same guest VM)**:
  - 1/2/4/8/16-GPU configurations with Ubuntu 18.04.5 LTS
  - 1/2/4/8-GPU configurations with Windows Server 2019
API Support

This release supports the following APIs:

- NVIDIA® CUDA® 11.6 for NVIDIA® Maxwell™, Pascal™, Volta™, Turing™ and NVIDIA Ampere architecture GPUs
- OpenGL® 4.6
- Vulkan® 1.3
- DirectX 11
- DirectX 12 (Windows 10)
- Open Computing Language (OpenCL™ software) 3.0

Note that for using graphics APIs on Windows (i.e. OpenGL, Vulkan, DirectX 11 and DirectX 12) or any WDDM 2.0+ based functionality on Data Center GPUs, vGPU is required. See the vGPU documentation for more information.

Supported NVIDIA Data Center GPUs

The NVIDIA Data Center GPU driver package is designed for systems that have one or more Data Center GPU products installed. This release of the driver supports CUDA C/C++ applications and libraries that rely on the CUDA C Runtime and/or CUDA Driver API.

Attention: Release 470 was the last driver branch to support Data Center GPUs based on the Kepler architecture. This includes discontinued support for the following compute capabilities:

- sm_30 (Kepler)
- sm_32 (Kepler)
- sm_35 (Kepler)
- sm_37 (Kepler)

For more information on GPU products and compute capability, see https://developer.nvidia.com/cuda-gpus.

### NVIDIA Server Platforms

<table>
<thead>
<tr>
<th>Product</th>
<th>Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA HGX A100</td>
<td>A100 and NVSwitch</td>
</tr>
<tr>
<td>NVIDIA HGX-2</td>
<td>V100 and NVSwitch</td>
</tr>
</tbody>
</table>

### RTX-Series / T-Series Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA RTX A6000</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA RTX A5000</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA RTX A4000</td>
<td>NVIDIA Ampere</td>
</tr>
</tbody>
</table>
## RTX-Series / T-Series Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>Quadro RTX 8000</td>
<td>Turing</td>
</tr>
<tr>
<td>Quadro RTX 6000</td>
<td>Turing</td>
</tr>
<tr>
<td>NVIDIA T1000</td>
<td>Turing</td>
</tr>
<tr>
<td>NVIDIA T600</td>
<td>Turing</td>
</tr>
<tr>
<td>NVIDIA T400</td>
<td>Turing</td>
</tr>
</tbody>
</table>

## Data Center A-Series Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA A100X</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA A100</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA A100 80 GB PCIe</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA A40</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA A30. A30X</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA A16</td>
<td>NVIDIA Ampere</td>
</tr>
<tr>
<td>NVIDIA A10, A10M</td>
<td>NVIDIA Ampere</td>
</tr>
</tbody>
</table>

## Data Center T-Series Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA T4</td>
<td>Turing</td>
</tr>
</tbody>
</table>

## Data Center V-Series Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA V100</td>
<td>Volta</td>
</tr>
</tbody>
</table>

## Data Center P-Series Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA Tesla P100</td>
<td>Pascal</td>
</tr>
<tr>
<td>NVIDIA Tesla P40</td>
<td>Pascal</td>
</tr>
<tr>
<td>NVIDIA Tesla P4</td>
<td>Pascal</td>
</tr>
</tbody>
</table>

## Data Center M-Class Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA Tesla M60</td>
<td>Maxwell</td>
</tr>
</tbody>
</table>
### Data Center M-Class Products

<table>
<thead>
<tr>
<th>Product</th>
<th>GPU Architecture</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA Tesla M40 24 GB</td>
<td>Maxwell</td>
</tr>
<tr>
<td>NVIDIA Tesla M40</td>
<td>Maxwell</td>
</tr>
<tr>
<td>NVIDIA Tesla M6</td>
<td>Maxwell</td>
</tr>
<tr>
<td>NVIDIA Tesla M4</td>
<td>Maxwell</td>
</tr>
</tbody>
</table>
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