

TESLA DRIVER VERSION 410.72(LINUX)/411.82(WINDOWS)

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Chapter 1. VERSION HIGHLIGHTS

This section provides highlights of the NVIDIA Tesla 410 Driver, version 410.72 for Linux and 411.82 for Windows.

1.1. New Features

- Added support for CUDA 10. For more information on CUDA 10, refer to the CUDA Toolkit 10 Release Notes
- ► Added support for NVIDIA Tesla T4 GPUs.
- Added support for CUDA compatibility upgrade paths for Tesla GPUs. See the official documentation for more information on the updated packages and deployment information.
- NVML Updates
 - Added new ECC fields NVML_MEMORY_LOCATION_DRAM and NVML_MEMORY_LOCATION_SRAM to nvmlMemoryLocation_enum for Turing GPUs
 - Added PCIe replay counters NVML_FI_DEV_PCIE_REPLAY_COUNTER and NVML FI DEV PCIE REPLAY ROLLOVER COUNTER
 - Added a new throttle reason for clocks (nvmlClocksThrottleReasonDisplayClockSetting)
 - ► Added nvmlSystemGetCudaDriverVersion_v2 to return the CUDA driver version in use
 - ► Added API nvmlDeviceGetRetiredPages_v2 to return the list of retired pages by source, including pages that are pending retirement, including timestamps
 - Added APIs to support blacklisting of GPUs
 - Added the ability to lock clocks in nvidia-smi and NVML (nvmlDeviceSetGpuLockedClocks and nvmlDeviceResetGpuLockedClocks APIs). The following commands can be used in nvidia-smi:
 - \$ nvidia-smi -lgc/--lcok-gpu-clock <minGpuClock. maxGpuClock>
 - \$ nvidia-smi -rgc/--reset-gpu-clock

- Added SMBPBI support to query current state and enable ECC mode
- Added SMBPBI support to query if a GPU reset is required

1.2. Improvements

- Added two new CUDA driver error codes
 (CUDA_ERROR_SYSTEM_DRIVER_MISMATCH and
 CUDA_ERROR_COMPAT_NOT_SUPPORTED_ON_DEVICE) to support CUDA compatibility upgrade paths
- Added a new field called "CUDA Version" to nvidia-smi to indicate the version of CUDA is currently in use by the system loader
- Added ability for nvidia-smi to handle SIGINT (e.g. ctrl-c) gracefully when using nvidia-smi -r to reset GPUs
- Added support for Intel C620-series (Lewisburg) chipsets to allow for CUDA P2P
- ▶ Improved kernel launch performance under CUDA debug API (e.g. cuda-gdb) on Volta
- Added various performance improvements for CUDA graphs APIs

1.3. Fixed Issues

- ► Fixed memory corruption issues for interop APIs between CUDA and Vulkan/ OpenGL on Linux x86.
- Fixed the metric calculation for single precision (FP32) utilization (single_precision_fu_utilization) from CUPTI (or nvprof) for Pascal (GP100) and Volta (V100) based GPUs.
- ► Fixed an issue with GPU passthrough virtualization on GP100 when system memory exceeds 40-bits (Pascal GPUs support up to 47-bits of physical addressing for system memory).
- ► Fixed an issue that caused a kernel panic when running jobs under MPS on POWER 9 systems.
- Fixed an issue with higher execution time for asynchronous CUDA API calls (e.g. cudaLaunchKernel or cudaStreamCreate/cudaStreamCreateDestory) to return when using managed memory.
- Fixed an issue where **nvidia-smi** would report incorrect GPU utilization when profiling via CUPTI (or nvprof) is enabled.
- Fixed a crash related to creating a Vulkan instance when restricting access to a subset of available GPUs.

1.4. Known Issues

NoScanout Mode

NoScanout mode is no longer supported on NVIDIA Tesla products. If NoScanout mode was previously used, then the following line in the "screen" section of /etc/X11/xorg.conf should be removed to ensure that X server starts on Tesla products:

```
Option "UseDisplayDevice" "None"
```

Tesla products now support one display of up to 4K resolution.

Unified Memory Support

Some Unified Memory APIs (for example, CPU page faults) are not supported on Windows in this version of the driver. Review the CUDA Programming Guide on the system requirements for Unified Memory

CUDA and unified memory is not supported when used with Linux power management states S3/S4.

IMPU FRU for Volta GPUs

The driver does not support the IPMI FRU multi-record information structure for NVLink. See the Design Guide for Tesla P100 and Tesla V100-SXM2 for more information.

Video Memory Support

For Windows 7 64-bit, this driver recognizes up to the total available video memory on Tesla cards for Direct3D and OpenGL applications.

For Windows 7 32-bit, this driver recognizes only up to 4 GB of video memory on Tesla cards for DirectX, OpenGL, and CUDA applications.

Experimental OpenCL Features

Select features in OpenCL 2.0 are available in the driver for evaluation purposes only.

The following are the features as well as a description of known issues with these features in the driver:

Device side enqueue

- ► The current implementation is limited to 64-bit platforms only.
- OpenCL 2.0 allows kernels to be enqueued with global_work_size larger than the compute capability of the NVIDIA GPU. The current implementation supports only combinations of global_work_size and local_work_size that are within the

- compute capability of the NVIDIA GPU. The maximum supported CUDA grid and block size of NVIDIA GPUs is available at http://docs.nvidia.com/cuda/cuda-c-programming-guide/index.html#computecapabilities. For a given grid dimension, the global_work_size can be determined by CUDA grid size x CUDA block size.
- For executing kernels (whether from the host or the device), OpenCL 2.0 supports non-uniform ND-ranges where global_work_size does not need to be divisible by the local_work_size. This capability is not yet supported in the NVIDIA driver, and therefore not supported for device side kernel enqueues.

Shared virtual memory

► The current implementation of shared virtual memory is limited to 64-bit platforms only.

1.5. Virtualization

To make use of GPU passthrough with virtual machines running Windows and Linux, the hardware platform must support the following features:

- ▶ A CPU with hardware-assisted instruction set virtualization: Intel VT-x or AMD-V.
- Platform support for I/O DMA remapping.
- ▶ On Intel platforms the DMA remapper technology is called Intel VT-d.
- ▶ On AMD platforms it is called AMD IOMMU.

Support for these feature varies by processor family, product, and system, and should be verified at the manufacturer's website.

Supported Hypervisors

The following hypervisors are supported:

Hypervisor	Notes
Citrix XenServer	Version 6.0 and later
VMware vSphere (ESX / ESXi)	Version 5.1 and later.
Red Hat KVM	Red Hat Enterprise Linux 7 with KVM
Microsoft Hyper-V	Windows Server 2016 Hyper-V Generation 2 Windows Server 2012 R2 Hyper-V

Tesla products now support one display of up to 4K resolution.

Supported Graphics Cards

The following GPUs are supported for device passthrough:

GPU Family	Boards Supported
Turing	Tesla: T4
Volta	Tesla: V100
Pascal	Quadro: P2000, P4000, P5000, P6000, GP100
	Tesla: P100, P40, P4
Maxwell	Quadro: K2200, M2000, M4000, M5000, M6000, M6000 24GB
	Tesla: M60, M40, M6, M4
Kepler	Quadro: K2000, K4000, K4200, K5000, K5200, K6000
	Tesla: K10, K20, K20x, K20Xm, K20c, K20s, K40m, K40c, K40s, K40st, K40t, K80, K520

Chapter 2. HARDWARE AND SOFTWARE SUPPORT

Support for these feature varies by processor family, product, and system, and should be verified at the manufacturer's website.

Supported Operating Systems

The Release 410 driver is supported on the following operating systems:

- Windows 64-bit operating systems:
 - Microsoft Windows[®] Server 2016
 - ► Microsoft Windows[®] Server 2012 R2
 - ► Microsoft Windows[®] 10
 - ► Microsoft Windows[®] 8.1 (**Not supported starting with Volta**)
 - Microsoft Windows[®] 7 (**Not supported starting with Volta**)
- Linux 64-bit distributions:
 - ▶ Fedora 27
 - OpenSUSE Leap 15
 - Red Hat Enterprise Linux 6 (**Deprecated**)
 - Red Hat Enterprise Linux 7
 - SUSE Linux Enterprise Server 15
 - ▶ Ubuntu 14.04 LTS
 - ▶ Ubuntu 16.04 LTS
 - ▶ Ubuntu 18.04 LTS

API Support

This release supports the following APIs:

- ► NVIDIA[®] CUDA[®] 10.0 for NVIDIA[®] KeplerTM, MaxwellTM, PascalTM, VoltaTM and TuringTM GPUs
- ▶ OpenGL[®] 4.5

- ▶ Vulkan[®] 1.1
- ▶ DirectX 11
- ▶ DirectX 12 (Windows 10)
- ► Open Computing Language (OpenCLTM software) 1.2

Supported NVIDIA Tesla GPUs

The Tesla driver package is designed for systems that have one or more Tesla products installed. This release of the Tesla driver supports CUDA C/C++ applications and libraries that rely on the CUDA C Runtime and/or CUDA Driver API.

Tesla T-Series Products	
Product	GPU Architecture
NVIDIA Tesla T4	Turing

Tesla V-Series Products	
Product	GPU Architecture
NVIDIA Tesla V100	Volta

Tesla P-Series Products	
Product	GPU Architecture
NVIDIA Tesla P100	Pascal
NVIDIA Tesla P40	Pascal
NVIDIA Tesla P4	Pascal

Tesla K-Series Products	
Product	GPU Architecture
NVIDIA Tesla K520	Kepler
NVIDIA Tesla K80	Kepler
NVIDIA Tesla K40 (m/c/s/st/t)	Kepler
NVIDIA Tesla K20 (x/c/m/Xm/x)	Kepler
NVIDIA Tesla K10	Kepler
NVIDIA Tesla K8	Kepler

Tesla M-Class Products	
Product	GPU Architecture
NVIDIA Tesla M60	Maxwell
NVIDIA Tesla M40 24 GB	Maxwell
NVIDIA Tesla M40	Maxwell
NVIDIA Tesla M6	Maxwell
NVIDIA Tesla M4	Maxwell

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