

## **BEST PRACTICES FOR CUDNN**

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## **Best Practices**

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# Chapter 1. INTRODUCTION



**Attention** These guidelines are applicable to 3D convolution and deconvolution functions starting in cuDNN v7.6.3.

This document provides guidelines for setting the cuDNN library parameters to enhance the performance of 3D convolutions. Specifically, these guidelines are focused on settings such as filter sizes, padding and dilation settings. Additionally, an application-specific use-case, namely, medical imaging, are presented to demonstrate the performance enhancement of 3D convolutions with these recommended settings.

Specifically, these guidelines are applicable to the following functions and their associated data types:

- cudnnConvolutionForward()
- cudnnConvolutionBackwardData()
- cudnnConvolutionBackwardFilter()

For more information, see the cuDNN Developer Guide and cuDNN API.

## Chapter 2. BEST PRACTICES FOR MEDICAL IMAGING

To optimize your performance in your model, ensure you meet the following general guidelines:

#### Layout

The layout is in NCHW format.

#### **Filter size**

The filter size is **Tx1x1**, **Tx2x2**, **Tx3x3**, **Tx5x5**, where **T** is a positive integer. There are additional limits for the value of **T** in **wgrad** and strided **dgrad**.

#### Padding

(filter size // 2), for example, 0x0x0 for 1x1x1 filter, 1x1x1 for 3x3x3 filter

#### Stride

Arbitrary for forward and backward filter; **dgrad**/**deconv**: 1x1x1 or 2x2x2 with 2x2x2 filter.

#### **Convolution mode**

Cross-correlation for forward, arbitrary for dgrad and wgrad.

#### Dilation

The dilation is 1x1x1.

#### Platform

The platform is Volta with input/output channels divisible by 8.

#### Batch/image size

cuDNN will fallback to non-Tensor Core kernel if it determines that the workspace required is larger than 256MB of GPU memory. The workspace required depends on many factors. For the Tensor Core kernels, the workspace size generally scales linearly with output tensor size. Therefore, this can be mitigated by using smaller image sizes or minibatch sizes.

# 2.1. Recommended Settings In cuDNN While Performing 3D Convolutions

The following table shows the specific improvements that were made in each patch release.

### Table 1 Recommended settings while performing 3D convolutions

Volta						
cuDNN versi	on	7.6.2	7.6.2	7.6.1	7.6.1	7.6.1
Convolution (3D or 2D)		3D				
Convolution or deconvolution (fprop, dgrad, 0r wgrad)		dgrad	fprop	wgrad	dgrad	fprop
Grouped convolution	Yes or No	No				
	Group size	NA	NA			
Data layout format (NHWC/NCHW) <sup>1</sup>		NCDHW	NCDHW <sup>2</sup>			
Input/output precision (FP16, FP32, or FP64)		FP16 or FP32	FP16 <sup>3</sup> or FP32 <sup>4</sup>			
Accumulator (compute) precision (FP16, FP32, or FP64)		Better to be the same input and output precision	FP32			
Filter (kernel) sizes		2x2x2	<ul> <li>T<sup>5</sup>x1x1</li> <li>Tx2x2</li> <li>Tx3x3</li> <li>Tx5x5</li> </ul>	<ul> <li>1x1x1</li> <li>2x2x2</li> <li>3x3x3</li> <li>5x5x5</li> </ul>	<ul> <li>Tx1x1</li> <li>Tx2x2</li> <li>Tx3x3</li> <li>Tx5x5</li> </ul>	<ul> <li>Tx1x1</li> <li>Tx2x2</li> <li>Tx3x3</li> <li>Tx5x5</li> </ul>
Padding Filter // 2 <sup>6</sup>						
Image sizes			256 MB WS limit <sup>7</sup>	256 MB WS limit <sup>8</sup>	256 MB WS limit <sup>9</sup>	256 MB WS limit <sup>10</sup>

<sup>&</sup>lt;sup>1</sup> NHWC/NCHW corresponds to NDHWC/NCDHW in 3D convolution.

```
buffer size = ceil(k / tileN) * tileN * ceil(n*o*p*q / tileM) *
    tileM
```

```
<sup>8</sup> wgrad: reduction
```

```
buffer size = ceil(c / tileN)* tileN * ceil(k*t*r*s /
```

<sup>&</sup>lt;sup>2</sup> With NCHW <> NHWC format transformation.

<sup>&</sup>lt;sup>3</sup> FP16: cudnn\_tensorop\_math

<sup>&</sup>lt;sup>4</sup> FP32: cudnn\_tensorop\_math\_allow\_conversion

<sup>&</sup>lt;sup>5</sup> An arbitrary positive value.

<sup>&</sup>lt;sup>6</sup> padding = filter // 2 constraints is no longer required in integrated kernel

<sup>&</sup>lt;sup>7</sup> fprop: reduction

Volta						
cuDNN version		7.6.2	7.6.2	7.6.1	7.6.1	7.6.1
Number of channels	С	Arbitrary	0 mod 8	-		
	К	Arbitrary	0 mod 8			
Convolution mode			Cross- correlation			Cross- correlation
Strides		2x2x2	Arbitrary stride	1x1x1		
Dilation		1x1x1		•		-

## Chapter 3. MEDICAL IMAGING PERFORMANCE

The following table shows the average speed-up of **unique cuDNN 3D convolution calls** for each network that satisfies the conditions in Best Practices For Medical Imaging.

Model	Batchsize	Avg. Speed-up of unique cuDNN 3D convolution API calls (7.6.3 vs. 7.5.1)	
V-Net (3D-Image segmentation)	2	4.4x	
	4	4.4x	
	8	4x	
	16	4x	
	32	4x	
	64	3.4x	
	128	3x	
3D-UNet (3D-Image	2	4.4x	
Segmentation)	4	4.1x	
	8	4.4x	
	16	4.3x	
	32	4x	
	64	4x	
	128	4.2x	

# Chapter 4. MEDICAL IMAGING LIMITATIONS

Your application will be functional but slow if the model has:

- Channel counts lower than 32 (gets worse the lower it is)
- Data gradients for convolutions with stride

If the above is in the network, use **cuDNNFind** to get the best option.

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