PyTorch

Release Notes
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Chapter 1. PyTorch Overview

The NVIDIA® Deep Learning SDK accelerates widely-used deep learning frameworks such as PyTorch.

PyTorch is a GPU-accelerated tensor computational framework with a Python front end. Functionality can be easily extended with common Python libraries such as NumPy, SciPy, and Cython. Automatic differentiation is done with a tape-based system at both a functional and neural network layer level. This functionality brings a high level of flexibility and speed as a deep learning framework and provides accelerated NumPy-like functionality.

PyTorch also includes standard defined neural network layers, deep learning optimizers, data loading utilities, and multi-gpu, and multi-node support. Functions are executed immediately instead of enqueued in a static graph, improving ease of use and provides a sophisticated debugging experience.

In the container, see /workspace/README.md for information about customizing your PyTorch image. For more information about PyTorch, including tutorials, documentation, and examples, see:

- PyTorch website
- PyTorch project

This document provides information about the key features, software enhancements and improvements, known issues, and how to run this container.
About this task

**Before** you can pull a container from the NGC container registry:

- Install Docker.
  - For NVIDIA DGX™ users, see [Preparing to use NVIDIA Containers Getting Started Guide](https://docs.nvidia.com/ngc/getting-started/prepare/)
  - For non-DGX users, see NVIDIA® GPU Cloud™ (NGC) container registry [installation documentation](https://docs.nvidia.com/ngc/getting-started/installation/)
  - based on your platform.

- Ensure that you have access and can log in to the NGC container registry.
  
  Refer to [NGC Getting Started Guide](https://docs.nvidia.com/ngc/getting-started/) for more information.

The deep learning frameworks, the NGC Docker containers, and the deep learning framework containers are stored in the `nvcr.io/nvidia` repository.
Chapter 3. Running PyTorch

Before you begin

Before you can run an NGC deep learning framework container, your Docker® environment must support NVIDIA GPUs. To run a container, issue the appropriate command as explained in Running A Container and specify the registry, repository, and tags.

About this task

On a system with GPU support for NGC containers, when you run a container, the following occurs:

- The Docker engine loads the image into a container which runs the software.
- You define the runtime resources of the container by including additional flags and settings that are used with the command.
  These flags and settings are described in Running A Container.
- The GPUs are explicitly defined for the Docker container (defaults to all GPUs, but can be specified by using the NVIDIA_VISIBLE_DEVICES environment variable).

For more information, refer to the nvidia-docker documentation.

Note: Starting in Docker 19.03, complete the steps below.

The method implemented in your system depends on the DGX OS version that you installed (for DGX systems), the NGC Cloud Image that was provided by a Cloud Service Provider, or the software that you installed to prepare to run NGC containers on TITAN PCs, Quadro PCs, or NVIDIA Virtual GPUs (vGPUs).

Procedure

1. Issue the command for the applicable release of the container that you want.
   The following command assumes that you want to pull the latest container.

   ```
   docker pull nvcr.io/nvidia/pytorch:24.01-py3
   ```

2. Open a command prompt and paste the pull command.
Ensure that the pull successfully completes before you proceed to step 3.
3. To run the container image, select one of the following modes:

▸ Interactive
  - If you have **Docker 19.03 or later**, a typical command to launch the container is:
    ```
    docker run --gpus all -it --rm -v local_dir:container_dir nvcr.io/nvidia/pytorch:<xx.xx>-py3
    ```
  - If you have **Docker 19.02 or earlier**, a typical command to launch the container is:
    ```
    nvidia-docker run -it --rm -v local_dir:container_dir nvcr.io/nvidia/pytorch:<xx.xx>-py3
    ```

▸ Non-interactive
  - If you have **Docker 19.03 or later**, a typical command to launch the container is:
    ```
    docker run --gpus all --rm -v local_dir:container_dir nvcr.io/nvidia/pytorch:<xx.xx>-py3 <command>
    ```
  - If you have **Docker 19.02 or earlier**, a typical command to launch the container is:
    ```
    nvidia-docker run --rm -v local_dir:container_dir nvcr.io/nvidia/pytorch:<xx.xx>-py3 <command>
    ```

**Note:** If you use multiprocessing for multi-threaded data loaders, the default shared memory segment size with which the container runs might not be enough. Therefore, you should increase the shared memory size by issuing one of the following commands:

▸ **--ipc=host**
▸ **--shm-size=<requested memory size>**

in the command line to
```
docker run --gpus all
```
Chapter 4. PyTorch Release 24.04

The NVIDIA container image for PyTorch, release 24.04 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.10/dist-packages/torch) in the container image.

The container also includes the following:

- **Ubuntu 22.04** including **Python 3.10**
- **NVIDIA CUDA 12.4**
- **NVIDIA cuBLAS 12.4.5.8**
- **NVIDIA cuDNN 9.1.0.70**
- **NVIDIA NCCL 2.21.5**
- **NVIDIA RAPIDS™ 24.02**
- **rdma-core 39.0**
- **NVIDIA HPC-X 2.18**
- **OpenMPI 4.1.4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2024.1.0.13**
- **Nsight Systems 2024.2.1.38**
- **NVIDIA TensorRT™ 8.6.3**
- **Torch-TensorRT 2.3.0a0**
- **NVIDIA DALI® 1.36**
- **nvImageCodec 0.2.0.7**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 1.5**
- **PyTorch quantization wheel 2.1.2**
Driver Requirements

Release 24.04 is based on **NVIDIA CUDA 12.4.1**, which requires **NVIDIA Driver** release 545 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 470.57 (or later R470), 525.85 (or later R525), 535.86 (or later R535), or 545.23 (or later R545).

The CUDA driver's compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R450, R460, R510, and R520 drivers, which are not forward-compatible with CUDA 12.3. For a complete list of supported drivers, see the [CUDA Application Compatibility topic](#). For more information, see [CUDA Compatibility and Upgrades](#).

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 24.04 is based on 2.3.0a0+6ddf5cf85e.

Announcements

- Starting with the 24.03 release, the NVIDIA Optimized PyTorch container release provides access to [lightning-thunder](#).
- Starting with the 23.11 release, NVIDIA Optimized PyTorch containers supporting iGPU architectures are published, and able to run on Jetson devices. Please refer to the [Frameworks Support Matrix](#) for information regarding which iGPU hardware/software is supported by which container.
- Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.
- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.
- A preview of Torch-TensorRT (1.4.0dev0) is now included. Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0. Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example,
using export PYTHONPATH="/opt/conda/lib/python3.10/site-packages" if your Conda package manager was installed in /opt/conda. Starting with the 24.02 PyTorch NGC container, we have started the deprecation process on torchtext and torchdata. Both packages will be removed starting with release 24.05.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>24.04</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.4.1</td>
<td>2.3.0a0+6ddf5cf85@torch 8.6.3</td>
<td></td>
</tr>
<tr>
<td>24.03</td>
<td></td>
<td>NVIDIA CUDA 12.4.0.41</td>
<td>2.3.0a0+40ec155@torch 8.5.8</td>
<td></td>
</tr>
<tr>
<td>24.02</td>
<td></td>
<td>NVIDIA CUDA 12.3.2</td>
<td>2.3.0a0+ebedce2 @torch 8.5.1</td>
<td></td>
</tr>
<tr>
<td>24.01</td>
<td></td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.2.0a0+81ea7a4 @torch 8.6.1.6</td>
<td></td>
</tr>
<tr>
<td>23.12</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td></td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.11</td>
<td></td>
<td>NVIDIA CUDA 12.3.0</td>
<td>2.2.0a0+6a974bec @torch 8.5.2.2</td>
<td></td>
</tr>
<tr>
<td>23.10</td>
<td></td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.1.0a0+32f93b1 @torch 8.5.1</td>
<td></td>
</tr>
<tr>
<td>23.09</td>
<td></td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1 @torch 8.5.1.1</td>
<td></td>
</tr>
<tr>
<td>23.08</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba @torch 8.5.1.2</td>
<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+4136153 @torch 8.5.1.3</td>
<td></td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td></td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0 @torch 8.6.1</td>
<td></td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f @torch 8.6.1</td>
<td></td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>2.0.0a0+1767026 @torch 8.5.3</td>
<td></td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51 @torch 8.5.2.2</td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td></td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96 @torch 8.5.1</td>
<td></td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930 @torch 8.5.0.12</td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+d0d6b1f @torch 8.5.0.12</td>
<td></td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>-----------------</td>
<td>--------------------------</td>
<td>-----------------</td>
<td>------------------</td>
</tr>
<tr>
<td>22.08</td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
<td></td>
</tr>
<tr>
<td>22.07</td>
<td>NVIDIA CUDA 11.7</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
<td></td>
</tr>
<tr>
<td>22.06</td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
<td></td>
</tr>
<tr>
<td>22.05</td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
<td></td>
</tr>
<tr>
<td>22.03</td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.02</td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfed5ad28</td>
<td>TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
<td></td>
</tr>
<tr>
<td>21.11</td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
<td></td>
</tr>
<tr>
<td>21.10</td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+3fd9dfc</td>
<td>TensorRT 8.0.3</td>
<td></td>
</tr>
<tr>
<td>21.09</td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+3fd9dfc</td>
<td>TensorRT 8.0.1.6</td>
<td></td>
</tr>
<tr>
<td>21.08</td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.02</td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
<td></td>
</tr>
<tr>
<td>20.12</td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td></td>
<td>1.5.0a0+3bbd36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td>1.2.0a0</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation ([torch.cuda.amp](https://pytorch.org/docs/stable/amp.html)). AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.
APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers **a number of advantages** over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found [here](#).
- APEX AMP examples can be found [here](#).

For more information about AMP, see the [Training With Mixed Precision Guide](#).

**Tensor Core Examples**

The [tensor core examples provided in GitHub](#) and [NGC](#) focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest [deep learning example](#) networks and [model scripts](#) for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the [Aggregated Residual Transformations for Deep Neural Networks](#) paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on [GitHub](#).

- **SE-ResNext** model: This **ResNeXt101-32x4d** model has an added Squeeze-and-Excitation (SE) module that was introduced in the [Squeeze-and-Excitation Networks](#) paper.

  This model script is available on [GitHub](#).

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.

  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the [codebase](#) that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

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- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the [Jasper: An End-to-End Convolutional Neural Acoustic Model](#) paper.

  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.

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This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. The NVIDIA BERT implementation is an optimized version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

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• **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

The paper describing the model can be found [here](https://nvidia.github.io/). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).  

• **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:

  • A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper
  

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).  

• **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).  

• **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).
- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub and NGC.

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**Known Issues**

- Up to 95% performance regression for TFT inference on Volta GPUs if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

- Up to 40% performance regression for BERT models on H100 GPUs may be observed if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

- UNet models running on A100 GPUs in distributed settings may have up to ~3x slowdown.
Chapter 5. PyTorch Release 24.03

The NVIDIA container image for PyTorch, release 24.03 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.10/dist-packages/torch`) in the container image.

The container also includes the following:

- **Ubuntu 22.04** including **Python 3.10**
- **NVIDIA CUDA 12.4.0.41**
- **NVIDIA cuBLAS 12.4.2.65**
- **NVIDIA cuDNN 9.0.0.306**
- **NVIDIA NCCL 2.20**
- **NVIDIA RAPIDS™ 24.02**
- **rdma-core 39.0**
- **NVIDIA HPC-X 2.18**
- **OpenMPI 4.1.4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2024.1.0.13**
- **Nsight Systems 2024.2.1.38**
- **NVIDIA TensorRT™ 8.6.3**
- **Torch-TensorRT 2.3.0a0**
- **NVIDIA DALI® 1.35**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 1.4**
- **PyTorch quantization wheel 2.1.2**
Driver Requirements

Release 24.03 is based on CUDA 12.4.0.41, which requires NVIDIA Driver release 545 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 470.57 (or later R470), 525.85 (or later R525), 535.86 (or later R535), or 545.23 (or later R545).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R450, R460, R510, and R520 drivers, which are not forward-compatible with CUDA 12.3. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 24.03 is based on 2.3.0a0+40ec155e58.

Announcements

‣ Starting with the 23.11 release, NVIDIA Optimized PyTorch containers supporting iGPU architectures are published, and able to run on Jetson devices. Please refer to the Frameworks Support Matrix for information regarding which iGPU hardware/software is supported by which container.

‣ Starting with the 23.11 release, NVIDIA PyTorch containers supporting integrated GPU embedded systems will be published.

‣ Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.

‣ Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

‣ A preview of Torch-TensorRT (1.4.0dev0) is now included.

  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

‣ Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

‣ Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.10/site-packages"` if your Conda package manager was installed in `/opt/conda`.

Starting with the 24.03 PyTorch NGC container, we have started the deprecation process on torchtext and torchdata. Both packages will be removed starting with release 24.05.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>24.03</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.4.0.41</td>
<td>2.3.0a0+40ec155e53</td>
<td>TensorRT 8.6.3</td>
</tr>
<tr>
<td>24.02</td>
<td></td>
<td>NVIDIA CUDA 12.3.2</td>
<td>2.3.0a0+ebedce2</td>
<td></td>
</tr>
<tr>
<td>24.01</td>
<td></td>
<td></td>
<td>2.2.0a0+81ea7a4</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.12</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>23.11</td>
<td></td>
<td>NVIDIA CUDA 12.3.0</td>
<td>2.2.0a0+6a974bec</td>
<td></td>
</tr>
<tr>
<td>23.10</td>
<td></td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.1.0a0+32f93b1</td>
<td></td>
</tr>
<tr>
<td>23.09</td>
<td></td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1</td>
<td></td>
</tr>
<tr>
<td>23.08</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td></td>
<td>2.1.0a0+4136153</td>
<td></td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td></td>
<td>2.0.0</td>
<td></td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td></td>
<td>1.14.0a0+44dac51</td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
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<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td></td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c TensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.11.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>----------------</td>
<td>----------------</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>1.2.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked tensorRT 5.1.5</td>
<td></td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation ([torch.cuda.amp](https://torchdoc.readthedocs.io/en/latest/amp.html)). AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMM and convolutions with...
FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found here.
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  
  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
  
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  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

**Known Issues**

- There is a known CUPTI permissions issue that is new in the 24.01 iGPU container. This issue prevents the profiler from being able to capture cuda events and manifests itself as a CUPTI Runtime Error with error code 35. This may be worked around by running the following command:

  ```
  rm -rf /usr/local/cuda/compat/lib.real
  ```

- Up to 95% performance regression for TFT inference on Volta GPUs if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

- Up to 40% performance regression for BERT models on H100 GPUs may be observed if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.
Chapter 6. PyTorch Release 24.02

The NVIDIA container image for PyTorch, release 24.02 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.10/dist-packages/torch) in the container image.

The container also includes the following:

- Ubuntu 22.04 including Python 3.10
- NVIDIA CUDA 12.3.2
- NVIDIA cuBLAS 12.3.4.1
- NVIDIA cuDNN 9.0.0.306
- NVIDIA NCCL 2.19.4
- NVIDIA RAPIDS™ 23.12
- rdma-core 39.0
- NVIDIA HPC-X 2.16rc4
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.3.1.1
- Nsight Systems 2023.4.1.97
- NVIDIA TensorRT™ 8.6.3
- Torch-TensorRT 2.2.0a0
- NVIDIA DALI® 1.34
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 1.3
- PyTorch quantization wheel 2.1.2
Driver Requirements

Release 24.02 is based on CUDA 12.3.2, which requires NVIDIA Driver release 545 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 470.57 (or later R470), 525.85 (or later R525), 535.86 (or later R535), or 545.23 (or later R545).

The CUDA driver's compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R450, R460, R510, and R520 drivers, which are not forward-compatible with CUDA 12.3. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 24.02 is based on 2.3.0a0+ebedce2.

Announcements

‣ Starting with the 23.11 release, NVIDIA Optimized PyTorch containers supporting iGPU architectures are published, and able to run on Jetson devices. Please refer to the Frameworks Support Matrix for information regarding which iGPU hardware/software is supported by which container.

‣ Starting with the 23.11 release, NVIDIA PyTorch containers supporting integrated GPU embedded systems will be published.

‣ Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.

‣ Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

‣ A preview of Torch-TensorRT (1.4.0dev0) is now included.

  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

‣ Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

‣ Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.10/site-packages"` if your Conda package manager was installed in `/opt/conda`.

Starting with the 24.02 PyTorch NGC container, we have started the deprecation process on torchtext and torchdata. Both packages will be removed starting with release 24.05.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the [Frameworks Support Matrix](#).

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>24.02</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.3.2</td>
<td>2.3.0a0+ebeced2</td>
<td>TensorRT 8.6.3</td>
</tr>
<tr>
<td>24.01</td>
<td></td>
<td></td>
<td>2.2.0a0+81ea7a4</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.12</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>23.11</td>
<td></td>
<td>NVIDIA CUDA 12.3.0</td>
<td>2.2.0a0+6a974bec</td>
<td></td>
</tr>
<tr>
<td>23.10</td>
<td></td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.1.0a0+32f93b1</td>
<td></td>
</tr>
<tr>
<td>23.09</td>
<td></td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1</td>
<td></td>
</tr>
<tr>
<td>23.08</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+4136153</td>
<td></td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td></td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td></td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td></td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td>Ubuntu</td>
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<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>------------</td>
<td>-----------------------------------</td>
<td>---------------------</td>
<td>---------------------</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td></td>
<td>TensorRT 8.0.3.4</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
</tbody>
</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation (torch.cuda.amp). AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.
APEX AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating torch.cuda.amp can be found here.
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  
  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
  
  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.
  
  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.
  
  This model script is available on GitHub and NGC.
- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.

  This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. The NVIDIA BERT implementation is an optimized version of the *Hugging Face implementation* paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](https://arxiv.org/). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:

  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).
- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model. This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.dev).

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper. This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.dev).

### Known Issues

- There is a known CUPTI permissions issue that is new in the 24.01 iGPU container. This issue prevents the profiler from being able to capture cuda events and manifests itself as a CUPTI Runtime Error with error code 35. This may be worked around by running the following command:

  ```
  rm -rf /usr/local/cuda/compat/lib.real
  ```

- Up to 95% performance regression for TFT inference on Volta GPUs if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

- Up to 40% performance regression for BERT models on H100 GPUs may be observed if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.
Chapter 7. PyTorch Release 24.01

The NVIDIA container image for PyTorch, release 24.01 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.10/dist-packages/torch) in the container image.

The container also includes the following:

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- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.3.1.1
- Nsight Systems 2023.4.1.97
- NVIDIA TensorRT™ 8.6.1.6
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Release 24.01 is based on CUDA 12.3.2, which requires NVIDIA Driver release 545 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 470.57 (or later R470), 525.85 (or later R525), 535.86 (or later R535), or 545.23 (or later R545).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R450, R460, R510, and R520 drivers, which are not forward-compatible with CUDA 12.3. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

✦ PyTorch container image version 24.01 is based on 2.2.0a0+81ea7a4.

Announcements

✦ Starting with the 23.11 release, NVIDIA Optimized PyTorch containers supporting iGPU architectures are published, and able to run on Jetson devices. Please refer to the Frameworks Support Matrix for information regarding which iGPU hardware/software is supported by which container.

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Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

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</thead>
<tbody>
<tr>
<td>24.01</td>
<td>22.04</td>
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<td>2.2.0a0+81ea7a4</td>
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</tr>
<tr>
<td>23.12</td>
<td></td>
<td>NVIDIA CUDA 12.3.0</td>
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<td></td>
</tr>
<tr>
<td>23.11</td>
<td></td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.1.0a0+32f93b1</td>
<td></td>
</tr>
<tr>
<td>23.10</td>
<td></td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1</td>
<td></td>
</tr>
<tr>
<td>23.09</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td></td>
</tr>
<tr>
<td>23.08</td>
<td></td>
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<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
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</tr>
<tr>
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<td></td>
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<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.13.0a0+936e930</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.12</td>
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<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1</td>
<td>1.13.0a0+8820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.11.0a0+180c44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+3fd9dfb</td>
<td>TensorRT 7.2.2.3 for Arm SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dfb</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
</tbody>
</table>
| 21.03             |        | NVIDIA CUDA 11.2.1 | 1.9.0a0+df837d0 | TensorRT 7.2.2.3+
| 21.02             |        | NVIDIA CUDA 11.2.0 | 1.8.0a0+52ea372 | TensorRT 7.2.2.3+
| 20.12             |        | NVIDIA CUDA 11.1.1 | 1.8.0a0+1605899 | TensorRT 7.2.2 |
| 20.11             | 18.04  | NVIDIA CUDA 11.0.0 | 1.8.0a0+17f8c32 | TensorRT 7.2.1 |
| 20.10             |        | NVIDIA CUDA 11.1.0 | 1.7.0a0+7036e91 | TensorRT 7.2.1 |
### Container Version

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td></td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>upstream commits up</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>through commit</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>9130ab38 from</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>July 31, 2019 as</td>
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<td>well as a cherry-</td>
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<td>picked</td>
<td></td>
</tr>
</tbody>
</table>

### Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation ([torch.cuda.amp](https://pytorch.org/docs/stable/amp.html)). AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but [torch.cuda.amp](https://pytorch.org/docs/stable/amp.html) is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating [torch.cuda.amp](https://pytorch.org/docs/stable/amp.html) can be found [here](https://pytorch.org/docs/stable/amp.html).
APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- ResNeXt101-32x4d model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- SE-ResNext model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  This model script is available on GitHub.

- TransformerXL model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
  This model script is available on GitHub.

- Jasper model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.
  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.
  This model script is available on GitHub and NGC.

- BERT model: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.
This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. The NVIDIA BERT implementation is an optimized version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

This model script is available on GitHub and NGC.

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](#). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed-precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on GitHub and NGC.

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:

  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper
  

  This model script is available on GitHub and NGC.

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on GitHub and NGC.

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.
- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper. This model script is available on GitHub and NGC.

**Known Issues**

- There is a known CUPTI permissions issue that is new in the 24.01 iGPU container. This issue prevents the profiler from being able to capture cuda events and manifests itself as a CUPTI Runtime Error with error code 35. This may be worked around by running the following command:
  
  ```bash
  rm -rf /usr/local/cuda/compat/lib.real
  ```

- Up to 95% performance regression for TFT inference on Volta GPUs if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

- Up to 40% performance regression for BERT models on H100 GPUs may be observed if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

The NVIDIA container image for PyTorch, release 23.12 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.10/dist-packages/torch) in the container image.

The container also includes the following:

- Ubuntu 22.04 including Python 3.10
- NVIDIA CUDA 12.3.2
- NVIDIA cuBLAS 12.3.4.1
- NVIDIA cuDNN 8.9.7.29
- NVIDIA NCCL 2.19.3
- NVIDIA RAPIDS™ 23.10
- rdma-core 39.0
- NVIDIA HPC-X 2.16
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.3.1.1
- Nsight Systems 2023.3.4.1
- NVIDIA TensorRT™ 8.6.1.6
- Torch-TensorRT 2.2.0a0
- NVIDIA DALI® 1.32
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 1.1
- PyTorch quantization wheel 2.1.2
Driver Requirements

Release 23.12 is based on CUDA 12.3.2, which requires NVIDIA Driver release 545 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 525.85 (or later R525) 535.86 (or later R535), or 545.23 (or later R545).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.3. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 23.12 is based on commit 81ea7a489a85d6f6de2c3b63206ca090927e203a s.

Announcements

‣ Starting with the 23.11 release, NVIDIA Optimized PyTorch containers supporting iGPU architectures are published, and able to run on Jetson devices. Please refer to the Frameworks Support Matrix for information regarding which iGPU hardware/software is supported by which container.

‣ Starting with the 23.11 release, NVIDIA PyTorch containers supporting integrated GPU embedded systems will be published.

‣ Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.

‣ Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

‣ A preview of Torch-TensorRT (1.4.0dev0) is now included.

  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

‣ Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

‣ Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using 
export PYTHONPATH="/opt/conda/lib/python3.10/site-packages" if your Conda package manager was installed in /opt/conda.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.12</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.3.2</td>
<td>2.2.0a0+81ea7a4</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.11</td>
<td></td>
<td>NVIDIA CUDA 12.3.0</td>
<td>2.2.0a0+6a974bec</td>
<td></td>
</tr>
<tr>
<td>23.10</td>
<td></td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.1.0a0+32f93b1</td>
<td></td>
</tr>
<tr>
<td>23.09</td>
<td></td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1</td>
<td></td>
</tr>
<tr>
<td>23.08</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+4136153</td>
<td>2.0.0</td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td></td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td></td>
<td>2.0.0</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfef5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
<td></td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0ae544c</td>
<td>TensorRT 8.0.2.2 for Arm SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dfe</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>TensorRT 8.0.1.6</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2c4b2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+16068999</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.7.0a0+7036e91</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------------</td>
<td>----------------</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation ([torch.cuda.amp](https://pytorch.org/docs/stable/amp.html)). AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but [torch.cuda.amp](https://pytorch.org/docs/stable/amp.html) is the future-proof alternative and offers *a number of advantages* over APEX AMP.

- Guidance and examples demonstrating [torch.cuda.amp](https://pytorch.org/docs/stable/amp.html) can be found [here](https://pytorch.org/docs/stable/amp.html).
APEX AMP examples can be found [here](#). For more information about AMP, see the [Training With Mixed Precision Guide](#).

**Tensor Core Examples**

The [tensor core examples provided in GitHub](#) and [NGC](#) focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest [deep learning example](#) networks and [model scripts](#) for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the [Aggregated Residual Transformations for Deep Neural Networks](#) paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on [GitHub](#).

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the [Squeeze-and-Excitation Networks](#) paper.
  
  This model script is available on [GitHub](#).

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the [codebase](#) that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
  
  This model script is available on [GitHub](#).

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the [Jasper: An End-to-End Convolutional Neural Acoustic Model](#) paper.
  
  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.
  
  This model script is available on [GitHub](#) and [NGC](#).

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.
This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. The NVIDIA BERT implementation is an optimized version of the *Hugging Face implementation* paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](https://arxiv.org/abs/1703.06870). NVIDIA's Mask R-CNN model is an optimized version of Facebook's implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).
- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](http://ngc.nvidia.com).

**Known Issues**

- Up to 95% performance regression for TFT inference on Volta GPUs if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

- Up to 40% performance regression for BERT models on H100 GPUs may be observed if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.
Chapter 9. PyTorch Release 23.11

The NVIDIA container image for PyTorch, release 23.11 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.10/dist-packages/torch) in the container image.

The container also includes the following:

- Ubuntu 22.04 including Python 3.10
- NVIDIA CUDA 12.3.0
- NVIDIA cuBLAS 12.3.2.9
- NVIDIA cuDNN 8.9.6
- NVIDIA NCCL 2.19.3
- NVIDIA RAPIDS™ 23.08
- Apex
- rdma-core 39.0
- NVIDIA HPC-X 2.15+
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.3.0.12
- Nsight Systems 2023.3.1.92
- NVIDIA TensorRT™ 8.6.1.6
- Torch-TensorRT 2.0.0.dev0
- NVIDIA DALI® 1.31.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 1.0.0+66d91d5
- PyTorch quantization wheel 2.1.2
Driver Requirements

Release 23.11 is based on CUDA 12.3.0, which requires NVIDIA Driver release 545 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 525.85 (or later R525) 535.86 (or later R535), or 545.23 (or later R545).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.3. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.11 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 23.11 is based on 2.2.0a0+6a974be torch.sparse now includes prototype support for semi-structured (2:4) sparsity on NVIDIA® GPUs.
‣ nvFuser for TorchScript is now deprecated
‣ Numpy is upgraded to 1.24.4 in lieu of deprecation

Announcements

‣ Starting with the 23.11 release, NVIDIA Optimized PyTorch containers supporting iGPU architectures are published, and able to run on Jetson devices. Please refer to the Frameworks Support Matrix for information regarding which iGPU hardware/software is supported by which container.
‣ Starting with the 23.11 release, NVIDIA PyTorch containers supporting integrated GPU embedded systems will be published.
‣ Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.
‣ Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular
Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

- A preview of Torch-TensorRT (1.4.0dev0) is now included.
  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

- Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.10/site-packages"` if your Conda package manager was installed in `/opt/conda`.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the [Frameworks Support Matrix](#).

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.11</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.3.0</td>
<td>2.0.0a0+6a974bec</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.10</td>
<td></td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.1.0a0+32f93b1</td>
<td></td>
</tr>
<tr>
<td>23.09</td>
<td></td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1</td>
<td></td>
</tr>
<tr>
<td>23.08</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td></td>
<td>2.1.0a0+4136153</td>
<td></td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td></td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.04</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td></td>
<td>1.14.0a0+44dac51</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>------------------</td>
<td>-----------------</td>
<td>--------------------</td>
<td>-----------------------</td>
<td>------------</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>N/A</td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td></td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td></td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.2.2 for Arm SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9d9cf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
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<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>---------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0</td>
<td>1.7.a0+7036e91</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.a0+6392713</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.0.194</td>
<td>1.4.a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.a0+24ae9b5</td>
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</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
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</tbody>
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**Automatic Mixed Precision (AMP)**

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A number of advantages can be found here.

APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found here.
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

### Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
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- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.
The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.

  This model is based on the [BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding](https://arxiv.org/abs/1810.04805) paper. The NVIDIA BERT implementation is an optimized version of the [Hugging Face implementation](https://huggingface.co/transformers) paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](https://arxiv.org/abs/1703.06870). NVIDIA's Mask R-CNN model is an optimized version of [Facebook's implementation](https://github.com/Caffe2/caffe2), which leverages mixed-precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:

  - A modified Tacotron 2 model from the [Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions](https://arxiv.org/abs/1802.00627) paper

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **SSD300 v1.1 model**: This model is based on the [SSD: Single Shot MultiBox Detector](https://arxiv.org/abs/1512.02325) paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao,
Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.
  
  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.
  
  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

**Known Issues**

- Up to 95% performance regression for TFT inference on Volta GPUs if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.

- Up to 40% performance regression for BERT models on H100 GPUs may be observed if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.
Chapter 10. PyTorch Release 23.10

The NVIDIA container image for PyTorch, release 23.10 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.10/dist-packages/torch`) in the container image.

The container also includes the following:

- Ubuntu 22.04 including **Python 3.10**
- NVIDIA CUDA® 12.2.2
- NVIDIA cuBLAS 12.2.5.6
- NVIDIA cuDNN 8.9.5
- NVIDIA NCCL 2.19.3
- NVIDIA RAPIDS™ 23.08
- Apex
- rdma-core 39.0
- NVIDIA HPC-X 2.15+
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.2.1.3
- Nsight Systems 2023.3.1.92
- NVIDIA TensorRT™ 8.6.1.6
- Torch-TensorRT 2.0.0.dev0
- NVIDIA DALI® 1.30.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 0.12+170797
- PyTorch quantization wheel 2.1.3
Driver Requirements

Release 23.10 is based on CUDA 12.2.2, which requires NVIDIA Driver release 535 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 525.85 (or later R525), or 535.86 (or later R535).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.2. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.10 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 23.10 is based on 2.1.0a0+32f93b1.
- Performance optimizations for Grace Hopper systems.
- **nvFUSER** is now available in /opt/pytorch
- Fixed iGPU OpenBLAS issues

Announcements

- Starting with the 23.10 release, NVIDIA PyTorch containers supporting integrated GPU embedded systems will be published.
- Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.
- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.
- A preview of Torch-TensorRT (1.4.0dev0) is now included.
Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.10/site-packages"` if your Conda package manager was installed in `/opt/conda`.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the [Frameworks Support Matrix](#).

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.10</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.2.2</td>
<td>2.1.0a0+32f93b1</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.09</td>
<td></td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1</td>
<td></td>
</tr>
<tr>
<td>23.08</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td></td>
<td>2.1.0a0+4136153</td>
<td></td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td></td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td></td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td></td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td></td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td>1.13.0a0+321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+321be6</td>
<td></td>
</tr>
</tbody>
</table>

PyTorch
<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+17f8c32</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2eb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+1605899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>21.03</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>------------------</td>
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<td>-----------------------</td>
<td>-----------------------</td>
<td>----------------</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>upstream commits up</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>through commit</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>9130ab38 from July 31,</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>2019 as well as a</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>cherry-picked</td>
<td></td>
</tr>
</tbody>
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**Automatic Mixed Precision (AMP)**

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  - The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
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  The paper describing the model can be found [here](https://arxiv.org). NVIDIA's Mask R-CNN model is an optimized version of Facebook's implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

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  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

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  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

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  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

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- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

**Known Issues**

- Up to 95% performance regression for TFT inference on Volta GPUs if TorchScript with nvFuser is enabled. Disable TorchScript or use an alternative backend to reduce the regression.
Chapter 11. PyTorch Release 23.09

The NVIDIA container image for PyTorch, release 23.09 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.10/dist-packages/torch`) in the container image.

The container also includes the following:

- **Ubuntu 22.04** including **Python 3.10**
- **NVIDIA CUDA® 12.2.1**
- **NVIDIA cuBLAS 12.2.5.6**
- **NVIDIA cuDNN 8.9.5**
- **NVIDIA NCCL 2.18.5**
- **NVIDIA RAPIDS™ 23.08**
- **Apex**
- **rdma-core 39.0**
- **NVIDIA HPC-X 2.16**
- **OpenMPI 4.1.4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2023.2.1.3**
- **Nsight Systems 2023.3.1.92**
- **NVIDIA TensorRT™ 8.6.1.6**
- **Torch-TensorRT 2.0.0.dev0**
- **NVIDIA DALI® 1.29.0**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 0.12**
- **PyTorch quantization wheel 2.1.2**
Driver Requirements

Release 23.09 is based on CUDA 12.2.1, which requires NVIDIA Driver release 535 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 525.85 (or later R525), or 535.86 (or later R535). The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.2. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.09 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 23.09 is based on 2.1.0a0+32f93b1.

Announcements

- Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.
- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.
- A preview of Torch-TensorRT (1.4.0dev0) is now included.
  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.8/site-packages"` if your Conda package manager was installed in `/opt/conda`.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the [Frameworks Support Matrix](#).

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.09</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+32f93b1</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.08</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+29c30b1</td>
<td></td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td></td>
<td>2.1.0a0+b5021ba</td>
<td></td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td></td>
<td>2.1.0a0+4136153</td>
<td></td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td></td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td></td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td></td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td></td>
<td>1.12.0a0+8a1a93a</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td></td>
<td>1.12.0a0+8a1a93a</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td></td>
<td>1.12.0a0+8a1a93a</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td></td>
<td>1.12.0a0+8a1a93a</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043TensorRT 8.2.18</td>
<td></td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.2.2 for x64 Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9d0f</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+3fd9d0f</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
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<td>------------------</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found [here](#).
- APEX AMP examples can be found [here](#).

For more information about AMP, see the [Training With Mixed Precision Guide](#).
Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding. The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores. This model script is available on GitHub.

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This model script is available on GitHub and NGC.

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  - A modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper
  

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  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.

- **GNMT v2 model**: This model is similar to the model that is discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
This model script is available on GitHub and NGC.

**Known Issues**

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.
  - Up to 20% performance regression for inference use cases on Volta GPUs.
Chapter 12. PyTorch Release 23.08

The NVIDIA container image for PyTorch, release 23.08 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.10/dist-packages/torch) in the container image.

The container also includes the following:

- **Ubuntu 22.04** including **Python 3.10**
- **NVIDIA CUDA® 12.2.1**
- **NVIDIA cuBLAS 12.2.5.1**
- **NVIDIA cuDNN 8.9.4**
- **NVIDIA NCCL 2.18.3**
- **NVIDIA RAPIDS™ 23.06**
- **Apex**
- **rdma-core 39.0**
- **NVIDIA HPC-X 2.15**
- **OpenMPI 4.1.4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2023.2.1.3**
- **Nsight Systems 2023.2.3.1001**
- **NVIDIA TensorRT™ 8.6.1.6**
- **Torch-TensorRT 2.0.0.dev0**
- **NVIDIA DALI® 1.28.0**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 0.11.0++3f01b4f**
- **PyTorch quantization wheel 2.1.2**
Driver Requirements

Release 23.08 is based on CUDA 12.2.1, which requires NVIDIA Driver release 535 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 525.85 (or later R525), or 535.86 (or later R535).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.2. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.08 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 23.08 is based on 2.1.0a0+29c30b1.

Announcements

- Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.

- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

- A preview of Torch-TensorRT (1.4.0dev0) is now included.

  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH=":/opt/conda/lib/python3.8/site-packages"` if your Conda package manager was installed in `/opt/conda`.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the [Frameworks Support Matrix](#).

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</tr>
</thead>
<tbody>
<tr>
<td>23.08</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.2.1</td>
<td>2.1.0a0+29c30b1</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.07</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+4136153</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>---------</td>
<td>--------------</td>
<td>------------</td>
<td>-------------------</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043 TensorRT 8.2.1.8</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c TensorRT 8.0.3.4 for x64 Linux TensorRT 8.0.2.2 for Arm SBSA Linux</td>
<td></td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+3fd9dce TensorRT 8.0.3</td>
<td></td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+c3d40fd TensorRT 7.2.3.4</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+df837d0 TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0 TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372 TensorRT 7.2.2.3+cuda11.1.0.024</td>
<td></td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+7036e91 TensorRT 7.2.1</td>
<td></td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32 TensorRT 7.1.3</td>
<td></td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.8.0a0+7036e91 TensorRT 7.1.3</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.8.0a0+9907a3e TensorRT 7.1.2</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe TensorRT 7.1.3</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>TensorRT 7.1.2</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>TensorRT 7.1.2</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>TensorRT 7.1.2</td>
<td></td>
</tr>
</tbody>
</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating torch.cuda.amp can be found here.
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.
Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  
  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
  
  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.
  
  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.
  
  This model script is available on GitHub and NGC.

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.
  
  This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. The NVIDIA BERT implementation is an optimized
version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

This model script is available on [GitHub](https://github.com) and [NGC](https://github.com).

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](https://github.com). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://github.com).

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper

  This model script is available on [GitHub](https://github.com) and [NGC](https://github.com).

- **SSD300 v1.1 model**: This model is based on the SSD: Single Shot MultiBox Detector paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://github.com).

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://github.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://github.com).

- **GNMT v2 model**: This model is similar to the model that is discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
This model script is available on GitHub and NGC.

Known Issues

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.
  - Up to 20% performance regression for inference use cases on Volta GPUs.
Chapter 13. PyTorch Release 23.07

The NVIDIA container image for PyTorch, release 23.07 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.10/dist-packages/torch`) in the container image.

The container also includes the following:
- Ubuntu 22.04 including Python 3.10
- NVIDIA CUDA® 12.1.1
- NVIDIA cuBLAS 12.1.3.1
- NVIDIA cuDNN 8.9.3
- NVIDIA NCCL 2.18.3
- NVIDIA RAPIDS™ 23.06
- Apex
- rdma-core 39.0
- NVIDIA HPC-X 2.15
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.1.4
- Nsight Systems 2023.2.3.1001
- NVIDIA TensorRT™ 8.6.1.6
- Torch-TensorRT 1.5.0.dev0
- NVIDIA DALI® 1.27.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 0.10.0+96ed6fc
- PyTorch quantization wheel 2.1.2
Driver Requirements

Release 23.07 is based on **CUDA 12.1.1**, which requires **NVIDIA Driver** release 530 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), 515.65 (or later R515), 525.85 (or later R525), or 530.30 (or later R530).

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GPU Requirements

Release 23.07 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see [CUDA GPUs](https://docs.nvidia.com/cuda/nvtx/#nvidia). For additional support details, see [Deep Learning Frameworks Support Matrix](https://docs.nvidia.com/cuda/index.html#nvidia).

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 23.07 is based on 2.1.0a0+b5021ba.

Announcements

- Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.
- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.
- A preview of Torch-TensorRT (1.4.0dev0) is now included.
  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
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</tr>
</thead>
<tbody>
<tr>
<td>23.07</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+b5021ba</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td>23.06</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+4136153</td>
<td></td>
</tr>
<tr>
<td>23.05</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.1</td>
<td>1.14.0a0+44dac51</td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+fe05266f</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+44dac51</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+bfe5ad28TensorRT 8.2.2</td>
<td>1.11.0a0+bfe5ad28TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.11.0a0+b6df043TensorRT 8.2.1.8</td>
<td>TensorRT 8.2.1.8 for x64 Linux</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.11.0a0+0aef44cTensorRT 8.0.3</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+3fd9dcfTensorRT 8.0.1.6</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td>1.10.0a0+ecc3718</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fdTensorRT 7.2.3.4</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td>1.9.0a0+2ecb2c7</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0TensorRT 7.2.2.3</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372TensorRT 7.2.2.3+cuda11.1.0.024</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899TensorRT 7.2.2</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32TensorRT 7.2.1</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91TensorRT 7.1.3</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3eTensorRT 7.1.2</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84dedTensorRT 7.0.0</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
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<td>TensorRT 7.0.0</td>
</tr>
</tbody>
</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating torch.cuda.amp can be found here.
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores.
This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the *Aggregated Residual Transformations for Deep Neural Networks* paper.

  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on [GitHub](https://github.com).

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the *Squeeze-and-Excitation Networks* paper.

  This model script is available on [GitHub](https://github.com).

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.

  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments.

  Our implementation is based on the [codebase](https://github.com) that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

  This model script is available on [GitHub](https://github.com).

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the *Jasper: An End-to-End Convolutional Neural Acoustic Model* paper.

  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.

  This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. The NVIDIA BERT implementation is an optimized version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.
The paper describing the model can be found [here](#). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

This model script is available on GitHub and NGC.

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper.

This model script is available on GitHub and NGC.

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on GitHub and NGC.

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

  This model script is available on GitHub and NGC.

**Known Issues**

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
- CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
- CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.
Chapter 14. PyTorch Release 23.06

The NVIDIA container image for PyTorch, release 23.06 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.10/dist-packages/torch`) in the container image.

The container also includes the following:

- **Ubuntu 22.04** including **Python 3.10**
- **NVIDIA CUDA® 12.1.1**
- **NVIDIA cuBLAS 12.1.3.1**
- **NVIDIA cuDNN 8.9.2**
- **NVIDIA NCCL 2.18.1**
- **NVIDIA RAPIDS™ 23.04**
- **Apex**
- **rdma-core 39.0**
- **NVIDIA HPC-X 2.15**
- **OpenMPI 4.1.4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2023.1.4**
- **Nsight Systems 2023.2.3.1001**
- **NVIDIA TensorRT™ 8.6.1.6**
- **Torch-TensorRT 1.5.0.dev0**
- **NVIDIA DALI® 1.26.0**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 0.9.0**
- **PyTorch quantization wheel 2.1.2**
Driver Requirements

Release 23.06 is based on CUDA 12.1.1, which requires NVIDIA Driver release 530 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), 515.65 (or later R515), 525.85 (or later R525), or 530.30 (or later R530).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.1. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.06 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 23.06 is based on 2.1.0a0+4136153.
- 23.06 provides the experimental UCC process group for the distributed backend. Users can experiment with it by creating UCC as the default process group via:
  ```python
  torch.distributed.init_process_group(backend="ucc", kwargs) or a side process group with any default via:
  
  torch.distributed.init_process_group(backend=any_backend,
  default_pg_kwargs)
  
  ucc_pg = torch.distributed.new_group(backend="ucc", ucc_pg_kwargs)
  ```

Announcements

- Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers are no longer tested on Pascal GPU architectures.
- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.
A preview of Torch-TensorRT (1.4.0dev0) is now included.

Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using export PYTHONPATH="/opt/conda/lib/python3.8/site-packages" if your Conda package manager was installed in /opt/conda.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.06</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.1.0a0+4136153</td>
<td>TensorRT 8.6.1.6</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.05</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>23.03</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.0.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.09</td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------------</td>
<td>--------------------</td>
<td>-------------------</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td></td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.0.2.2 for Arm SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td></td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td></td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
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<td>----------</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
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</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.08</td>
<td></td>
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- **Tacotron 2 and WaveGlow v1.1 model:** This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper

  This model script is available on GitHub and NGC.

- **SSD300 v1.1 model:** This model is based on the SSD: Single Shot MultiBox Detector paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on GitHub and NGC.

- **Neural Collaborative Filtering (NCF) model:** This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model:** This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.

- **GNMT v2 model:** This model is similar to the model that is discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
This model script is available on GitHub and NGC.

Known Issues

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.

- Performance regression up to 28% for FastPitch on NVIDIA Ampere architecture and Hopper GPUs.

- A full iteration CUDA graph capture including gradient AllReduce, Optimizer, and Parameter AllGather operations could fail with a CUDA error. We recommend reducing the scope of the CUDA graph capture as a workaround.
Chapter 15. PyTorch Release 23.05

The NVIDIA container image for PyTorch, release 23.05 is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.10/dist-packages/torch) in the container image.

The container also includes the following:

- Ubuntu 22.04 including Python 3.10
- NVIDIA CUDA® 12.1.1
- NVIDIA cuBLAS 12.1.3.1
- NVIDIA cuDNN 8.9.1.23
- NVIDIA NCCL 2.18.1
- NVIDIA RAPIDS™ 23.04
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.14
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.1.1.4
- Nsight Systems 2023.2.1.89
- NVIDIA TensorRT™ 8.6.1.2
- Torch-TensorRT 1.4.0.dev0
- NVIDIA DALI® 1.25.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 0.8
- PyTorch quantization wheel 2.1.2
Driver Requirements

Release 23.05 is based on **CUDA 12.1.1**, which requires **NVIDIA Driver** release 530 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), 515.65 (or later R515), 525.85 (or later R525), or 530.30 (or later R530).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.1. For a complete list of supported drivers, see the [CUDA Application Compatibility](#) topic. For more information, see [CUDA Compatibility and Upgrades](#).

GPU Requirements

Release 23.05 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see [CUDA GPUs](#). For additional support details, see [Deep Learning Frameworks Support Matrix](#).

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 23.05 is based on 2.0.0 with a cherry-pick to fix potential performance regressions via #97838.
- The 23.05 container updates to Ubuntu 22.04 and Python to 3.10.6.

Announcements

- Starting with the 23.06 release, the NVIDIA Optimized Deep Learning Framework containers will no longer be tested on Pascal GPU architectures.
- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.
- NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.
  
  Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.
- A preview of Torch-TensorRT (1.4.0dev0) is now included.
Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container.

To profile models in PyTorch, use DLProf:

DLProf can help data scientists, engineers, and researchers understand improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in `/workspace/nvidia-examples`).

You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

- Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.8/site-packages"` if your Conda package manager was installed in `/opt/conda`.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.05</td>
<td>22.04</td>
<td>NVIDIA CUDA 12.1.1</td>
<td>2.0.0</td>
<td>TensorRT 8.6.1.2</td>
</tr>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td></td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>-----------------</td>
<td>--------------------</td>
<td>------------------------</td>
<td>----------------------</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td></td>
<td>TensorRT 8.0.3.4</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0ae44c</td>
<td>TensorRT 8.0.2.2 for x64 Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9d9cf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
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<td>Ubuntu</td>
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<td>-------------------</td>
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<td>-----------------------</td>
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<td>---------------------------</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.7.0a0+7036e91</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
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<td>19.11</td>
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</tr>
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</tr>
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  This model script is available on GitHub and NGC.

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  - A modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper

  This model script is available on GitHub and NGC.

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  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on GitHub and NGC.

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  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao,
Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngd.nvidia.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.
  
  This model script is available on [GitHub](https://github.com) and [NGC](https://ngd.nvidia.com).

- **GNMT v2 model**: This model is similar to the model that is discussed in the [Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation](https://arxiv.org) paper.
  
  This model script is available on [GitHub](https://github.com) and [NGC](https://ngd.nvidia.com).

**Known Issues**

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.

- The following CVEs were detected in OpenCV: CVE-2023-2618, CVE-2023-2617, which will be addressed in future releases.

- Performance regression up to 35% for FastPitch on Volta GPUs.
Chapter 16. PyTorch Release 23.04

The NVIDIA container image for PyTorch, release 23.04, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.8/dist-packages/torch`) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8**
- **NVIDIA CUDA® 12.1.0**
- **NVIDIA cuBLAS 12.1.3**
- **NVIDIA cuDNN 8.9.0**
- **NVIDIA NCCL 2.17.1**
- **NVIDIA RAPIDS™ 23.02**
- **Apex**
- **rdma-core 36.0**
- **NVIDIA HPC-X 2.13**
- **OpenMPI 4.1.4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2023.1.0.15**
- **Nsight Systems 2023.1.1.127**
- **NVIDIA TensorRT™ 8.6.1**
- **Torch-TensorRT 1.4.0.dev0**
- **NVIDIA DALI® 1.23.0**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 0.7**
- **PyTorch quantization wheel 2.1.2**
Driver Requirements

Release 23.04 is based on CUDA 12.1.0, which requires NVIDIA Driver release 530 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), 515.65 (or later R515), 525.85 (or later R525), or 530.30 (or later R530).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.1. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.04 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 23.04 is based on 2.1.0a0+fe05266f.
- Configuration of NCCL communicators via: https://github.com/pytorch/pytorch/pull/97394

Announcements

- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

- NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.

- A preview of Torch-TensorRT (1.4.0dev0) is now included.

Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container.

To profile models in PyTorch, use DLProf:

DLProf can help data scientists, engineers, and researchers understand improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DLProf is available on NGC or through a Python PIP wheel installation.

The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples).

You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.8/site-packages"` if your Conda package manager was installed in /opt/conda.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.1.0a0+fe05266f</td>
<td>TensorRT 8.6.1</td>
</tr>
<tr>
<td>23.03</td>
<td></td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28 TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043 TensorRT 8.2.1.8</td>
<td></td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043 TensorRT 8.2.1.8</td>
<td></td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c for x64 Linux TensorRT 8.0.3.4</td>
<td></td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c for Arm SBSA Linux TensorRT 8.0.3.4</td>
<td></td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dce TensorRT 8.0.3</td>
<td></td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+3fd9dce TensorRT 8.0.1.6</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718 TensorRT 7.2.3.4</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd TensorRT 7.2.3.4</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7 TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7 TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0 TensorRT 7.2.2.3</td>
<td></td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372 TensorRT 7.2.2.3+cuda11.1.0.024</td>
<td></td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899 TensorRT 7.2.2</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>TensorRT 7.1.2</td>
<td></td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>TensorRT 5.1.5</td>
<td></td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation and a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMM operations and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.
APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found here.
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  
  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and take advantage of Tensor Cores.
  
  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.
  
  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.
  
  This model script is available on GitHub and NGC.
‣ **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.

This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. The NVIDIA BERT implementation is an optimized version of the *Hugging Face implementation* paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

This model script is available on GitHub and NGC.

‣ **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

The paper describing the model can be found here. NVIDIA's Mask R-CNN model is an optimized version of *Facebook's implementation*, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

This model script is available on GitHub and NGC.

‣ **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:

  ▶ A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper
  

This model script is available on GitHub and NGC.

‣ **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

This model script is available on GitHub and NGC.

‣ **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

This model script is available on GitHub and NGC.
- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.
  This model script is available on [GitHub](https://github.com) and [NGC](https://nvidia.github.io/)

- **GNMT v2 model**: This model is similar to the model that is discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
  This model script is available on [GitHub](https://github.com) and [NGC](https://nvidia.github.io/)

### Known Issues
- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.
Chapter 17. PyTorch Release 23.03

The NVIDIA container image for PyTorch, release 23.03, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.8/dist-packages/torch) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8
- NVIDIA CUDA® 12.1.0
- NVIDIA cuBLAS from CUDA 12.1.0
- NVIDIA cuDNN 8.8.1.3
- NVIDIA NCCL 2.17.1
- NVIDIA RAPIDS™ 23.02
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.13
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2023.1.0.15
- Nsight Systems 2023.1.1.127
- NVIDIA TensorRT™ 8.5.3
- Torch-TensorRT 1.4.0dev0
- NVIDIA DALI® 1.23.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 0.6.0
- PyTorch quantization wheel 2.1.2
Driver Requirements

Release 23.03 is based on **CUDA 12.1.0**, which requires **NVIDIA Driver** release 530 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), 515.65 (or later R515), 525.85 (or later R525), or 530.30 (or later R530).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.1. For a complete list of supported drivers, see the **CUDA Application Compatibility** topic. For more information, see **CUDA Compatibility and Upgrades**.

GPU Requirements

Release 23.03 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see **CUDA GPUs**. For additional support details, see **Deep Learning Frameworks Support Matrix**.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 23.03 is based on **2.0.0a0+1767026**.

Announcements

- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

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NVIDIA PyTorch Container Versions

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<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.03</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.1.0</td>
<td>2.0.0a0+1767026</td>
<td>TensorRT 8.5.3</td>
</tr>
<tr>
<td>23.02</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.2</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936ee930</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+8a1a93a</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.2.2 for Arm SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+eccc718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.7.0a0+7036e91</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.0.3</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
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</tr>
</tbody>
</table>

### Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation and a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found [here](#).
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

**Tensor Core Examples**

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.

  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.

  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.

  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.

  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.

  This model script is available on GitHub and NGC.

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.
This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. The NVIDIA BERT implementation is an optimized version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](https://arxiv.org/). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).
- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.dev).

### Known Issues

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.
  - Vgg16 training use cases might see a large performance regression on V100 when the workload is using close to all available device memory due to an unexpected memory thrashing when `torch.backends.cudnn.benchmark = True` is used. The performance can be restored by disabling `cudnn.benchmark` or by reducing the memory usage.
Chapter 18. PyTorch Release 23.02

The NVIDIA container image for PyTorch, release 23.02, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.8/dist-packages/torch`) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8
- NVIDIA CUDA® 12.0.1
- NVIDIA cuBLAS from CUDA 12.0.1
- NVIDIA cuDNN 8.7.0.84
- NVIDIA NCCL 2.16.5 (optimized for NVIDIA NVLink®)
- NVIDIA RAPIDS™ 22.12.0
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.13
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2022.4.1.6
- Nsight Systems 2022.5.1
- NVIDIA TensorRT™ 8.5.3
- Torch-TensorRT 1.4.0dev0
- NVIDIA DALI® 1.22.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 0.5
Driver Requirements

Release 23.02 is based on CUDA 12.0.1, which requires NVIDIA Driver release 525 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), 515.65 (or later R515), or 525.85 (or later R525).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, R460, and R520 drivers, which are not forward-compatible with CUDA 12.0. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.02 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 23.02 is based on 1.14.0a0+44dac51.

Announcements

‣ Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

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Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

‣ Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
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<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.02</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51 TensorRT 8.5.3</td>
<td></td>
</tr>
<tr>
<td>23.01</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96 TensorRT 8.5.1</td>
<td></td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td>1.13.0a0+936e930 TensorRT 8.5.0.12</td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6 TensorRT 8.4.2.4</td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td></td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.2.2 for Arm SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcb</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td></td>
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<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
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</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1605899</td>
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<td>20.11</td>
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- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on GitHub and NGC.

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  This model script is available on GitHub and NGC.
GNMT v2 model: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

This model script is available on GitHub and NGC.

Known Issues

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.
  - CVE-2021-29063 - Mpmath v1.0.0 through v1.2.1 exposes a Regular Expression Denial of Service (ReDOS) vulnerability.

- Known security vulnerabilities:
Chapter 19. PyTorch Release 23.01

The NVIDIA container image for PyTorch, release 23.01, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the default Python environment (`/usr/local/lib/python3.8/dist-packages/torch`) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8**
- **NVIDIA CUDA® 12.0.1**
- **NVIDIA cuBLAS from CUDA 12.0.1**
- **NVIDIA cuDNN 8.7.0.84**
- **NVIDIA NCCL 2.16.5** (optimized for **NVIDIA NVLink®**)
- **NVIDIA RAPIDS™ 22.12.0** (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- **Apex**
- **rdma-core 36.0**
- **NVIDIA HPC-X 2.13**
- **OpenMPI 4.1.4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2022.4.1.6**
- **Nsight Systems 2022.5.1**
- **NVIDIA TensorRT™ 8.5.2.2**
- **Torch-TensorRT 1.4.0dev0**
- **NVIDIA DALI® 1.21.0**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 0.4**
Driver Requirements

Release 23.01 is based on CUDA 12.0.1, which requires NVIDIA Driver release 525 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), 515.65 (or later R515), or 525.85 (or later R525).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 12.0. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 23.01 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 23.01 is based on 1.14.0a0+44dac51.

Announcements

- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

- NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

  Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.

- A preview of Torch-TensorRT (1.1.0a0) is now included.

  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, use DLProf.

DLProf can help data scientists, engineers, and researchers understand improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples).

You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using `export PYTHONPATH="/opt/conda/lib/python3.8/site-packages"` if your Conda package manager was installed in /opt/conda.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>23.01</td>
<td>20.04</td>
<td>NVIDIA CUDA 12.0.1</td>
<td>1.14.0a0+44dac51</td>
<td>TensorRT 8.5.2.2</td>
</tr>
<tr>
<td>22.12</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
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</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
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</tr>
<tr>
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<td></td>
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<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
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</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
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<tr>
<td>22.03</td>
<td></td>
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<td>TensorRT 8.2.3</td>
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<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
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<td>22.01</td>
<td></td>
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<td></td>
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  This model script is available on GitHub and NGC.

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.
- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.
  
  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.labs.nvidia.com).

**Known Issues**

- Certain cuDNN cases that use runtime compilation via NVRTC, particularly on ARM SBSA systems, can fail with `CUDNN_STATUS_RUNTIME_PREREQUISITE_MISSING`. A workaround for this situation is to export `LD_LIBRARY_PATH=$LD_LIBRARY_PATH:/usr/local/cuda-11/lib64`. This will be fixed in an upcoming release.

- GNMTv2 performance regression of up to 50% for inference use cases and up to 20% for training.

- The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  
  - CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
  - CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via `SAMPLESPERPIXEL`.
  - CVE-2021-29063 - Mpmath v1.0.0 through v1.2.1 exposes a Regular Expression Denial of Service (ReDOS) vulnerability.

- Known security vulnerabilities:
  
  - CVE-2022-25882 for ONNX<1.13.0
Chapter 20. PyTorch Release 22.12

The NVIDIA container image for PyTorch, release 22.12, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the default Python environment (/usr/local/lib/python3.8/dist-packages/torch) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8
- NVIDIA CUDA® 11.8.0
- NVIDIA cuBLAS 11.11.3.6
- NVIDIA cuDNN 8.7.0.84
- NVIDIA NCCL 2.15.5 (optimized for NVIDIA NVLink®)
- NVIDIA RAPIDS™ 22.10.01 (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.13
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorFlow 2.9.0
- Nsight Compute 2022.3.0.0
- Nsight Systems 2022.4.2.1
- NVIDIA TensorRT™ 8.5.1
- Torch-TensorRT 1.1.0a0
- NVIDIA DALI® 1.20.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 0.3.0
Driver Requirements

Release 22.12 is based on CUDA 11.8.0, which requires NVIDIA Driver release 520 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 515.65 (or later R515).

The CUDA driver's compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 11.8. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 22.12 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 22.12 is based on 1.14.0a0+410ce96.

Announcements

‣ Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

‣ NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.

‣ A preview of Torch-TensorRT (1.1.0a0) is now included.

Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

‣ Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, use DLProf.

DLProf can help data scientists, engineers, and researchers understand improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

Starting with the 22.11 PyTorch NGC container, miniforge is removed and all Python packages are installed in the default Python environment. In case you depend on Conda-specific packages, which might not be available on PyPI, we recommend building these packages from source. A workaround is to manually install a Conda package manager, and add the conda path to your PYTHONPATH for example, using export PYTHONPATH="/opt/conda/lib/python3.8/site-packages" if your Conda package manager was installed in /opt/conda.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

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<thead>
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<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.12</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.14.0a0+410ce96 TensorRT 8.5.1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>1.13.0a0+936e930</td>
<td></td>
</tr>
<tr>
<td>22.11</td>
<td></td>
<td></td>
<td>1.13.0a0+d0d6b1f TensorRT 8.5.0.12</td>
<td></td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td></td>
<td>1.13.0a0+d321be6 TensorRT 8.4.2.4</td>
<td></td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+08820cb TensorRT 8.4.1</td>
<td></td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td></td>
<td>1.13.0a0+340c412 TensorRT 8.2.5</td>
<td></td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>---------</td>
<td>-----------------------------------</td>
<td>------------------</td>
<td>---------------------------------</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0ae44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td></td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td></td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td></td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>------------------</td>
<td>---------------</td>
<td>---------------------------</td>
<td>-----------------------------</td>
<td>------------------</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+3bbb36e</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation and a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found [here](#).
- APEX AMP examples can be found [here](#).

For more information about AMP, see the [Training With Mixed Precision Guide](#).
Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  
  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
  
  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.
  
  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.
  
  This model script is available on GitHub and NGC.

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.
  
  This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. The NVIDIA BERT implementation is an optimized
version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

This model script is available on GitHub and NGC.

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found here. NVIDIA's Mask R-CNN model is an optimized version of Facebook's implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on GitHub and NGC.

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper

  This model script is available on GitHub and NGC.

- **SSD300 v1.1 model**: This model is based on the SSD: Single Shot MultiBox Detector paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

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This model script is available on GitHub and NGC.

Known Issues

‣ GNMTv2 inference performance regression of up to 50% due to an MKL slowdown. Other CPU-sensitive workloads could also be affected.

‣ The following CVEs might be flagged but were patched by backporting the fixes into the corresponding libraries in our release:
  ‣ CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
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- NVIDIA HPC-X 2.12.2tp1
- OpenMPI 4.1.4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2022.3.0.0
- Nsight Systems 2022.4.2.1
- NVIDIA TensorRT™ 8.5.1
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The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 11.8. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

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</tr>
</thead>
<tbody>
<tr>
<td>22.11</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+936e930</td>
<td>TensorRT 8.5.1</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.10</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>------------------------------</td>
<td>--------------------</td>
<td>-------------------</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td></td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.0.3.4</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.2.2 for x64 Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td></td>
<td>1.7.0a0+8debf4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td></td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>-----------</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bbb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td></td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td></td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td></td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
</tbody>
</table>
Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  
  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
  
  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.
  
  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.
  
  This model script is available on GitHub and NGC.

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.
  
  This model is based on the BERT Pre-training of Deep Bidirectional Transformers for Language Understanding paper. The NVIDIA BERT implementation is an optimized
version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

This model script is available on GitHub and NGC.

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](#). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on GitHub and NGC.

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper

  This model script is available on GitHub and NGC.

- **SSD300 v1.1 model**: This model is based on the SSD: Single Shot MultiBox Detector paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on GitHub and NGC.

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.

- **GNMT v2 model**: This model is similar to the model that is discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

**Known Issues**

- In rare cases, cuDNN autotuning could cause a long startup time or a hang. In these cases, disable autotuning using `torch.backends.cudnn.benchmark = False`.
- GNMTv2 inference performance regression of up to 50% due to an MKL slowdown. Other CPU-sensitive workloads could also be affected.
- BERT pretraining performance regression of up to 17% for workloads using dynamic input shapes.
- Tacotron2 inference performance regression of up to 15% for workloads using dynamic input shapes.

**Security CVEs**

- CVE-2022-45198 - Pillow before 9.2.0 performs Improper Handling of Highly Compressed GIF Data (Data Amplification).
- CVE-2022-45199 - Pillow before 9.3.0 allows denial of service via SAMPLESPERPIXEL.
Chapter 22. PyTorch Release 22.10

The NVIDIA container image for PyTorch, release 22.10, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8**
- **NVIDIA CUDA® 11.8.0**
- **NVIDIA cuBLAS 11.11.3.6**
- **NVIDIA cuDNN 8.6.0.163**
- **NVIDIA NCCL 2.15.5** (optimized for **NVIDIA NVLink®**)
- **NVIDIA RAPIDS™ 22.08.01** (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- **Apex**
- **rdma-core 36.0**
- **NVIDIA HPC-X 2.12.2tp1**
- **OpenMPI 4.1.5a1**
- **GDRCopy 2.3**
- **TensorBoard 2.10.0**
- **Nsight Compute 2022.3.0.22**
- **Nsight Systems 2022.4.2.1**
- **NVIDIA TensorRT™ 8.5.0.12**
- **Torch-TensorRT 1.1.0a0**
- **NVIDIA DALI® 1.18.0**
- **MAGMA 2.6.2**
- **JupyterLab 2.3.2** including **Jupyter-TensorBoard**
- **TransformerEngine 0.1.0**
Driver Requirements

Release 22.10 is based on **CUDA 11.8.0**, which requires **NVIDIA Driver** release 520 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 515.65 (or later R515).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 11.8. For a complete list of supported drivers, see the [CUDA Application Compatibility topic](#). For more information, see [CUDA Compatibility and Upgrades](#).

GPU Requirements

Release 22.10 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see [CUDA GPUs](#). For additional support details, see [Deep Learning Frameworks Support Matrix](#).

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch container image version 22.10** is based on 1.13.0a0+d0d6b1f.

Announcements

- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.

- **NVIDIA Deep Learning Profiler (DLProf) v1.8**, which was included in the 21.12 container, was the final release of DLProf.

- Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.

- A preview of Torch-TensorRT (1.1.0a0) is now included.

  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, use DLProf:

DLProf can help data scientists, engineers, and researchers understand improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the [Frameworks Support Matrix](#).

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.10</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.09</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043 TensorRT 8.2.1.8</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
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<tr>
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</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.0.2</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bbb36e</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
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<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>-----------------</td>
<td>----------------</td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked TensorRT 5.1.5</td>
<td></td>
</tr>
</tbody>
</table>

### Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation and a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a **number of advantages** over APEX AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found [here](#).
- APEX AMP examples can be found [here](#).

For more information about AMP, see the [Training With Mixed Precision Guide](#).

### Tensor Core Examples

The [tensor core examples provided in GitHub](#) and [NGC](#) focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model: This model was introduced in the [Aggregated Residual Transformations for Deep Neural Networks](#) paper.
It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the *Squeeze-and-Excitation Networks* paper.
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  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.
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- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the *Jasper: An End-to-End Convolutional Neural Acoustic Model* paper.
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  The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.
  This model script is available on GitHub and NGC.
- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

### Known Issues
- None.
Chapter 23. PyTorch Release 22.09

The NVIDIA container image for PyTorch, release 22.09, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8
- NVIDIA CUDA® 11.8.0
- NVIDIA cuBLAS 11.11.3.6
- NVIDIA cuDNN 8.6.0.163
- NVIDIA NCCL 2.15.1 (optimized for NVIDIA NVLink®)
- NVIDIA RAPIDS™ 22.08.01 (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.12.1a0
- OpenMPI 4.1.2rc4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2022.3.0.22
- Nsight Systems 2022.3.1.43
- NVIDIA TensorRT™ 8.5.0.12
- Torch-TensorRT 1.1.0a0
- NVIDIA DALI® 1.17.0
- MAGMA 2.6.2
- JupyterLab 2.3.2 including Jupyter-TensorBoard
- TransformerEngine 0.1.0
Driver Requirements

Release 22.09 is based on **CUDA 11.8.0**, which requires **NVIDIA Driver** release 520 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), 510.47 (or later R510), or 515.65 (or later R515).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 11.8. For a complete list of supported drivers, see the **CUDA Application Compatibility** topic. For more information, see **CUDA Compatibility and Upgrades**.

GPU Requirements

Release 22.09 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, NVIDIA Ampere architecture, and NVIDIA Hopper™ architecture families. For a list of GPUs to which this compute capability corresponds, see **CUDA GPUs**. For additional support details, see **Deep Learning Frameworks Support Matrix**.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 22.09 is based on **1.13.0a0+d0d6b1f**.
- CUDA lazy module loading is on by default. To disable it, use `unset CUDA_MODULE_LOADING` or set it to `EAGER`.
- The experimental cuDNN v8 API is enabled by default. To disable it, use `unset TORCH_CUDNN_V8_API_ENABLED` or set it to `0`.
- TransformerEngine v0.1.0 to support FP8 on Hopper.

Announcements

- Transformer Engine is a library for accelerating Transformer models on NVIDIA GPUs. It includes support for 8-bit floating point (FP8) precision on Hopper GPUs which provides better training and inference performance with lower memory utilization. Transformer Engine also includes a collection of highly optimized modules for popular Transformer architectures and an automatic mixed precision-like API that can be used seamlessly with your PyTorch code.
- NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.
  
  Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the `nvidia-pyindex`.
- A preview of Torch-TensorRT (1.1.0a0) is now included.
Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container.

To profile models in PyTorch, use DLProf:

DLProf can help data scientists, engineers, and researchers understand improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples).

You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.09</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.8.0</td>
<td>1.13.0a0+d0d6b1f</td>
<td>TensorRT 8.5.0.12</td>
</tr>
<tr>
<td>22.08</td>
<td></td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.11.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.11.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.11.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.11.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.11.0a0+2ecb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.11.0a0+df837d0</td>
<td>TensorRT 7.2.2.3 + cuda11.1.0.024</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.11.0a0+52ea372</td>
<td>TensorRT 7.2.2.3 + cuda11.1.0.024</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.11.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.11.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.11.0a0+7036e91</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.11.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.11.0a0+6392713</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.11.0a0+9907a3e</td>
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<tr>
<td>20.09</td>
<td></td>
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For more information about AMP, see the [Training With Mixed Precision Guide](#).

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This model script is available on GitHub and NGC.

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This model script is available on GitHub and NGC.

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

This model script is available on GitHub and NGC.

**Known Issues**

- Up to 49% performance regression in DLRM on multi-GPU training runs.
On H100 NVLink systems using 2 GPUs for training, certain communication patterns can trigger a corner-case bug that manifests either as a hang or as an "illegal instruction" exception. A workaround for this case is to set the environment variable `NCCL_PROTO=^LL128`. This issue will be addressed in an upcoming release.
Chapter 24. PyTorch Release 22.08

The NVIDIA container image for PyTorch, release 22.08, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8**
- **NVIDIA CUDA® 11.7.1**
- **NVIDIA cuBLAS 11.10.3.66**
- **NVIDIA cuDNN 8.5.0.96**
- **NVIDIA NCCL 2.12.12** (optimized for **NVIDIA NVLink®**)
- **NVIDIA RAPIDS™ 22.06** (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- **Apex**
- **rdma-core 36.0**
- **NVIDIA HPC-X 2.10 with UCX 1.12.0**
- **OpenMPI 4.1.2rc4+**
- **GDRCopy 2.3**
- **TensorBoard 2.9.0**
- **Nsight Compute 2022.2.1**
- **Nsight Systems 2022.1.3.18**
- **NVIDIA TensorRT™ 8.4.2.4**
- **Torch-TensorRT 1.1.0a0**
- **NVIDIA DALI® 1.16.0**
- **MAGMA 2.6.2**

Jupyter and JupyterLab:

- **Jupyter Client 6.0.0**
Jupyter Core 4.6.1
Jupyter Notebook 6.0.3
JupyterLab 2.3.2, including Jupyter-TensorBoard
JupyterLab Server 1.0.6
Jupyter-TensorBoard

Driver Requirements

Release 22.08 is based on CUDA 11.7.1, which requires NVIDIA Driver release 515 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), or 510.47 (or later R510).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 11.7. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 22.08 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, and NVIDIA Ampere Architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 22.08 is based on 1.13.0a0+d321be6.
- CUDA Module loading is set to LAZY starting with the 22.08 container. To enable the default eager loading behavior, use `export CUDA_MODULE_LOADING=EAGER` or `unset CUDA_MODULE_LOADING`. Refer to the CUDA C++ Programming Guide for more information about this environment variable.

Announcements

- NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.

- A preview of Torch-TensorRT (1.1.0a0) is now included.

Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, use DLProf:

  DLProf can help data scientists, engineers, and researchers understand improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.08</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.7.1</td>
<td>1.13.0a0+d321be6</td>
<td>TensorRT 8.4.2.4</td>
</tr>
<tr>
<td>22.07</td>
<td></td>
<td>NVIDIA CUDA 11.7.0 Update 1 Preview</td>
<td>1.13.0a0+08820cb</td>
<td>TensorRT 8.4.1</td>
</tr>
<tr>
<td>22.06</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+81a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043TensorRT 8.2.1.8</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------------</td>
<td>---------------------------------------</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dfc</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.0.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0.0</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.0</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td></td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation and a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only three lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

APEX AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative and offers a number of advantages over APEX AMP.

- Guidance and examples demonstrating torch.cuda.amp can be found here.
- APEX AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NGC focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- ResNetXt101-32x4d model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.

  This model script is available on GitHub.

- **TransformerXL** model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.

  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

  This model script is available on GitHub.

- **Jasper** model: This repository provides an implementation of the Jasper model in PyTorch from the Jasper: An End-to-End Convolutional Neural Acoustic Model paper.

  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.

  This model script is available on GitHub and NGC.

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.

  This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. The NVIDIA BERT implementation is an optimized version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

  This model script is available on GitHub and NGC.

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on GitHub and NGC.
- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper
This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.
  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.
This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.
  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.
This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.
This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.
This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

### Known Issues

- Performance regression on Volta and NVIDIA Ampere Architecture for ResNet-like model inference use cases of up to 18%.
- Performance regression on Volta and NVIDIA Ampere Architecture for Tactron2+Waveglow inference use cases of up to 35%.
- The default `antialiasing` argument for resizing operations in DALI 1.16.0 was changed to `True`, which could result in performance regressions on CPU-limited use cases. Set this argument to `False` to restore the previous behavior.
Chapter 25. PyTorch Release 22.07

The NVIDIA container image for PyTorch, release 22.07, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8
- NVIDIA CUDA® 11.7 Update 1 Preview
- NVIDIA cuBLAS 11.10.3.66
- NVIDIA cuDNN 8.4.1
- NVIDIA NCCL 2.12.12 (optimized for NVIDIA NVLink®)
- NVIDIA RAPIDS™ 22.04 (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.10 with UCX 1.12.0
- OpenMPI 4.1.2rc4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2022.2.1
- Nsight Systems 2022.1.3.3
- NVIDIA TensorRT™ 8.4.1
- Torch-TensorRT 1.1.0a0
- NVIDIA DALI® 1.15.0
- MAGMA 2.6.2
- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
Driver Requirements

Release 22.07 is based on CUDA 11.7 Update 1 Preview, which requires NVIDIA Driver release 515 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), or 510.47 (or later R510).

The CUDA driver’s compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 11.7. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 22.07 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, and NVIDIA Ampere GPU architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ **PyTorch** container image version 22.07 is based on 1.13.0a0+08820cb.

Announcements

‣ NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.

‣ A preview of Torch-TensorRT (1.1.0a0) is now included.

Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

‣ Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, use DLProf.

  DLProf can help data scientists, engineers, and researchers understand and improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples).

  You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

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<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.07</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+08820cb TensorRT 8.4.1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.13.0a0+340c412 TensorRT 8.2.5</td>
<td></td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6 TensorRT 8.2.4.2</td>
<td></td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef TensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c TensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfeSad28 TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043 TensorRT 8.2.1.8</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0ae4f44c TensorRT 8.0.2.2 for Arm SBSA Linux</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>----------</td>
<td>--------------------</td>
<td>-----------------------</td>
<td>------------------------</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1ed6899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td>TensorRT 5.1.5</td>
</tr>
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Each example model trains with mixed precision Tensor Cores on NVIDIA Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

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  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

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  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

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- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

**Known Issues**

None.
Chapter 26. PyTorch Release 22.06

The NVIDIA container image for PyTorch, release 22.06, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8
- NVIDIA CUDA® 11.7 Update 1 Preview
- NVIDIA cuBLAS 11.10.3.66
- NVIDIA cuDNN 8.4.1
- NVIDIA NCCL 2.12.10 (optimized for NVIDIA NVLink®)
- NVIDIA RAPIDS™ 22.04 (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.10 with UCX 1.12.0
- OpenMPI 4.1.2rc4+
- GDRCopy 2.3
- TensorBoard 2.9.0
- Nsight Compute 2022.2.1
- Nsight Systems 2022.1.3.3
- NVIDIA TensorRT™ 8.2.5
- Torch-TensorRT 1.1.0a0
- NVIDIA DALI® 1.14.0
- MAGMA 2.6.2
- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
Driver Requirements

Release 22.06 is based on CUDA 11.7 Update 1 Preview, which requires NVIDIA Driver release 515 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), or 510.47 (or later R510).

The CUDA driver's compatibility package only supports particular drivers. Thus, users should upgrade from all R418, R440, and R460 drivers, which are not forward-compatible with CUDA 11.7. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 22.06 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, and NVIDIA Ampere GPU architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 22.06 is based on 1.13.0a0+340c412.
‣ The TF32 numerical format is enabled by default for cuBLAS and cuDNN operations starting with the 22.06 release. If you encounter training issues especially for regression, generative or higher-order models, or by using TF32 operations in pre- or post-processing steps, try to disable TF32 by setting `torch.set_float32_matmul_precision('highest')`.

Announcements

‣ NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

  Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.
‣ A preview of Torch-TensorRT (1.1.0a0) is now included.
Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 22.05 release, the PyTorch container is available for the Arm SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container.

To profile models in PyTorch, use DLProf.

DLProf can help data scientists, engineers, and researchers understand and improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples).

You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

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The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

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<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.06</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.7 Update 1 Preview</td>
<td>1.13.0a0+340c412</td>
<td>TensorRT 8.2.5</td>
</tr>
<tr>
<td>22.05</td>
<td></td>
<td>NVIDIA CUDA 11.7.0</td>
<td>1.12.0a0+8a1a93a</td>
<td></td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bdl3bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5c</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfeSad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
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</tr>
<tr>
<td>-------------------</td>
<td>----------</td>
<td>----------------------------------------------</td>
<td>-----------------------</td>
<td>---------------------------------------</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9d9cf</td>
<td>TensorRT 8.0.2.2 for Arm SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.7.0a0+7036a91</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.5.0a0+3b9b36e</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+17e1ba</td>
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</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.0.167</td>
<td></td>
<td></td>
</tr>
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<td></td>
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<td></td>
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<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
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<td></td>
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**Known Issues**

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Release 22.05 is based on CUDA 11.7, which requires NVIDIA Driver release 515 or later. However, if you are running on a data center GPU (for example, T4 or any other data center GPU), you can use NVIDIA driver release 450.51 (or later R450), 470.57 (or later R470), or 510.47 (or later R510).

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Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 22.05 is based on 1.12.0a0+8a1a93a.
‣ CUDA 11.7 introduces lazy module loading, which can save memory usage in your application. To enable it, use export CUDA_MODULE_LOADING="LAZY".

Announcements

‣ NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

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</tr>
</thead>
<tbody>
<tr>
<td>22.05</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.7.0</td>
<td>112.0a0+8a1a93a</td>
<td>TensorRT 8.2.5.1</td>
</tr>
<tr>
<td>22.04</td>
<td></td>
<td>NVIDIA CUDA 11.6.2</td>
<td>112.0a0+bd13bc6</td>
<td>TensorRT 8.2.4.2</td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>112.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>111.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td></td>
<td>111.0a0+bfe5ad28TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>111.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.0.3.4</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>110.0a0+0af44c for x64 Linux</td>
<td>TensorRT 8.0.2.2 for Arm SBSA Linux</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>---------</td>
<td>----------------------</td>
<td>------------------</td>
<td>-------------------</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.8.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
</tbody>
</table>

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- [ResNeXt101-32x4d](#) model: This model was introduced in the [Aggregated Residual Transformations for Deep Neural Networks](#) paper.
  - It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on [GitHub](#).
- **SE-ResNext model**: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the *Squeeze-and-Excitation Networks* paper.

  This model script is available on [GitHub](https://github.com).

- **TransformerXL model**: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.

  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

  This model script is available on [GitHub](https://github.com).

- **Jasper model**: This repository provides an implementation of the Jasper model in PyTorch from the *Jasper: An End-to-End Convolutional Neural Acoustic Model* paper.

  The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without external data.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **BERT model**: Bidirectional Encoder Representations from Transformers (BERT) is a new method of pretraining language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks.

  This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. NVIDIA’s BERT implementation is an optimized version of the Hugging Face implementation paper that leverages mixed-precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation.

  The paper describing the model can be found [here](https://arxiv.org/). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA V100 GPUs for 1.3x faster training time while maintaining target accuracy.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
- A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper.


This model script is available on GitHub and NGC.

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.

  The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

  This model script is available on GitHub and NGC.

- **Neural Collaborative Filtering (NCF) model**: This model focuses on providing recommendations, which is also known as collaborative filtering with implicit feedback.

  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

  This model script is available on GitHub and NGC.

**Known Issues**

- Up to 30% inference and 21% training performance drop for EfficientDet-D0.
- Up to 20% inference and 17% training performance drop for NCF.
- Potential out-of-memory issues in Tacotron2 when modules are scripted in amp. Disable autocast in TorchScript by using `torch._C._jit_set_autocast_mode(False)` if you encounter this issue.
Chapter 28. PyTorch Release 22.04

The NVIDIA container image for PyTorch, release 22.04, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is prebuilt and installed in the Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch/`) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8**
- **NVIDIA CUDA® 11.6.2**
- **cuBLAS 11.9.3.115**
- **NVIDIA cuDNN 8.4.0.27**
- **NVIDIA NCCL 2.12.10** (optimized for **NVIDIA NVLink®**)  
- NVIDIA RAPIDS™ 22.02 (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- **Apex**
- **rdma-core 36.0**
- NVIDIA HPC-X 2.10 with UCX 1.12.0
- **OpenMPI 4.1.2rc4+**
- **GDRCopy 2.3**
- TensorBoard 2.8.0
- **Nsight Compute 2022.1.1.2**
- **Nsight Systems 2022.2.1.31-5fe97ab**
- **NVIDIA TensorRT™ 8.2.4.2**
- **Torch-TensorRT 1.1.0a0**
- **NVIDIA DALI® 1.12.0**
- **MAGMA 2.5.2**
- Jupyter and JupyterLab:
  - **Jupyter Client 6.0.0**
- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 2.3.2, including Jupyter-TensorBoard
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

**Driver Requirements**

Release 22.04 is based on CUDA 11.6.2, which requires NVIDIA Driver release 510 or later. However, if you are running on a Data Center GPU (for example, T4 or any other Tesla board), use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), 460.27 (or later R460), or 470.57 (or later R470). The CUDA driver’s compatibility package only supports specific drivers. For a complete list of supported drivers, see CUDA Application Compatibility. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

**GPU Requirements**

Release 22.04 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, and NVIDIA Ampere GPU architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

**Key Features and Enhancements**

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 22.04 is based on 1.12.0a0+bd13bc6.

**Announcements**

- NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.

  Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.

- A preview of Torch-TensorRT (1.1.0a0) is now included.

  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

- Starting with the 21.10 release, a beta version of the PyTorch container is available for the ARM SBSA platform.

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, use DLProf.

DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.6.2</td>
<td>1.12.0a0+bd13bc6 TensorRT 8.2.4.2</td>
<td></td>
</tr>
<tr>
<td>22.03</td>
<td></td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef TensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043 TensorRT 8.2.1.8</td>
<td></td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>TensorRT 8.0.3.4</td>
<td></td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c for x64 Linux TensorRT 8.0.2.2 for ARM SBSA Linux</td>
<td></td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf TensorRT 8.0.3</td>
<td></td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>TensorRT 8.0.1.6</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
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<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
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</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
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</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
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</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
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</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.0.1</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.0</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+5f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+74e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
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- ResNeXt101-32x4d model: This model was introduced in the Aggregated Residual Transformations for Deep Neural Networks paper.
  
  It is based on the regular ResNet model, which substitutes 3x3 convolutions in the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- SE-ResNext model: This ResNeXt101-32x4d model has an added Squeeze-and-Excitation (SE) module that was introduced in the Squeeze-and-Excitation Networks paper.
  
  This model script is available on GitHub.

- TransformerXL model: This transformer-based language model has a segment-level recurrence and a novel relative positional encoding.
  
  The enhancements that were introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments.
Our implementation is based on the codebase that was published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters, our modifications were made to achieve better hardware usage and to take advantage of Tensor Cores.

This model script is available on GitHub.

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- **Mask R-CNN model**: Mask R-CNN is a convolution-based neural network that is used for object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, which leverages mixed precision arithmetic by using Tensor Cores on NVIDIA Tesla® V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub and NGC.

- **Tacotron 2 and WaveGlow v1.1 model**: This text-to-speech (TTS) system is a combination of the following neural network models:
  - A modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper

This model script is available on GitHub and NGC.

- **SSD300 v1.1 model**: This model is based on the *SSD: Single Shot MultiBox Detector* paper.
The main difference between this model and the model described in the paper is in the backbone. Specifically, the VGG model is obsolete and has been replaced by the ResNet50 model.

This model script is available on GitHub and NGC.

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  The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper.

  This model script is available on GitHub and NGC.

- **ResNet50 v1.5 model**: This model is a modified version of the original ResNet50 v1 model.

  This model script is available on GitHub and NGC.

- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

  This model script is available on GitHub and NGC.

**Known Issues**

Up to an 18% performance regression in the NCF inference on Volta GPUs.
Chapter 29. PyTorch Release 22.03

The NVIDIA container image for PyTorch, release 22.03, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is prebuilt and installed in the Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA® 11.6.1
- cuBLAS 11.8.1.74
- NVIDIA cuDNN 8.3.3.40
- NVIDIA NCCL 2.12.9 (optimized for NVIDIA NVLink®™)
- NVIDIA RAPIDS™ 22.02 (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- Apex
- rdma-core 36.0
- NVIDIA HPC-X 2.10 with UCX 1.12.0
- OpenMPI 4.1.2rc4+
- GDRCopy 2.3
- TensorBoard 2.8.0
- Nsight Compute 2022.1.1.2
- Nsight Systems 2021.5.2.53
- NVIDIA TensorRT™ 8.2.3
- Torch-TensorRT 1.1.0a0
- NVIDIA DALI® 1.11.1
- MAGMA 2.5.2
- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
PyTorch Release 22.03

- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 2.3.2, including Jupyter-TensorBoard
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

Driver Requirements

Release 22.03 is based on CUDA 11.6.1, which requires NVIDIA Driver release 510 or later. However, if you are running on a Data Center GPU (for example, T4 or any other Tesla board), use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), 460.27 (or later R460), or 470.57 (or later R470). The CUDA driver’s compatibility package only supports specific drivers. For a complete list of supported drivers, see CUDA Application Compatibility. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 22.03 supports CUDA compute capability 6.0 and later. This corresponds to GPUs in the NVIDIA Pascal, NVIDIA Volta™, NVIDIA Turing™, and NVIDIA Ampere GPU architecture families. For a list of GPUs to which this compute capability corresponds, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 22.03 is based on 1.12.0a0+2c916ef.

Announcements

- NVIDIA Deep Learning Profiler (DLProf) v1.8, which was included in the 21.12 container, was the final release of DLProf.
  Starting with the 22.01 container, DLProf is no longer included, but it can still be manually installed by using a pip wheel on the nvidia-pyindex.
- A preview of Torch-TensorRT (1.1.0a0) is now included.
  Torch-TRT is the TensorRT integration for PyTorch and brings the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
- Starting with the 21.10 release, a beta version of the PyTorch container is available for the ARM SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, use DLProf.

DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization by using the DLProf Viewer in a web browser or by analyzing text reports. DL Prof is available on NGC or through a Python PIP wheel installation.

The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples).

You can obtain the models from Github or the NVIDIA GPU Cloud (NGC) instead. Some Python packages that were included in previous containers to support these example models have also been removed. Depending on their specific use cases, you might need to add some packages that were previously preinstalled.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For earlier container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.03</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.6.1</td>
<td>1.12.0a0+2c916ef</td>
<td>TensorRT 8.2.3</td>
</tr>
<tr>
<td>22.02</td>
<td></td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6d0f43</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aeff44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.10.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+6392713</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
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</tr>
<tr>
<td>20.01</td>
<td></td>
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<td>1.2.0</td>
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</table>

*19.08 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked*
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation and a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. AMP will select an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but `torch.cuda.amp` is the future-proof alternative and offers a number of advantages over Apex AMP.

- Guidance and examples demonstrating `torch.cuda.amp` can be found [here](#).
- Apex AMP examples can be found [here](#).

For more information about AMP, see the [Training With Mixed Precision Guide](#).

Tensor Core Examples

The [tensor core examples provided in GitHub](#) and [NGC](#) focus on achieving the best performance and convergence from NVIDIA Volta™ tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and NVIDIA Turing™, so you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

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- **GNMT v2 model**: This model is similar to the model that is discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper.

  This model script is available on [GitHub](https://github.com) and [NGC](https://ngc.nvidia.com).

**Known Issues**

None
Chapter 30. PyTorch Release 22.02

The NVIDIA container image for PyTorch, release 22.02, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch/`) in the container image.

The container also includes the following:
- **Ubuntu 20.04** including **Python 3.8** environment
- **NVIDIA CUDA 11.6.0**
- **cuBLAS 11.8.1.74**
- **NVIDIA cuDNN 8.3.2.44**
- **NVIDIA NCCL 2.11.4** (optimized for NVLink™)
- RAPIDS 21.12 (For x86, only these libraries are included: cudf, xgboost, rmm, cupl, and cugraph.)
- **APEX**
- **rdma-core 36.0**
- NVIDIA HPC-X 2.10
- **OpenMPI 4.1.2rc4+**
- OpenUCX 1.12.0
- GDRCopy 2.3
- TensorBoard 2.8.0
- **TensorRT 8.2.3**
- **Torch-TensorRT 1.1.0a0**
- **SHARP 2.5**
- **APEX**
- **Nsight Compute 2022.1.0.0**
- **Nsight Systems 2021.5.2.53**
- TensorBoard 2.8.0
Driver Requirements

Release 22.02 is based on NVIDIA CUDA 11.6.0, which requires NVIDIA Driver release 510 or later. However, if you are running on a Data Center GPU (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), 460.27 (or later R460), or 470.57 (or later R470). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 22.02 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 22.02 is based on 1.11.0a0+17540c5c.

Announcements

- DLProf v1.8, which was included in the 21.12 container, was the last release of DLProf. Starting with the 22.01 container, DLProf is no longer included. It can still be manually installed via a pip wheel on the nvidia-pyindex.
- A preview of Torch-TensorRT (1.1.0a0) is now included. Torch-TRT is the TensorRT integration for PyTorch bringing the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
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PyTorch Release 22.02

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- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.
- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). Instead they can be obtained from Github or the NVIDIA GPU Cloud (NGC). Some python packages, included in previous containers to support these example models, have also been removed. Depending on their specific use cases, users may need to add some packages that were previously pre-installed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.02</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+17540c5cTensorRT 8.2.3</td>
<td></td>
</tr>
<tr>
<td>22.01</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28TensorRT 8.2.2</td>
<td></td>
</tr>
<tr>
<td>21.12</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043 TensorRT 8.2.1.8</td>
<td></td>
</tr>
<tr>
<td>21.11</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c TensorRT 8.0.3.4 for x64 Linux</td>
<td></td>
</tr>
<tr>
<td>21.10</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcd TensorRT 8.0.3</td>
<td></td>
</tr>
<tr>
<td>21.09</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.2</td>
<td>TensorRT 8.0.1.6</td>
<td></td>
</tr>
<tr>
<td>21.08</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.1</td>
<td>TensorRT 7.2.3.4</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd TensorRT 7.2.3.4</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
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<td>----------------</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
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</tr>
<tr>
<td>19.08</td>
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end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model.** BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **NCF model.** The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
Known Issues

- Starting in 22.02 the PyTorch container does not build Caffe2 anymore. If scripted models were exported in the legacy format (using our 19.09 or previous NGC containers corresponding to PyTorch 1.2.0 or previous releases) you might need to re-export the model in order to be able to load it in 22.02.

- **Torch-TensorRT vgg_qat.ipynb notebook:**
  - There is a missing field `truncate_long_and_double=True` in `torch_tensotorrt` compilation of a CIFAR10 based QAT model demonstrated in `vgg_qat.ipynb` (QAT notebook). Please add it to `vgg_qat.ipynb` before you compile the model with `torch_tensotorrt`. The notebook can be found at `/opt/pytorch/torch_tensotorrt/notebooks/vgg_qat.ipynb`.
  - The same field is also required for the post-training quantization sample to pass successfully. Please add `truncate_long_and_double=True` to the compile spec during `torch_tensotorrt` compilation. Location: `/opt/pytorch/torch_tensotorrt/examples/int8/ptq/main.cpp`.

- **ARM**
  - Passing external CUDA Streams to PyTorch via `torch.cuda.streams.ExternalStream(stream_v)` might fail and is being debugged.
Chapter 31. PyTorch Release 22.01

The NVIDIA container image for PyTorch, release 22.01, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch/`) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.6.0
- cuBLAS 11.8.1.74
- NVIDIA cuDNN 8.3.2.44
- NVIDIA NCCL 2.11.4 (optimized for NVLink™)
- RAPIDS 21.12 (For x86, only these libraries are included: cudf, xgboost, rmm, cuml, and cugraph.)
- rdma-core 36.0
- NVIDIA HPC-X 2.10
- OpenMPI 4.1.2rc4+
- OpenUCX 1.12.0
- GDRCopy 2.3
- TensorRT 8.2.2
- Torch-TensorRT 1.1.0a0
- SHARP 2.5
- APEX
- Nsight Compute 2022.1.0.0
- Nsight Systems 2021.3.2.4
- TensorBoard 2.8.0
- DALI 1.9
- MAGMA 2.5.2
Jupyter and JupyterLab:

- Jupyter Client 6.0.0
- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 2.3.2
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

Driver Requirements

Release 22.01 is based on NVIDIA CUDA 11.6.0, which requires NVIDIA Driver release 510 or later. However, if you are running on a Data Center GPU (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), 460.27 (or later R460), or 470.57 (or later R470). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 22.01 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 22.01 is based on 1.11.0a0+bfe5ad28.
- The 22.01 container ships with a preview of the cuDNN v8 API with bfloat16 support and can be enabled using export CUDNN_V8_API_ENABLED=1. To use the new neural network-based heuristics, use export USE_HEURISTIC_MODE_B=1 in addition to export CUDNN_V8_API_ENABLED=1. Please refer to the cuDNN API docs for more information about this heuristic mode (https://docs.nvidia.com/deeplearning/cudnn/api/index.html).

Announcements

- DLProf v1.8, which was included in the 21.12 container, was the last release of DLProf. Starting with the 22.01 container, DLProf is no longer included. It can still be manually installed via a pip wheel on the nvidia-pyindex.
A preview of Torch-TensorRT (1.1.0a0) is now included. Torch-TRT is the TensorRT integration for PyTorch bringing the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.

Starting with the 21.10 release, a beta version of the PyTorch container is available for the ARM SBSA platform.

Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.

The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). Instead they can be obtained from Github or the NVIDIA GPU Cloud (NGC). Some python packages, included in previous containers to support these example models, have also been removed. Depending on their specific use cases, users may need to add some packages that were previously pre-installed.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>22.01</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.6.0</td>
<td>1.11.0a0+bfe5ad28</td>
<td>TensorRT 8.2.2</td>
</tr>
<tr>
<td>21.12</td>
<td></td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
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<tr>
<td>-------------------</td>
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</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
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<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td></td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td></td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td></td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
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</tr>
<tr>
<td>19.08</td>
<td></td>
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<td>1.2.0a0 including</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>upstream commits</td>
<td></td>
</tr>
<tr>
<td></td>
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<td>up through commit</td>
<td></td>
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<tr>
<td></td>
<td></td>
<td></td>
<td>9130ab38 from</td>
<td></td>
</tr>
<tr>
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<td></td>
<td></td>
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**Known Issues**

- The version of OpenUCX included with PyTorch container image version 21.11 has known issues with RAPIDS UCX-Py. When using Dask with this container version, pass `protocol="tcp"` to `LocalCUDACluster()`, not `protocol="ucx"`, to work around these issues. Additionally, `LocalCUDACluster` UCX-specific configurations must remain unspecified; they are: `enable_tcp_over_ucx`, `enable_nvlink`, `enable_infiniband`, `enable_rdmacm` and `ucx_net_devices`.

- **ARM**
  - Passing external CUDA Streams to PyTorch via `torch.cuda.streams.ExternalStream(stream_v)` might fail and is being debugged.
Chapter 32. PyTorch Release 21.12

The NVIDIA container image for PyTorch, release 21.12, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8** environment
- **NVIDIA CUDA 11.5.0**
- **cuBLAS 11.7.3.1**
- **NVIDIA cuDNN 8.3.1.22**
- **NVIDIA NCCL 2.11.4** (optimized for NVLink™)
- **RAPIDS 21.10**
- **rdma-core 36.0**
- **OpenMPI 4.1.2a1**
- **OpenUCX 1.11.0rc1**
- **GDRCopy 2.3**
- **NVIDIA HPC-X 2.9**
- **TensorRT 8.2.1.8**
- **Torch-TensorRT 1.1.0a0**
- **SHARP 2.5**
- **APEX**
- **Nsight Compute 2021.3.0.13**
- **Nsight Systems 2021.3.2.4**
- **TensorBoard 2.7.0**
- **DALI 1.8**
- **MAGMA 2.5.2**
- **DLProf 1.8.0**
Jupyter and JupyterLab:

- Jupyter Client 6.0.0
- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 2.3.2
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

Driver Requirements

Release 21.12 is based on **NVIDIA CUDA 11.5.0**, which requires **NVIDIA Driver** release 495 or later. However, if you are running on a Data Center GPU (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), 460.27 (or later R460), or 470.57 (or later R470). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the **CUDA Application Compatibility** topic. For more information, see [CUDA Compatibility and Upgrades](https://docs.nvidia.com/deeplearning/cuda/compatibility/) and [NVIDIA CUDA and Drivers Support](https://docs.nvidia.com/deeplearning/index.html).

GPU Requirements

Release 21.12 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see [CUDA GPUs](https://docs.nvidia.com/deeplearning/cuda/gpus/). For additional support details, see [Deep Learning Frameworks Support Matrix](https://docs.nvidia.com/deeplearning/index.html).

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 21.12 is based on [1.11.0a0+b6df043](https://docs.nvidia.com/deeplearning/cudnn/api/index.html).
- The 21.12 container ships with a preview of the cuDNN v8 API and can be enabled via `export CUDNN_V8_API_ENABLED=1`. To use the new neural network-based heuristics, use `export USE_HEURISTIC_MODE_B=1` in addition to `export CUDNN_V8_API_ENABLED=1`. Please refer to the cuDNN API docs for more information about this heuristic mode ([https://docs.nvidia.com/deeplearning/cudnn/api/index.html](https://docs.nvidia.com/deeplearning/cudnn/api/index.html)).

Announcements

- DLProf v1.8, which is included in the 21.12 container, will be the last release of DLProf. Starting with the 22.01 container, DLProf will no longer be included. It can still be manually installed via a pip wheel on the nvidia-pyindex.
- A preview of Torch-TensorRT (1.1.0a0) is now included. Torch-TRT is the TensorRT integration for PyTorch bringing the capabilities of TensorRT directly to Torch in one line Python and C++ APIs.
- Starting with the 21.10 release, a beta version of the PyTorch container is available for the ARM SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.
- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). Instead they can be obtained from Github or the NVIDIA GPU Cloud (NGC). Some python packages, included in previous containers to support these example models, have also been removed. Depending on their specific use cases, users may need to add some packages that were previously pre-installed.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.12</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.5.0</td>
<td>1.11.0a0+b6df043</td>
<td>TensorRT 8.2.1.8</td>
</tr>
<tr>
<td>21.11</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0aef44c</td>
<td>TensorRT 8.0.3.4 for x64 Linux</td>
</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.2.2 for ARM SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td></td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>---------</td>
<td>---------------</td>
<td>---------------</td>
<td>-------------------------------</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.9.0a0+df837do</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>11.2.1</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>11.1.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>11.0.3</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>11.0.194</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.5.0a0+3bbb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
</tbody>
</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

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Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 21.11 is based on 1.11.0a0+b6df043.
- Preview of Torch-TensorRT (1.0.0a0): The TensorRT integration for PyTorch.
- PyTorch container image version 21.11 introduces RAPIDS libraries cuDF, cuML, cuGraph, RMM, and XGBoost.

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</tr>
</thead>
<tbody>
<tr>
<td>21.11</td>
<td>20.04</td>
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</tr>
<tr>
<td>21.10</td>
<td></td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
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</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9d6c</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------------</td>
<td>------------------</td>
<td>-------------------</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8de84fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.2.0a0 including</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td>upstream commits</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
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<tr>
<td>19.09</td>
<td></td>
<td></td>
<td>1.2.0</td>
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<td>1.2.0a0 including</td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td>commits up through</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>commit 9130ab38</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>from July 31, 2019</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>as well as a cherry-picked</td>
<td></td>
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### Known Issues

- The version of OpenUCX included with PyTorch container image version 21.11 has known issues with RAPIDS UCX-Py. When using Dask with this container version, pass `protocol="tcp"` to `LocalCUDACluster()`, not `protocol="ucx"`, to work around these issues. Additionally, `LocalCUDACluster` UCX-specific configurations must remain unspecified; they are: `enable_tcp_over_ucx`, `enable_nvlink`, `enable_infiniband`, `enable_rdma_cm` and `ucx_net_devices`.

- **ARM**

  - Passing external CUDA Streams to PyTorch via 
    `
torch.cuda.streams.ExternalStream(stream_v)` might fail and is being debugged.
The NVIDIA container image for PyTorch, release 21.10, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2
- NVIDIA cuDNN 8.2.4.15
- NVIDIA NCCL 2.11.4 (optimized for NVLink™)
- rdma-core 36.0
- OpenMPI 4.1.2a1
- OpenUCX 1.11.0rc1
- GDRCopy 2.3
- NVIDIA HPC-X 2.9
- Nsight Systems 2021.3.2.4
- TensorRT 8.0.3.4 for x64 Linux
- TensorRT 8.0.2.2 for ARM SBSA Linux
- SHARP 2.5
- APEX
- Nsight Compute 2021.2.2.0
- Nsight Systems 2021.3.2.4
- TensorBoard 2.6.0
- DALI 1.6
- MAGMA 2.5.2
- DLProf 1.6.0
- Jupyter and JupyterLab:
Driver Requirements
Release 21.10 is based on NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2, which requires NVIDIA Driver release 470 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), or 460.27 (or later R460). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements
Release 21.10 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements
This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 21.10 is based on 1.10.0a0+0aef44c

Announcements

- Starting with the 21.10 release, a beta version of the PyTorch container is available for the ARM SBSA platform.
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.
- The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). Instead they can be obtained
from Github or the NVIDIA GPU Cloud (NGC). Some python packages, included in previous containers to support these example models, have also been removed. Depending on their specific use cases, users may need to add some packages that were previously pre-installed.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.10</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.2 with cuBLAS 11.6.5.2</td>
<td>1.10.0a0+0ae44c</td>
<td>TensorRT 8.0.3.4</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>for x64 Linux</td>
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<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>TensorRT 8.0.2.2</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>for ARM SBSA Linux</td>
</tr>
<tr>
<td>21.09</td>
<td></td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.8.0a0+52ea372</td>
<td></td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+1606899</td>
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</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td></td>
<td></td>
<td>1.7.0a0+6392713</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------------------</td>
<td>-------------------</td>
<td>-------------------</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb836e</td>
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</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
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</tr>
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<td></td>
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</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
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<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>upstream commits</td>
<td></td>
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<tr>
<td></td>
<td></td>
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<td>9130ab38 from</td>
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<td></td>
<td></td>
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<td>July 31, 2019 as</td>
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<td>well as a cherry-</td>
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- **ARM**
  - Passing external CUDA Streams to PyTorch via `torch.cuda.streams.ExternalStream(stream_v)` might fail and is being debugged.
Chapter 35. PyTorch Release 21.09

The NVIDIA container image for PyTorch, release 21.09, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch/`) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.4.2
- cuBLAS 11.6.1.51
- NVIDIA cuDNN 8.2.4.15
- NVIDIA NCCL 2.11.4
- APEX
- rdma-core 36.0
- OpenMPI 4.1.2a1
- OpenUCX 1.11.0rc
- GDRCopy 2.3
- NVIDIA HPC-X 2.9
- Nsight Compute 2021.2.2.0
- Nsight Systems 2021.3.1.57
- TensorRT 8.0.3
- TensorBoard 2.6.0
- DALI 1.5
- MAGMA 2.5.2
- DLPprof 1.5.0
- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
  - Jupyter Core 4.6.1
Driver Requirements

Release 21.09 is based on NVIDIA CUDA 11.4.2, which requires NVIDIA Driver release 470 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), or 460.27 (or later R460). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 21.09 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 21.09 is based on 1.10.0a0+3fd9dcf

Announcements

‣ Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

‣ Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.

‣ The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). Instead they can be obtained from Github or the NVIDIA GPU Cloud (NGC). Some python packages, included in previous containers to support these example models, have also been removed. Depending on their specific use cases, users may need to add some packages that were previously pre-installed.
NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.09</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.2</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.3</td>
</tr>
<tr>
<td>21.08</td>
<td></td>
<td>NVIDIA CUDA 11.4.1</td>
<td>TensorRT 8.0.1.6</td>
<td></td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td></td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.05</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.04</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
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</table>
**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

**Tensor Core Examples**

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.
- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the *Aggregated Residual Transformations for Deep Neural Networks* paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the *Squeeze-and-Excitation Networks* paper. This model script is available on GitHub.

- **TransformerXL** model. Transformer-XL is a transformer-based language model with a segment-level recurrence and a novel relative positional encoding. Enhancements introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters. Our modifications were made to achieve better hardware utilization and to take advantage of Tensor Cores. This model script is available on GitHub.

- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper *Jasper: An End-to-End Convolutional Neural Acoustic Model*. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT** model. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. NVIDIA's implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model**. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA's Mask R-CNN model is an optimized version of Facebook's implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model**. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper and a flow-based neural network model from the *WaveGlow: A Flow-based Generative Network for Speech Synthesis* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
SSD300 v1.1 model. The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

NCF model. The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

ResNet50 v1.5 model. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

Known Issues

- Known performance regressions in 21.09 vs. 21.07:
  - Up to 20% performance drop for Tacotron inference due to missing fused kernels in the scripted model.
Chapter 36. PyTorch Release 21.08

The NVIDIA container image for PyTorch, release 21.08, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch/`) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.4.1
- cuBLAS 11.5.4
- NVIDIA cuDNN 8.2.2.26
- NVIDIA NCCL 2.10.3
- APEX
- rdma-core 36.0
- OpenMPI 4.1.2a1
- OpenUCX 1.11.0rc
- GDRCopy 2.2
- NVIDIA HPC-X 2.9
- Nsight Systems 2021.2.4.12
- TensorRT 8.0.1.6
- TensorBoard 2.6.0
- DALI 1.4
- MAGMA 2.5.2
- DLProf 1.4.0
- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
  - Jupyter Core 4.6.1
  - Jupyter Notebook 6.0.3
Driver Requirements

Release 21.08 is based on NVIDIA CUDA 11.4.1, which requires NVIDIA Driver release 470 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), or 460.27 (or later R460). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 21.08 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 21.08 is based on 1.10.0a0+3fd9dcf

Announcements

‣ Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

‣ Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.

‣ The TensorCore example models are no longer provided in the core PyTorch container (previously shipped in /workspace/nvidia-examples). Instead they can be obtained from Github or the NVIDIA GPU Cloud (NGC). Some python packages, included in previous containers to support these example models, have also been removed. Depending on their specific use cases, users may need to add some packages that were previously pre-installed
# NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the [Frameworks Support Matrix](#).

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<th>PyTorch</th>
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</tr>
</thead>
<tbody>
<tr>
<td>21.08</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.1</td>
<td>1.10.0a0+3fd9dcf</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.07</td>
<td></td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
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</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52a372</td>
<td>TensorRT 7.1.3.2</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1066899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0.0</td>
<td>1.7.0a0+7036e91</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+6392713</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.05</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.04</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
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</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>-----------------</td>
<td>-------------------------</td>
<td>-----------------------</td>
<td>-----------------------</td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>1.2.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

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For more information about AMP, see the Training With Mixed Precision Guide.

**Tensor Core Examples**

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time.

- ResNeXt101-32x4d model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on
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- **BERT model.** BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA's implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA's Mask R-CNN model is an optimized version of Facebook's implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and
is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **NCF model.** The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

**Known Issues**

- Known performance regressions in 21.08 vs. 21.07:
  - Up to 20% performance drop for Tacotron inference due to missing fused kernels in the scripted model.
Chapter 37. PyTorch Release 21.07

The NVIDIA container image for PyTorch, release 21.07, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch/`) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.4.0
- cuBLAS 11.5.2.43
- NVIDIA cuDNN 8.2.2.26
- NVIDIA NCCL 2.10.3
- APEX
- rdma-core 32.1
- OpenMPI 4.1.1rc1
- OpenUCX 1.10.1
- GDRCopy 2.2
- NVIDIA HPC-X 2.8.2rc3
- Nsight Compute 2021.2.0.0
- Nsight Systems 2021.2.4.12
- TensorRT 8.0.1.6
- TensorBoard 2.5.0
- DALI 1.3
- MAGMA 2.5.2
- DLProf 1.3.0
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
  - SE-ResNext
Driver Requirements

Release 21.07 is based on NVIDIA CUDA 11.4.0, which requires NVIDIA Driver release 470 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), or 460.27 (or later R460). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 21.07 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 21.07 is based on 1.10.0a0+ecc3718
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.07</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.4.0</td>
<td>1.10.0a0+ecc3718</td>
<td>TensorRT 8.0.1.6</td>
</tr>
<tr>
<td>21.06</td>
<td></td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+8deb4fe</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
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<td></td>
</tr>
<tr>
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<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
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<td>-------------------</td>
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<td>--------------</td>
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<td>----------</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td></td>
<td>1.5.0a0+3bba36e 1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td></td>
<td>1.3.0a0+24ae9b5 1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
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### Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the [Training With Mixed Precision Guide](#).

### Tensor Core Examples

The tensor core examples provided in GitHub and [NVIDIA GPU Cloud (NGC)](#) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.
Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

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- **TransformerXL** model. Transformer-XL is a transformer-based language model with a segment-level recurrence and a novel relative positional encoding. Enhancements introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters. Our modifications were made to achieve better hardware utilization and to take advantage of Tensor Cores. This model script is available on GitHub.

- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper Jasper: An End-to-End Convolutional Neural Acoustic Model. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

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- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

**Known Issues**

- The 21.07 release includes libsystemd and libudev versions that have a known vulnerability that was discovered late in our QA process. See CVE-2021-33910 for details. This will be fixed in the next release.
Chapter 38. PyTorch Release 21.06

The NVIDIA container image for PyTorch, release 21.06, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including Python 3.8 environment
- **NVIDIA CUDA 11.3.1**
- **cuBLAS 11.5.1.109**
- **NVIDIA cuDNN 8.2.1**
- **NVIDIA NCCL 2.9.9** (optimized for NVLink™)
- **APEX**
- **rdma-core 32.1**
- **OpenMPI 4.1.1rc1**
- **OpenUCX 1.10.1**
- **GDRCopy 2.2**
- **NVIDIA HPC-X 2.8.2rc3**
- **Nsight Compute 2021.1.0.0**
- **Nsight Systems 2021.2.1.58**
- **TensorRT 7.2.3.4**
- **TensorBoard 1.15.5**
- **DALI 1.2**
- **MAGMA 2.5.2**
- **DLProf 1.2.0**
- **PyProf r21.06**
- Tensor Core optimized examples:
  - **ResNeXt101-32x4d**
SE-ResNext
TransformerXL
Jasper
BERT
Mask R-CNN
Tacotron 2 and WaveGlow v1.1
SSD300 v1.1
Neural Collaborative Filtering (NCF)
ResNet50 v1.5
GNMT v2

Jupyter and JupyterLab:

- Jupyter Client 6.0.0
- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 2.3.1
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

Driver Requirements

Release 21.06 is based on NVIDIA CUDA 11.3.1, which requires NVIDIA Driver release 465.19.01 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), or 460.27 (or later R460). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 21.06 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 21.06 is based on 1.9.0a0+c3d40fd
- Ubuntu 20.04 with May 2021 updates
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

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</tr>
</thead>
<tbody>
<tr>
<td>21.06</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.3.1</td>
<td>1.9.0a0+c3d40fd</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.05</td>
<td></td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td></td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.11</td>
<td></td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bbb36e</td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td>TensorRT 5.1.5</td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td></td>
</tr>
</tbody>
</table>

### Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

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- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

- **TransformerXL** model. Transformer-XL is a transformer-based language model with a segment-level recurrence and a novel relative positional encoding. Enhancements introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters. Our modifications were made to achieve better hardware utilization and to take advantage of Tensor Cores. This model script is available on GitHub.

- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper Jasper: An End-to-End Convolutional Neural Acoustic Model. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model**. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA's implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model**. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA's Mask R-CNN model is an optimized version of Facebook's implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model**. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based...
Generative Network for Speech Synthesis paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **NCF model.** The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

**Known Issues**

- Known performance regressions in 21.06 vs. 21.05:
  - On Turing and NVIDIA Ampere Architecture GPUs:
    - Up to 15% performance drop for GNMT training
  - On Volta:
    - Up to 20% performance drop for Tacotron training.
- Manual synchronization is required in CUDA graphs workloads between graph replays.
- The PyTorch container includes a version of Django with a known vulnerability that was discovered late in our QA process. See CVE-2021-31542 for details. This will be fixed in the next release.
- The PyTorch container includes a version of Pillow with known vulnerabilities discovered late in our QA process. See CVE-2021-25287, CVE-2021-28676, CVE-2021-28677, and CVE-2021-25288 for details. This will be fixed in the next release.
Chapter 39. PyTorch Release 21.05

The NVIDIA container image for PyTorch, release 21.05, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.3.0
- cuBLAS 11.5.1.101
- NVIDIA cuDNN 8.2.0.51
- NVIDIA NCCL 2.9.8 (optimized for NVLink™)
- APEX
- rdma-core 32.1
- OpenMPI 4.1.1rc1
- OpenUCX 1.10.0
- GDRCopy 2.2
- NVIDIA HPC-X 2.8.2rc3
- Nsight Compute 2021.1.0.0
- Nsight Systems 2021.1.3.14
- TensorRT 7.2.3.4
- TensorBoard 1.15.5
- DALI 1.0.0
- MAGMA 2.5.2
- DLProf 1.1.0
- PyProf r21.05
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
- **SE-ResNext**
- **TransformerXL**
- **Jasper**
- **BERT**
- **Mask R-CNN**
- **Tacotron 2 and WaveGlow v1.1**
- **SSD300 v1.1**
- **Neural Collaborative Filtering (NCF)**
- **ResNet50 v1.5**
- **GNMT v2**

**Jupyter and JupyterLab:**
- **Jupyter Client 6.0.0**
- **Jupyter Core 4.6.1**
- **Jupyter Notebook 6.0.3**
- **JupyterLab 2.3.1**
- **JupyterLab Server 1.0.6**
- **Jupyter-TensorBoard**

### Driver Requirements

Release 21.05 is based on **NVIDIA CUDA 11.3.0**, which requires **NVIDIA Driver** release 465.19.01 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), or 460.27 (or later R460). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see [CUDA Compatibility and Upgrades](https://developer.nvidia.com/cuda) and [NVIDIA CUDA and Drivers Support](https://developer.nvidia.com/cuda-support).

### GPU Requirements

Release 21.05 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see [CUDA GPUs](https://developer.nvidia.com/cuda). For additional support details, see [Deep Learning Frameworks Support Matrix](https://developer.nvidia.com/cuda-support).

### Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 21.05 is based on 1.9.0a0+2ecb2c7
- Ubuntu 20.04 with April 2021 updates
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Starting in 21.06, PyProf will no longer be included in the NVIDIA PyTorch container. To profile models in PyTorch, please use NVIDIA Deep Learning Profiler (DLProf). DLProf can help data scientists, engineers and researchers understand and improve performance of their models with visualization via DLProf Viewer in the web browser, or by analyzing text reports. DL Prof is available on NGC or a Python PIP wheel installation.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.05</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.3.0</td>
<td>1.9.0a0+2ecb2c7</td>
<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
<td>21.04</td>
<td></td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.03</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+6392713</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td></td>
<td>1.5.0a0+3bbb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
</tbody>
</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.
- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the *Aggregated Residual Transformations for Deep Neural Networks* paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the *Squeeze-and-Excitation Networks* paper. This model script is available on GitHub.

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- **BERT** model. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model**. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model**. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper and a flow-based neural network model from the *WaveGlow: A Flow-based Generative Network for Speech Synthesis* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
SSD300 v1.1 model. The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

NCF model. The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

ResNet50 v1.5 model. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

Known Issues

Known performance regressions in 21.05 vs. 21.04:

- On NVIDIA Ampere Architecture GPUs:
  - Up to 17% performance drop for VGG16 training
- Manual synchronization is required in CUDA graphs workloads between graph replays.
- The DLProf TensorBoard plugin included with the 21.04 and 21.05 releases is an incorrect version with respect to the DLProf command line tool included in those releases. To correct this, use the following command:

  ```bash
  $ pip install --index-url=https://developer.download.nvidia.com/compute/redist\n  nvidia_tensorboard_plugin_dlprof==1.1.0
  ```
Chapter 40. PyTorch Release 21.04

The NVIDIA container image for PyTorch, release 21.04, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8** environment
- **NVIDIA CUDA 11.3.0**
- **cuBLAS 11.5.1.101**
- **NVIDIA cuDNN 8.2.0.41**
- **NVIDIA NCCL 2.9.6** (optimized for NVLink™)
- **APEX**
- **rdma-core 32.1**
- **OpenMPI 4.1.1rc1**
- **OpenUCX 1.10.0**
- **GDRCopy 2.2**
- **NVIDIA HPC-X 2.8.2rc3**
- **Nsight Compute 2021.1.0.18**
- **Nsight Systems 2021.1.3.14**
- **TensorRT 7.2.3.4**
- **TensorBoard 1.15.5**
- **DALI 1.0.0**
- **MAGMA 2.5.2**
- **DLPprof 1.1.0**
- **PyProf r21.04**
- Tensor Core optimized examples:
  - **ResNeXt101-32x4d**
- SE-ResNext
- TransformerXL
- Jasper
- BERT
- Mask R-CNN
- Tacotron 2 and WaveGlow v1.1
- SSD300 v1.1
- Neural Collaborative Filtering (NCF)
- ResNet50 v1.5
- GNMT v2

- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
  - Jupyter Core 4.6.1
  - Jupyter Notebook 6.0.3
  - JupyterLab 2.3.1
  - JupyterLab Server 1.0.6
  - Jupyter-TensorBoard

**Driver Requirements**

Release 21.04 is based on NVIDIA CUDA 11.3.0, which requires NVIDIA Driver release 465.19.01 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450), or 460.27 (or later R460). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

**GPU Requirements**

Release 21.04 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

**Key Features and Enhancements**

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 21.04 is based on 1.9.0a0+2ecb2c7
- Experimental release of the nvfuser backend for scripted models. Users can enable it using the context manager: `with torch.jit.fuser("fuser2");`
Ubuntu 20.04 with March 2021 updates

Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
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<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.04</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.3.0</td>
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<td>TensorRT 7.2.3.4</td>
</tr>
<tr>
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<td></td>
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<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8de4f837</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

**Tensor Core Examples**

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.
- **SE-ResNext model.** The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

- **TransformerXL model.** Transformer-XL is a transformer-based language model with a segment-level recurrence and a novel relative positional encoding. Enhancements introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters. Our modifications were made to achieve better hardware utilization and to take advantage of Tensor Cores. This model script is available on GitHub.

- **Jasper model.** This repository provides an implementation of the Jasper model in PyTorch from the paper Jasper: An End-to-End Convolutional Neural Acoustic Model. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model.** BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA’s implementation of BERT is an optimized version of the codebase, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
**NCF model.** The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.com/).

**ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original [ResNet50 v1 model](https://github.com). This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.com/).

**GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.com/).

### Known Issues

- Known performance regressions in 21.04 vs. 21.03:
  - On NVIDIA Ampere Architecture GPUs:
    - Up to 38% performance drop for FastPitch training
    - Up to 30% performance drop in MaskRCNN training
  - On Turing:
    - Up to 20% performance drop in MaskRCNN training
    - Up to 15% performance drop in VGG16 training
- Manual synchronization is required in CUDA graphs workloads between graph replays.
- The DLProf TensorBoard plugin included with the 21.04 release is an incorrect version with respect to the DLProf command line tool included in those releases. To correct this, use the following command:

```bash
$ pip install --index-url=https://developer.download.nvidia.com/compute/redist nvidia_tensorboard_plugin_dlprof==1.1.0
```
Chapter 41. PyTorch Release 21.03

The NVIDIA container image for PyTorch, release 21.03, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch/`) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.2.1 including cuBLAS 11.4.1.1026
- NVIDIA cuDNN 8.1.1
- NVIDIA NCCL 2.8.4 (optimized for NVLink™)
- APEX
- MLNX_OFED
- OpenMPI 4.0.5
- TensorBoard 1.15.5
- Nsight Compute 2020.3.1.0
- Nsight Systems 2020.4.3.7
- TensorRT 7.2.2.3
- DALI 0.31.0
- MAGMA 2.5.2
- DLProf 1.0.0
- PyProf r21.03
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
  - SE-ResNext
  - TransformerXL
  - Jasper
  - BERT
- Mask R-CNN
- Tacotron 2 and WaveGlow v1.1
- SSD300 v1.1
- Neural Collaborative Filtering (NCF)
- ResNet50 v1.5
- GNMT v2

Drivers Requirements

Driver Requirements

Work 21.03 is based on NVIDIA CUDA 11.2.1, which requires NVIDIA Driver release 460.32.03 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450). The CUDA driver's compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

GPU Requirements

Release 21.03 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 21.03 is based on 1.9.0a0+df837d0
- NVIDIA CUDA 11.2.1 including cuBLAS 11.4.1.1026
- The latest version of NVIDIA cuDNN 8.1.1
- The latest version of DALI 0.31.0
- The latest version of DLProf 1.0.0
- The latest version of PyProf r21.03
- Ubuntu 20.04 with February 2021 updates
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.03</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.2.1</td>
<td>1.9.0a0+df837d0</td>
<td>TensorRT 7.2.2.3</td>
</tr>
<tr>
<td>21.02</td>
<td></td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
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<td>1.2.0</td>
<td></td>
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</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.
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- **BERT model**. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model**. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

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- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

### Known Issues

Known performance regressions in 21.03 vs. 21.02:

- On A102 (e.g. A40):
  - Up to 28% performance drop for BERT large inference and pre-training
  - Up to 33% performance drop for Transformer training

- On TU102 (e.g. RTX6000):
  - Up to 18% performance drop for inference on ResNet-like models
  - Up to 25% performance drop for SSD inference
  - Up to 15% performance drop for WaveGlow
Chapter 42. PyTorch Release 21.02

The NVIDIA container image for PyTorch, release 21.02, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.8/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 20.04 including Python 3.8 environment
- NVIDIA CUDA 11.2.0 including cuBLAS 11.3.1
- NVIDIA cuDNN 8.1.0
- NVIDIA NCCL 2.8.4 (optimized for NVLink™)
- APEX
- MLNX_OFED
- OpenMPI 4.0.5
- TensorBoard 1.15.5
- Nsight Compute 2020.3.0.18
- Nsight Systems 2020.4.3.7
- TensorRT 7.2.2.3+cuda11.1.0.024
- DALI 0.29.0
- MAGMA 2.5.2
- DLPprof 0.19.0
- PyProf r21.02
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
  - SE-ResNext
  - TransformerXL
  - Jasper
  - BERT
Mask R-CNN
- Tacotron 2 and WaveGlow v1.1
- SSD300 v1.1
- Neural Collaborative Filtering (NCF)
- ResNet50 v1.5
- GNMT v2
- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
  - Jupyter Core 4.6.1
  - Jupyter Notebook 6.0.3
  - JupyterLab 1.2.0
  - JupyterLab Server 1.0.6
  - Jupyter-TensorBoard

Driver Requirements

Release 21.02 is based on NVIDIA CUDA 11.2.0, which requires NVIDIA Driver release 460.27.04 or later. However, if you are running on Data Center GPUs (formerly Tesla), for example, T4, you may use NVIDIA driver release 418.40 (or later R418), 440.33 (or later R440), 450.51 (or later R450). The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades and NVIDIA CUDA and Drivers Support.

GPU Requirements

Release 21.02 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 21.02 is based on 1.8.0a0+52ea372
- NVIDIA CUDA 11.2.0 including cuBLAS 11.3.1
- The latest version of NVIDIA cuDNN 8.0.5
- The latest version of NVIDIA NCCL 2.8.4
- The latest version of Nsight Compute 2020.3.0.18
- The latest version of Nsight Systems 2020.4.3.7
- The latest version of TensorRT 7.2.2.3+cuda11.1.0.024
The latest version of DALI 0.29
The latest version of DLProf 0.19.0
The latest version of PyProf r21.02
Ubuntu 20.04 with January 2021 updates

Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

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<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>21.02</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.2.0</td>
<td>1.8.0a0+52ea372</td>
<td>TensorRT 7.2.2.3+cuda11.1.0.024</td>
</tr>
<tr>
<td>20.12</td>
<td></td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
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<td>20.11</td>
<td>18.04</td>
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<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
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<td>20.07</td>
<td></td>
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<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3b436e</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Container Version</td>
<td>Ubuntu</td>
<td>CUDA Toolkit</td>
<td>PyTorch</td>
<td>TensorRT</td>
</tr>
<tr>
<td>-------------------</td>
<td>--------</td>
<td>--------------</td>
<td>---------</td>
<td>----------</td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
</tbody>
</table>

**Automatic Mixed Precision (AMP)**

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a **number of advantages** over Apex AMP.

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For more information about AMP, see the [Training With Mixed Precision Guide](#).

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The tensor core examples provided in GitHub and [NVIDIA GPU Cloud (NGC)](#) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the [Aggregated Residual Transformations for Deep Neural Networks](#) paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on [GitHub](#).
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Jasper model. This repository provides an implementation of the Jasper model in PyTorch from the paper Jasper: An End-to-End Convolutional Neural Acoustic Model. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

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Tacotron 2 and WaveGlow v1.1 model. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

SSD300 v1.1 model. The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
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- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original [ResNet50 v1 model](https://github.com). This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.com/).  

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper*. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.com/).  

### Known Issues

- Known performance regressions in 21.02 vs. 20.12 for:  
  - SSD-Resnet-50 training on Ampere up to 8%  
  - ResNext101 inference on Volta and Ampere up to 24%  
  - Se-ResNeXt101 inference on Ampere up to 17%  
  - SSD inference on Volta up to 17%
Chapter 43. PyTorch Release 21.01

The NVIDIA container image release for PyTorch 21.01 has been canceled. The next release will be the 21.02 release which is expected to be released at the end of February.
Chapter 44. PyTorch Release 20.12

The NVIDIA container image for PyTorch, release 20.12, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.8/site-packages/torch`) in the container image.

The container also includes the following:

- **Ubuntu 20.04** including **Python 3.8** environment
- **NVIDIA CUDA 11.1.1** including **cuBLAS 11.3.0**
- **NVIDIA cuDNN 8.0.5**
- **NVIDIA NCCL 2.8.3** (optimized for **NVLink™**)
- **APEX**
- **MLNX_OFED**
- **OpenMPI 4.0.5**
- **TensorBoard 1.15.0+nv20.11**
- **Nsight Compute 2020.2.1.8**
- **Nsight Systems 2020.3.4.32**
- **TensorRT 7.2.2**
- **DALI 0.28.0**
- **MAGMA 2.5.2**
- **DLProf 0.18.0**
- **PyProf r20.12**
- Tensor Core optimized examples:
  - **ResNeXt101-32x4d**
  - **SE-ResNext**
  - **TransformerXL**
  - **Jasper**
  - **BERT**
Driven Requirements

Release 20.12 is based on NVIDIA CUDA 11.1.1, which requires NVIDIA Driver release 455 or later. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.xx, 440.30, or 450.xx. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 20.12 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.12 is based on 1.8.0a0+1606899
- NVIDIA CUDA 11.1.1 including cuBLAS 11.3.0
- The latest version of NVIDIA cuDNN 8.0.5
- The latest version of NVIDIA NCCL 2.8.3
- The latest version of Nsight Compute 2020.2.1.8
- The latest version of TensorRT 7.2.2
- The latest version of DALI 0.28
The latest version of **DLProf 0.18.0**

The latest version of **PyProf r20.12**

Ubuntu 20.04 with November 2020 updates

### Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- **Transformer** has been removed.

### NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the [Frameworks Support Matrix](#).

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>20.12</td>
<td>20.04</td>
<td>NVIDIA CUDA 11.1.1</td>
<td>1.8.0a0+1606899</td>
<td>TensorRT 7.2.2</td>
</tr>
<tr>
<td>20.11</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
<td>1.8.0a0+17f8c32</td>
<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.10</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+7036e91</td>
<td></td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bbb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.4.0a0+174e1ba</td>
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<td>1.3.0a0+24ae9b5</td>
<td></td>
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<td>1.2.0a0 including upstream commits up through commit</td>
<td>TensorRT 5.1.5</td>
</tr>
</tbody>
</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

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- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

**Known Issues**

- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via torch.logdet(W.unsqueeze(0).float()).squeeze() as is done in this release.

- Known performance regressions in 20.12 vs. 20.11 for:
  - MaskR-CNN training up to 15%
  - Transformer-XL inference of approx. 10%
  - Tacotron2+Waveglow inference up to 50%
  - FastPitch inference and training up to 15%
Chapter 45. PyTorch Release 20.11

The NVIDIA container image for PyTorch, release 20.11, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 18.04 including Python 3.6 environment
- NVIDIA CUDA 11.1.0 including cuBLAS 11.2.1
- NVIDIA cuDNN 8.0.4
- NVIDIA NCCL 2.8.2 (optimized for NVLink™)
- APEX
- MLNX_OFED 5.1
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  - BERT
- Mask R-CNN
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- SSD300 v1.1
- Neural Collaborative Filtering (NCF)
- ResNet50 v1.5
- GNMT v2

Jupyter and JupyterLab:
- Jupyter Client 6.0.0
- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 1.2.0
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

### Driver Requirements

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Release 20.11 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

### Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.11 is based on 1.8.0a0+17f8c32
- The latest version of NVIDIA NCCL 2.8.2
- The latest version of DALI 0.27
- The latest version of DLProf 0.17.0
- The latest version of PyProf r20.11
- Ubuntu 18.04 with October 2020 updates
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

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<td></td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td></td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
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<td>1.5.0a0+8f84ded</td>
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</tr>
<tr>
<td>20.02</td>
<td></td>
<td></td>
<td>1.5.0a0+3bb36e</td>
<td></td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td></td>
<td>1.4.0a0+a5b4d78</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td></td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
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<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked TensorRT 5.1.5</td>
<td></td>
</tr>
</tbody>
</table>
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- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

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- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

### Known Issues

- The PyProf version string in the 20.11 container is truncated to `3.` and does not display the full version string as `3.6.0`.
- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via `torch.logdet(W.unsqueeze(0).float()).squeeze()` as is done in this release.
- Known performance regressions in 20.11 vs. 20.09 for:
  - MaskR-CNN training up to 9%
  - Transformer mixed-precision training up to 11%
  - Transformer-XL training and inference of approx. 10%
  - ResNet and ResNext inference up to 17%
- Known performance regressions in 20.11 vs. 2010 for:
  - GNMT mixed-precision training and inference up to 20%
The NVIDIA container image for PyTorch, release 20.10, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch/`) in the container image.

The container also includes the following:

- **Ubuntu 18.04** including **Python 3.6** environment
- **NVIDIA CUDA 11.1.0** including **cuBLAS 11.2.1**
- **NVIDIA cuDNN 8.0.4**
- **NVIDIA NCCL 2.7.8** (optimized for **NVLink™**)
- **APEX**
- **MLNX_OFED**
- **OpenMPI 4.0.5**
- **TensorBoard 1.15.0+nv**
- **Nsight Compute 2020.2.0.18**
- **Nsight Systems 2020.3.4.32**
- **TensorRT 7.2.1**
- **DALI 0.26.0**
- **MAGMA 2.5.2**
- **DLProf 0.16.0**
- **PyProf r20.10**
- Tensor Core optimized examples:
  - **ResNeXt101-32x4d**
  - **SE-ResNext**
  - **TransformerXL**
  - **Jasper**
  - **BERT**
Driver Requirements

Release 20.10 is based on NVIDIA CUDA 11.1.0, which requires NVIDIA Driver release 455 or later. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.xx, 440.30, or 450.xx. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 20.10 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, Turing, and NVIDIA Ampere GPU architecture families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.10 is based on 1.7.0a0+7036e91
- The latest version of NVIDIA CUDA 11.1.0 including cuBLAS 11.2.1
- The latest version of NVIDIA cuDNN 8.0.4
- The latest version of TensorRT 7.2.1
- The latest version of OpenMPI 4.0.5
- The latest version of Nsight Compute 2020.2.0.18
- The latest version of Nsight Systems 2020.3.4.32
The latest version of **DALI 0.26**

The latest version of **DLProf 0.16.0**

The latest version of **PyProf 3.5.0**

Ubuntu 18.04 with September 2020 updates

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**Announcements**

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the [Frameworks Support Matrix](#).

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<th>Ubuntu</th>
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<th>PyTorch</th>
<th>TensorRT</th>
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<tr>
<td>20.10</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.1.0</td>
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<td>TensorRT 7.2.1</td>
</tr>
<tr>
<td>20.09</td>
<td></td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+8deb4fe</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.08</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.7.0a0+6392713</td>
<td></td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
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<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bbb36e</td>
<td></td>
</tr>
<tr>
<td>20.02</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>20.01</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+174e1ba</td>
<td></td>
</tr>
<tr>
<td>19.12</td>
<td></td>
<td>NVIDIA CUDA 10.1.243</td>
<td>1.3.0a0+24ae9b5</td>
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Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

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- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

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**Known Issues**

- The 20.10 container ships with libfreetype6 2.8.1-2ubuntu2.1, which is vulnerable to CVE-2020-15999. Use
  
  ```bash
  $ apt-get update
  $ apt --only-upgrade install libfreetype6
  ```
  
  to apply the patch to fix this issue, or alternatively, purge this package from the container.

- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via
  
  ```python
  torch.logdet(W.unsqueeze(0).float()).squeeze()
  ```
  
  as is done in this release.

- Known performance regressions in 20.10 vs. 20.09 for:
  
  - Bert mixed-precision training up to 13%
  - MaskR-CNN training up to 9%
  - Transformer mixed-precision training up to 14%
  - Transformer-XL training and inference of approx. 10%
  - ResNet and ResNext inference up to 17%
  - FastPitch FP32 training up to 13%
Chapter 47. PyTorch Release 20.09

The NVIDIA container image for PyTorch, release 20.09, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch/`) in the container image.

The container also includes the following:

- **Ubuntu 18.04** including **Python 3.6** environment
- **NVIDIA CUDA 11.0.3** including **cuBLAS 11.2.0**
- **NVIDIA cuDNN 8.0.4**
- **NVIDIA NCCL 2.7.8** (optimized for **NVLink™**)
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- **Nsight Compute 2020.1.2.4**
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  - **Jasper**
  - **BERT**
- Mask R-CNN
- Tacotron 2 and WaveGlow v1.1
- SSD300 v1.1
- Neural Collaborative Filtering (NCF)
- ResNet50 v1.5
- GNMT v2

Jupyter and JupyterLab:
- Jupyter Client 6.0.0
- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 1.2.0
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

**Driver Requirements**

Release 20.09 is based on NVIDIA CUDA 11.0.3, which requires NVIDIA Driver release 450 or later. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.xx or 440.30. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

**GPU Requirements**

Release 20.09 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

**Key Features and Enhancements**

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.09 is based on 1.7.0a0+8deb4fe with minor cherry-picked bug fixes
- The latest version of NVIDIA cuDNN 8.0.4
- The latest version of DALI 0.25.1
- The latest version of DLProf 0.15.0
- The latest version of PyProf r20.09
- Ubuntu 18.04 with August 2020 updates
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

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<td>19.09</td>
<td></td>
<td></td>
<td>upstream commits</td>
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<td>up through commit</td>
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<td></td>
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<td>9130ab38 from</td>
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<td>July 31, 2019 as</td>
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Known Issues

- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via `torch.logdet(W.unsqueeze(0).float()).squeeze()` as is done in this release.
- Known performance regressions in 20.08 vs. 20.07 for ResNet- and ResNext-like models on all architectures due to a temporal workaround in the dispatching mechanism (commit 0494e0a) up to 21%.
- Known NVIDIA Ampere GPU architecture performance regressions for FastPitch training in FP32 in 20.09 vs. 20.08 up to 10%.
Chapter 48. PyTorch Release 20.08

The NVIDIA container image for PyTorch, release 20.08, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 18.04 including Python 3.6 environment
- NVIDIA CUDA 11.0.3 including cuBLAS 11.2.0
- NVIDIA cuDNN 8.0.2
- NVIDIA NCCL 2.7.8 (optimized for NVLink™)
- APEX
- MLNX_OFED
- OpenMPI 3.1.6
- TensorBoard 1.15.0+nv
- Nsight Compute 2020.1.2.4
- Nsight Systems 2020.3.2.6
- TensorRT 7.1.3
- DALI 0.24
- MAGMA 2.5.2
- DLPprof 0.14.0
- PyProf r20.08
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
  - SE-ResNext
  - TransformerXL
  - Jasper
  - BERT
Driver Requirements

Release 20.08 is based on NVIDIA CUDA 11.0.3, which requires NVIDIA Driver release 450 or later. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.xx or 440.30. The CUDA driver's compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 20.08 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 20.08 is based on 1.7.0a0+6392713 with minor cherry-picked bug fixes
- The latest version of NVIDIA CUDA 11.0.3 including cuBLAS 11.2.0
- The latest version of NVIDIA NCCL 2.7.8
- The latest version of NVIDIA cuDNN 8.0.2
- The latest version of Nsight Compute 2020.1.2.4
- The latest version of Nsight Systems 2020.3.2.6
- The latest version of DALI 0.24
The latest version of DLProf 0.14.0
The latest version of PyProf r20.08
Ubuntu 18.04 with July 2020 updates

Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
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<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>20.08</td>
<td>18.04</td>
<td>NVIDIA CUDA 11.0.3</td>
<td>1.7.0a0+6392713</td>
<td>TensorRT 7.1.3</td>
</tr>
<tr>
<td>20.07</td>
<td></td>
<td>NVIDIA CUDA 11.0.194</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
</tr>
<tr>
<td>20.06</td>
<td></td>
<td>NVIDIA CUDA 11.0.167</td>
<td>1.5.0a0+8f84ded</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>20.03</td>
<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.5.0a0+3bbb36e</td>
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<tr>
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<td></td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 5.1.5</td>
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<tr>
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</table>
Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

- **TransformerXL** model. Transformer-XL is a transformer-based language model with a segment-level recurrence and a novel relative positional encoding. Enhancements introduced in Transformer-XL help capture better long-term dependencies by attending to tokens from multiple previous segments. Our implementation is based on the codebase published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters. Our modifications were made to achieve better hardware utilization and to take advantage of Tensor Cores. his model script is available on GitHub.
- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper *Jasper: An End-to-End Convolutional Neural Acoustic Model*. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model**. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model**. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model**. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper and a flow-based neural network model from the *WaveGlow: A Flow-based Generative Network for Speech Synthesis* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model**. The SSD300 v1.1 model is based on the *SSD: Single Shot MultiBox Detector* paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **NCF model**. The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **ResNet50 v1.5 model**. The ResNet50 v1.5 model is a modified version of the original *ResNet50 v1.1 model*. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

Known Issues

- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via \(\text{torch.logdet}(W.\text{unsqueeze}(0).\text{float()}).\text{squeeze()}\) as is done in this release.
- Known performance regressions in 20.08 vs. 20.07 for ResNet- and ResNext-like models on all architectures due to a temporal workaround in the dispatching mechanism (commit 0494e0a) up to 18%.
- Known Turing performance regressions for FastPitch and WaveGlow inference in 20.08 vs. 20.07 up to 75%.
The NVIDIA container image for PyTorch, release 20.07, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch/`) in the container image.

The container also includes the following:

- **Ubuntu 18.04** including **Python 3.6** environment
- **NVIDIA CUDA 11.0.194** including **cuBLAS 11.1.0**
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- **APEX**
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- **Nsight Compute 2020.1.1.8**
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Driver Requirements

Release 20.07 is based on NVIDIA CUDA 11.0.194, which requires NVIDIA Driver release 450 or later. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.xx or 440.30. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

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Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.07 is based on 1.6.0a0+9907a3e with minor cherry-picked bug fixes
- The latest version of NVIDIA CUDA 11.0.194 including cuBLAS 11.1.0
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- The latest version of NVIDIA cuDNN 8.0.1
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- The latest version of Nsight Systems 2020.3.2.6
- The latest version of TensorRT 7.1.3
- The latest version of OpenMPI 3.1.6
- Ubuntu 18.04 with June 2020 updates
- Latest version of DALI 0.23

**Announcements**

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

**NVIDIA PyTorch Container Versions**

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

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- **ResNet50 v1.5 model**. The ResNet50 v1.5 model is a modified version of the original *ResNet50 v1.1 model*. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

Known Issues

- There is up to 5% performance drop on Transformer-XL mixed precision training in the 20.07 container compared to 19.11. Disabling the profiling executor at the beginning of your script might reduce this effect via:
  ```python
  torch._C._jit_set_profiling_executor(False)
  torch._C._jit_set_profiling_mode(False)
  ```
- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via 
  ```python
  torch.logdet(W.unsqueeze(0).float()).squeeze()
  ```
- Known Turing performance regressions in 20.07 vs. 20.03 container:
  - Up to 10% performance drop on InceptionV3 for mixed precision training
  - Up to 10% performance drop on MaskNCF FP32 training
  - Up to 25% performance drop on MaskRCNN FP32 training
  - Up to 10% performance drop on ResNet50 for mixed precision training.
- Known Volta performance regressions in 20.07 vs. 20.03 container:
  - Up to 30% performance drop on WaveGlow for FP32 training
  - Up to 11% performance drop on ResNet101 and ResNet152 mixed precision training
  - Up to 10% performance drop on full FP16 VGG16 training
- Known Pascal performance regressions in 20.07 vs. 20.03 container:
  - Up to 19% performance drop on MaskRCNN for FP32 training
- When FFT Tiled algo are used with 3D convolution, an intermittent silent failure might happen due to dependency on the order of the stream execution. In some cases this might be manifested as NaNs in the output and we recommend to disable cuDNN via 
  ```python
  torch.backends.cudnn.enabled = False
  ```
- Channels-last memory format is experimental in the 20.07 container. Potential convergence issues for ResNet variants are being investigated. On NVIDIA Ampere architecture based GPUs unexpected NaN values due to a race condition in a cuDNN kernel might be observed. We recommend to use the default memory format in case you run into these issues.
Chapter 50. PyTorch Release 20.06

The NVIDIA container image for PyTorch, release 20.06, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- **Ubuntu 18.04** including **Python 3.6** environment
- **NVIDIA CUDA 11.0.167** including **cuBLAS 11.1.0**
- **NVIDIA cuDNN 8.0.1**
- **NVIDIA NCCL 2.7.5** (optimized for NVLink™)
- **APEX**
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- **PyProf r20.06**
- Tensor Core optimized examples:
  - **ResNeXt101-32x4d**
  - **SE-ResNext**
  - **TransformerXL**
  - **Jasper**
  - **BERT**
PyTorch Release 20.06

- Mask R-CNN
- Tacotron 2 and WaveGlow v1.1
- SSD300 v1.1
- Neural Collaborative Filtering (NCF)
- ResNet50 v1.5
- GNMT v2

Jupyter and JupyterLab:
- Jupyter Client 6.0.0
- Jupyter Core 4.6.1
- Jupyter Notebook 6.0.3
- JupyterLab 1.2.14
- JupyterLab Server 1.0.6
- Jupyter-TensorBoard

**Driver Requirements**

Release 20.06 is based on NVIDIA CUDA 11.0.167, which requires NVIDIA Driver release 450 or later. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 418.xx or 440.30. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

**GPU Requirements**

Release 20.06 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

**Key Features and Enhancements**

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.06 is based on 1.6.0a0+9907a3e
- The latest version of NVIDIA CUDA 11.0.167 including cuBLAS 11.1.0
- The latest version of NVIDIA NCCL 2.7.5
- The latest version of NVIDIA cuDNN 8.0.1
- The latest version of Nsight Compute 2020.1.0.33
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- The latest version of TensorRT 7.1.2
- The latest version of OpenMPI 3.1.6
- Ubuntu 18.04 with May 2020 updates
- Latest version of DALI 0.22
- Native Automatic Mixed Precision (torch.cuda.amp). See official API documentation and examples. torch.cuda.amp is intended as the future-proof replacement for Apex AMP, and offers a number of advantages.
- Integrated latest NVIDIA Deep Learning SDK to support NVIDIA A100 using CUDA 11 and cuDNN 8
- Various bug fixes for channels-last layout optimization. Note that this layout is still in experimental form. See Known Issues below.
- Performance improvements for various torch.distribution methods by switching to the TensorIterator implementation
- Default TF32 support for Ampere-based GPUs

Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

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<tr>
<td>20.06</td>
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<td>NVIDIA CUDA 11.0.167</td>
<td>1.6.0a0+9907a3e</td>
<td>TensorRT 7.1.2</td>
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<tr>
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Automatic Mixed Precision (AMP)

Automatic Mixed Precision (AMP) for PyTorch is available in this container through the native implementation as well as a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Apex AMP is included to support models that currently rely on it, but torch.cuda.amp is the future-proof alternative, and offers a number of advantages over Apex AMP.

Guidance and examples demonstrating torch.cuda.amp can be found here. Apex AMP examples can be found here.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.
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- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper *Jasper: An End-to-End Convolutional Neural Acoustic Model*. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model**. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model**. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model**. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model**. The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **NCF model**. The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user
interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

**Known Issues**

- There is up to 5% performance drop on Transformer-XL mixed precision training in the 20.01 container compared to 19.11. Disabling the profiling executor at the beginning of your script might reduce this effect via:
  ```python
  torch._C._jit_set_profiling_executor(False)
  torch._C._jit_set_profiling_mode(False)
  ```
- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via `torch.logdet(W.unsqueeze(0).float()).squeeze()` as is done in this release.
- Known Turing performance regressions in 20.06 vs. 20.03 container:
  - Up to 10% performance drop on InceptionV3 for mixed precision training
  - Up to 10% performance drop on MaskNCF FP32 training
  - Up to 25% performance drop on MaskRCNN FP32 training
  - Up to 10% performance drop on ResNet50 for mixed precision training.
- Known Volta performance regressions in 20.06 vs. 20.03 container:
  - Up to 30% performance drop on WaveGlow for FP32 training
  - Up to 11% performance drop on ResNet101 and ResNet152 mixed precision training
- Known Pascal performance regressions in 20.06 vs. 20.03 container:
  - Up to 19% performance drop on MaskRCNN for FP32 training
- When FFT Tiled algo are used with 3D convolution, an intermittent silent failure might happen due to dependency on the order of the stream execution. In some cases this might be manifested as NaNs in the output and we recommend to disable cuDNN via `torch.backends.cudnn.enabled = False`.
- Channels-last memory format is experimental in the 20.06 container. Potential convergence issues for ResNet variants are being investigated. On NVIDIA Ampere architecture based GPUs unexpected NaN values due to a race condition in a cuDNN.
kernel might be observed. We recommend to use the default memory format in case you run into these issues.
Chapter 51. PyTorch Release 20.03

The NVIDIA container image for PyTorch, release 20.03, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 18.04 including Python 3.6 environment
- NVIDIA CUDA 10.2.89 including cuBLAS 10.2.2.89
- NVIDIA cuDNN 7.6.5
- NVIDIA NCCL 2.6.3 (optimized for NVLink™)
- APEX
- MLNX_OFED
- OpenMPI 3.1.4
- TensorBoard 2.1.0
- Nsight Compute 2019.5.0
- Nsight Systems 2020.1.1
- TensorRT 7.0.0
- DALI 0.19.0
- MAGMA 2.5.2
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
  - SE-ResNext
  - TransformerXL
  - Jasper
  - BERT
  - Mask R-CNN
  - Tacotron 2 and WaveGlow v1.1
- SSD300 v1.1
- Neural Collaborative Filtering (NCF)
- ResNet50 v1.5
- GNMT v2

- Jupyter and JupyterLab:
  - Jupyter Client 6.0.0
  - Jupyter Core 4.6.1
  - Jupyter Notebook 6.0.3
  - JupyterLab 1.0.4
  - JupyterLab Server 1.0.6
  - Jupyter-TensorBoard

Driver Requirements

Release 20.03 is based on NVIDIA CUDA 10.2.89, which requires NVIDIA Driver release 440.33.01. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 396, 384.111+, 410, 418.xx or 440.30. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 20.03 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.03 is based on 1.5.0a0+8f84ded
- Latest version of DALI 0.19.0
- Performance improvements for elementwise operations
- Performance improvements for per-channel quantization
- Relaxation of cudnn batchnorm input shape requirements
- Ubuntu 18.04 with February 2020 updates

Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
Transformer has been removed.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

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Automatic Mixed Precision (AMP)

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Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.
Tensor Core Examples

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GNMT v2 model. The GNMT v2 model is similar to the one discussed in the [Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation](#) paper. This model script is available on [GitHub](#) as well as [NVIDIA GPU Cloud (NGC)](#).

Known Issues

There is up to 5% performance drop on Transformer-XL mixed precision training in the 20.01 container compared to 19.11. Disabling the profiling executor at the beginning of your script might reduce this effect via:

```python
torch._C._jit_set_profiling_executor(False)
torch._C._jit_set_profiling_mode(False)
```
- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via `torch.logdet(W.unsqueeze(0).float()).squeeze()` as is done in this release.
Chapter 52. PyTorch Release 20.02

The NVIDIA container image for PyTorch, release 20.02, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 18.04 including Python 3.6 environment
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Driver Requirements

Release 20.02 is based on NVIDIA CUDA 10.2.89, which requires NVIDIA Driver release 440.33.01. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 396, 384.111+, 410, 418.xx or 440.30. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 20.02 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 20.02 is based on PyTorch 1.4.0a0+a5b4d78 with a fix for wrong results in LU factorization using MAGMA<=2.5.1.
- Latest version of DALI 0.18.0 Beta
- Latest version of Nsight Systems 2020.1.1
- Latest version of Jupyter Notebook 6.0.3
- Ubuntu 18.04 with January 2020 updates
- Initial support for channel-last layout for convolutions
- Support for loop unrolling and vectorized loads and stores in TensorIterator
- Support for input activations with more than $2^{31}$ values
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.
- Transformer has been removed.

NVIDIA PyTorch Container Versions

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Automatic Mixed Precision (AMP)

NVIDIA’s Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.
Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

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- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

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- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper Jasper: An End-to-End Convolutional Neural Acoustic Model. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model.** BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor
Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

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- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper and a flow-based neural network model from the *WaveGlow: A Flow-based Generative Network for Speech Synthesis* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

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- **NCF model.** The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the *Neural Collaborative Filtering* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

### Known Issues

- There is up to 5% performance drop on Transformer-XL mixed precision training in the 20.01 container compared to 19.11. Disabling the profiling executor at the beginning of your script might reduce this effect via:

  ```python
  torch._C._jit_set_profiling_executor(False)
  ```
torch._C._jit_set_profiling_mode(False)

- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via `torch.logdet(W.unsqueeze(0).float()).squeeze()` as is done in this release.
The NVIDIA container image for PyTorch, release 20.01, is available on NGC.

**Contents of the PyTorch container**

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch/`) in the container image.

The container also includes the following:

- Ubuntu 18.04 including Python 3.6 environment
- NVIDIA CUDA 10.2.89 including cuBLAS 10.2.2.89
- NVIDIA cuDNN 7.6.5
- NVIDIA NCCL 2.5.6 (optimized for NVLink™)
- APEX
- MLNX_OFED
- OpenMPI 3.1.4
- TensorBoard 2.1.0
- Nsight Compute 2019.5.0
- Nsight Systems 2019.6.1
- TensorRT 7.0.0
- DALI 0.17.0 Beta
- MAGMA 2.5.2
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
  - SE-ResNext
  - TransformerXL
  - Jasper
  - BERT
  - Mask R-CNN
  - Tacotron 2 and WaveGlow v1.1
Driver Requirements

Release 20.01 is based on NVIDIA CUDA 10.2.89, which requires NVIDIA Driver release 440.33.01. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 396, 384.111+, 410, 418.xx or 440.30. The CUDA driver's compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 20.01 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 20.01 is based on PyTorch 1.4.0a0+a5b4d78 with a fix for wrong results in LU factorization using MAGMA<=2.5.1.
- Latest version of TensorRT 7.0.0
- Latest version of DALI 0.17.0 Beta
- Latest version of MAGMA 2.5.2
- Ubuntu 18.04 with December 2019 updates
Announcements

- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

NVIDIA PyTorch Container Versions

The following table shows what versions of Ubuntu, CUDA, PyTorch, and TensorRT are supported in each of the NVIDIA containers for PyTorch. For older container versions, refer to the Frameworks Support Matrix.

<table>
<thead>
<tr>
<th>Container Version</th>
<th>Ubuntu</th>
<th>CUDA Toolkit</th>
<th>PyTorch</th>
<th>TensorRT</th>
</tr>
</thead>
<tbody>
<tr>
<td>20.01</td>
<td>18.04</td>
<td>NVIDIA CUDA 10.2.89</td>
<td>1.4.0a0+a5b4d78</td>
<td>TensorRT 7.0.0</td>
</tr>
<tr>
<td>19.12</td>
<td>16.04</td>
<td>NVIDIA CUDA 10.0.2.43</td>
<td>1.4.0a0+174e1ba</td>
<td>TensorRT 6.0.1</td>
</tr>
<tr>
<td>19.11</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19.10</td>
<td></td>
<td>NVIDIA CUDA 10.0.2.43</td>
<td>1.3.0a0+24ae9b5</td>
<td></td>
</tr>
<tr>
<td>19.09</td>
<td></td>
<td></td>
<td>1.2.0</td>
<td></td>
</tr>
<tr>
<td>19.08</td>
<td></td>
<td></td>
<td>1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked</td>
<td>TensorRT 5.1.5</td>
</tr>
</tbody>
</table>

Automatic Mixed Precision (AMP)

NVIDIA's Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

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PyTorch Release 20.01

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- A workaround for the WaveGlow training regression from our past containers is to use a fake batch dimension when calculating the log determinant via
  ```python
  torch.logdet(W.unsqueeze(0).float()).squeeze()
  ``` as is done in this release.

- The mixed-precision recipe for Transformer training might create unexpectedly NaN outputs. We recommend using FP32 or AMP with `opt_level='O0'` with the 20.01 container.
Chapter 54. PyTorch Release 19.12

The NVIDIA container image for PyTorch, release 19.12, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

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  - **Tacotron 2 and WaveGlow v1.1**
  - **SSD300 v1.1**
Neural Collaborative Filtering (NCF)
Transformer
ResNet50 v1.5
GNMT v2
Jupyter and JupyterLab:
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   Jupyter Core 4.6.1
   Jupyter Notebook 6.0.2
   JupyterLab 1.0.4
   JupyterLab Server 1.0.6
   Jupyter-TensorBoard

Driver Requirements
Release 19.12 is based on NVIDIA CUDA 10.2.89, which requires NVIDIA Driver release 440.30. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 396, 384.111+, 410, 418.xx or 440.30. The CUDA driver's compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements
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Key Features and Enhancements
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**Known Issues**

- There is up to 70% performance drop on Waveglow training in the 19.12 container compared to 19.10; due to a magma-CUDA bug recreating cuBLAS handles. You can minimize this regression by setting the `CUBLAS_WORKSPACE_CONFIG=:16:8` environment variable before running the Waveglow code.

- There is up to 20% performance drop for SSD training on Pascal GPUs in the 19.12 container compared to 19.11.

- There is up to 8% performance drop on BERT Large mixed-precision training in the 19.12 container compared to 19.10.

- The mixed-precision recipe for Transformer training might create unexpectedly NaN outputs. We recommend to use FP32 or AMP with `opt_level=’O0’` with the 19.12 container.
Chapter 55. PyTorch Release 19.11

The NVIDIA container image for PyTorch, release 19.11, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

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Jupyter and JupyterLab:

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Key Features and Enhancements

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Latest version of NVIDIA CUDA 10.2.89 including cuBLAS 10.2.2.89

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Latest versions of Jupyter Client 5.3.4 and Jupyter Core 4.6.1
PyTorch Release 19.11

- Added a TransformerXL Tensor Core optimized example
- Ubuntu 18.04 with October 2019 updates

Announcements
- Deep learning framework containers 19.11 and later include experimental support for Singularity v3.0.

Automatic Mixed Precision (AMP)

NVIDIA's Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub and NVIDIA GPU Cloud (NGC) focus on achieving the best performance and convergence from NVIDIA Volta tensor cores by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta and Turing, therefore you can get results much faster than training without Tensor Cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNext** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

- **TransformerXL** model. Transformer-XL is a transformer-based language model with a segment-level recurrence and a novel relative positional encoding. Enhancements introduced in Transformer-XL help capture better long-term dependencies by...
attending to tokens from multiple previous segments. Our implementation is based on the codebase published by the authors of the Transformer-XL paper. Our implementation uses modified model architecture hyperparameters. Our modifications were made to achieve better hardware utilization and to take advantage of Tensor Cores. His model script is available on GitHub.

- **Jasper model.** This repository provides an implementation of the Jasper model in PyTorch from the paper *Jasper: An End-to-End Convolutional Neural Acoustic Model*. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model.** BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper and a flow-based neural network model from the *WaveGlow: A Flow-based Generative Network for Speech Synthesis* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the *SSD: Single Shot MultiBox Detector* paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **NCF model.** The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao,
Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Transformer model.** The Transformer model is based on the optimized implementation in Facebook’s Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

**Known Issues**

- The performance of Mask R-CNN in FP32 precision is up to 14% slower in the 19.11 container compared to the 19.06 release. For best performance on Mask R-CNN, it is recommended to use automatic mixed precision training. This can easily be done using the float16 option with the MaskRCNN example included in this container.

- There is up to 66% performance drop on WaveGlow training in the 19.11 container compared to 19.10.

- The mixed-precision recipe for Transformer training might create unexpectedly NaN outputs. We recommend to use FP32 or AMP with `opt_level='O0'` with the 19.11 container.

- There is a 3-15% performance drop on Tacotron2 inference in the 19.11 container compared to 19.09.
Chapter 56. PyTorch Release 19.10

The NVIDIA container image for PyTorch, release 19.10, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 18.04 including Python 3.6 environment
- NVIDIA CUDA 10.1.243 including cuBLAS 10.2.1.243
- NVIDIA cuDNN 7.6.4
- NVIDIA NCCL 2.4.8 (optimized for NVLink™)
- APEX
- MLNX_OFED
- OpenMPI 3.1.4
- TensorBoard 2.0.0
- Nsight Compute 2019.4.0
- Nsight Systems 2019.5.1
- TensorRT 6.0.1
- DALI 0.14.0 Beta
- Tensor Core optimized examples:
  - ResNeXt101-32x4d
  - SE-ResNext
  - Jasper
  - BERT
  - Mask R-CNN
  - Tacotron 2 and WaveGlow v1.1
  - SSD300 v1.1
  - Neural Collaborative Filtering (NCF)
- Transformer
- ResNet50 v1.5
- GNMT v2
- Jupyter and JupyterLab:
  - Jupyter Client 5.3.3
  - Jupyter Core 4.5.0
  - Jupyter Notebook 5.7.8
  - JupyterLab 1.0.4
  - JupyterLab Server 1.0.6
  - Jupyter-TensorBoard

Driver Requirements

Release 19.10 is based on NVIDIA CUDA 10.1.243, which requires NVIDIA Driver release 418.xx. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 396, 384.111+ or 410. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.10 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 19.10 is based on PyTorch 1.3.0a0+24ae9b5.
- Latest version of NVIDIA cuDNN 7.6.4
- Latest version of TensorBoard 2.0.0
- Latest version of DALI 0.14.0 Beta
- Latest version of Nsight Systems 2019.5.1
- Latest version of Jupyter Client 5.3.3
- Added the ResNeXt101-32x4d and SE-ResNext Tensor Core optimized model examples.
- Ubuntu 18.04 with September 2019 updates
Automatic Mixed Precision (AMP)

NVIDIA's Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.

Tensor Core Examples

The tensor core examples provided in GitHub focus on achieving the best performance and convergence by using the latest deep learning example networks and model scripts for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **ResNeXt101-32x4d** model. The ResNeXt101-32x4d is a model introduced in the Aggregated Residual Transformations for Deep Neural Networks paper. It is based on regular ResNet model, substituting 3x3 convolutions inside the bottleneck block for 3x3 grouped convolutions. This model script is available on GitHub.

- **SE-ResNeXt** model. The SE-ResNeXt101-32x4d is a ResNeXt101-32x4d model with added Squeeze-and-Excitation (SE) module introduced in the Squeeze-and-Excitation Networks paper. This model script is available on GitHub.

- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper Jasper: An End-to-End Convolutional Neural Acoustic Model. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model**. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model
is based on the **BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding** paper. NVIDIA's implementation of BERT is an optimized version of the **Hugging Face implementation**, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on **GitHub** as well as **NVIDIA GPU Cloud (NGC)**.

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found [here](https://arxiv.org/abs/1708.02217). NVIDIA's Mask R-CNN model is an optimized version of Facebook's implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on **GitHub** as well as **NVIDIA GPU Cloud (NGC)**.

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the **Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions** paper and a flow-based neural network model from the **WaveGlow: A Flow-based Generative Network for Speech Synthesis** paper. This model script is available on **GitHub** as well as **NVIDIA GPU Cloud (NGC)**.

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the **SSD: Single Shot MultiBox Detector** paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on **GitHub** as well as **NVIDIA GPU Cloud (NGC)**.

- **NCF model.** The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the **Neural Collaborative Filtering** paper. This model script is available on **GitHub** as well as **NVIDIA GPU Cloud (NGC)**.

- **Transformer model.** The Transformer model is based on the optimized implementation in **Facebook's Fairseq NLP Toolkit** and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB. This model script is available on **GitHub** as well as **NVIDIA GPU Cloud (NGC)**.

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a modified version of the original **ResNet50 v1 model.** This model script is available on **GitHub** as well as **NVIDIA GPU Cloud (NGC)**.
- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the *Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation* paper. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia-cuda.github.io/).  

**Known Issues**

- Performance of Mask R-CNN in FP32 precision is up to 20% slower in the 19.10 container compared to the 19.06 release. For best performance on Mask R-CNN, it is recommended to use automatic mixed precision training. This can easily be done using the float16 option with the MaskRCNN example included in this container.

- There is a 15-20% performance drop on WaveGlow inference in the 19.10 container compared to 19.08 using automatic mixed precision (AMP) with V100 compared to previous releases. To workaround this issue, install cuDNN 7.6.2 or use the 19.08 container.

- The mixed-precision recipe for Transformer training might create unexpectedly NaN outputs. We recommend to use FP32 or AMP with `opt_level='O0'` with the 19.10 container.

- There is a 3-15% performance drop on Tacotron2 inference in the 19.10 container compared to the previous release.
Chapter 57. PyTorch Release 19.09

The NVIDIA container image for PyTorch, release 19.09, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 18.04 including Python 3.6 environment
- NVIDIA CUDA 10.1.243 including cuBLAS 10.2.1.243
- NVIDIA cuDNN 7.6.3
- NVIDIA NCCL 2.4.8 (optimized for NVLink™)
- APEX
- MLNX_OFED
- OpenMPI 3.1.4
- TensorBoard 1.14.0+nv
- Nsight Compute 2019.4.0
- Nsight Systems 2019.4.2
- TensorRT 6.0.1
- DALI 0.12.0 Beta
- Tensor Core optimized examples:
  - Jasper
  - BERT
  - Mask R-CNN
  - Tacotron 2 and WaveGlow v1.1
  - SSD300 v1.1
  - Neural Collaborative Filtering (NCF)
  - Transformer
  - ResNet50 v1.5
Driver Requirements

Release 19.09 is based on NVIDIA CUDA 10.1.243, which requires NVIDIA Driver release 418.xx. However, if you are running on Tesla (for example, T4 or any other Tesla board), you may use NVIDIA driver release 396, 384.111+ or 410. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.09 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 19.09 is based on PyTorch 1.2.0.
- Latest version of NVIDIA cuDNN 7.6.3
- Latest versions of Nsight Compute 2019.4.0 and Nsight Systems 2019.4.2
- Latest version of TensorRT 6.0.1
- Latest version of JupyterLab Server 1.0.6
- Ubuntu 18.04 with August 2019 updates

Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against
each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **Jasper** model. This repository provides an implementation of the Jasper model in PyTorch from the paper **Jasper: An End-to-End Convolutional Neural Acoustic Model**. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.github.io).

- **BERT** model. BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the **BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding** paper. NVIDIA’s implementation of BERT is an optimized version of the [Hugging Face implementation](https://github.com/huggingface/transformers), leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.github.io).

- **Mask R-CNN** model. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found [here](https://arxiv.org/abs/1708.02982). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.github.io).

- **Tacotron 2 and WaveGlow v1.1** model. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the **Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions** paper and a flow-based neural network model from the **WaveGlow: A Flow-based Generative Network for Speech Synthesis** paper. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.github.io).

- **SSD300 v1.1** model. The SSD300 v1.1 model is based on the **SSD: Single Shot MultiBox Detector** paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.github.io).

- **NCF** model. The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the **Neural Collaborative Filtering** paper. This model script is available on [GitHub](https://github.com) as well as [NVIDIA GPU Cloud (NGC)](https://nvidia.github.io).
Transformer model. The Transformer model is based on the optimized implementation in Facebook's Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

ResNet50 v1.5 model. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

Automatic Mixed Precision (AMP)

NVIDIA's Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.

Known Issues

Performance of Mask R-CNN in FP32 precision is up to 20% slower in the 19.07 container compared to the previous release. For best performance on Mask R-CNN, it is recommended to use automatic mixed precision training. This can easily be done using the float16 option with the MaskRCNN example included in this container.

Due to recent changes on batch norm multiplier initialization (PyTorch commit: c60465873c5cf8f1a36da39f7875224d4c48d7ca), all batch norm multiplier is initialized as constant 1, instead of uniformly distributed between 0 and 1, as it was previously. This has caused accuracy issue for our TACOTRON2 model. If similar accuracy regression is observed during an update from 19.06 to 19.08, we recommend to re-initialize the batch norm multiplier using uniformed distribution. This could be done by passing your model to the following function:
```python
def init_bn(module):
    if isinstance(module, torch.nn.modules.batchnorm._BatchNorm):
        if module.affine:
            module.weight.data.uniform_()
        for child in module.children():
            init_bn(child)
```

- There is a 34-60% performance drop on WaveGlow training in the 19.09 container on 16 GPU systems using mixed precision training compared to previous releases.

- There is a 15-20% performance drop on WaveGlow inference in the 19.09 container using automatic mixed precision (AMP) with V100 compared to previous releases. To workaround this issue, install cuDNN 7.6.2 or use the 19.08 container.

- Nsight Compute is currently located in `/opt/nvidia/nsight-compute/2019.4.0`, while Nsight Systems can be found in `/usr/local/cuda-10.1/NsightSystems-cli-2019.4.2/bin/nsys`. 
Chapter 58. PyTorch Release 19.08

The NVIDIA container image for PyTorch, release 19.08, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch/`) in the container image.

The container also includes the following:

- **Ubuntu 18.04** including **Python 3.6** environment
- **NVIDIA CUDA 10.1.243** including **cuBLAS 10.2.1.243**
- **NVIDIA cuDNN 7.6.2**
- **NVIDIA NCCL 2.4.8** (optimized for NVLink™)
- **APEX**
- **MLNX_OFED +4.0**
- **OpenMPI 3.1.4**
- **TensorBoard 1.14.0+nv**
- **Nsight Compute 10.1.168**
- **Nsight Systems 2019.3.7.9**
- **TensorRT 5.1.5**
- **DALI 0.12.0 Beta**
- Tensor Core optimized examples:
  - **Jasper**
  - **BERT**
  - **Mask R-CNN**
  - **Tacotron 2 and WaveGlow v1.1**
  - **SSD300 v1.1**
  - **Neural Collaborative Filtering (NCF)**
  - **Transformer**
  - **ResNet50 v1.5**
Driver Requirements

Release 19.08 is based on NVIDIA CUDA 10.1.243, which requires NVIDIA Driver release 418.87. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384.111+ or 410. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.08 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 19.08 is based on PyTorch 1.2.0a0 including upstream commits up through commit 9130ab38 from July 31, 2019 as well as a cherry-picked performance fix 9462ca29.
- Latest version of NVIDIA CUDA 10.1.243 including cuBLAS 10.2.1.243
- Latest version of NVIDIA cuDNN 7.6.2
- Latest version of NVIDIA NCCL 2.4.8
- Latest version of DALI 0.12.0 Beta
- Latest version of OpenMPI 3.1.4
- Latest version of Nsight Systems 2019.3.7.9
- Latest version of MLNX_OFED +4.0
- Added a Jasper Tensor Core model script example
- Ubuntu 18.04 with July 2019 updates
Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **Jasper model.** This repository provides an implementation of the Jasper model in PyTorch from the paper *Jasper: An End-to-End Convolutional Neural Acoustic Model*. The Jasper model is an end-to-end neural acoustic model for automatic speech recognition (ASR) that provides near state-of-the-art results on LibriSpeech among end-to-end ASR models without any external data. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **BERT model.** BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the *BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding* paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the *Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions* paper and a flow-based neural network model from the *WaveGlow: A Flow-based Generative Network for Speech Synthesis* paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the *SSD: Single Shot MultiBox Detector* paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).
NCF model. The Neural Collaborative Filtering (NCF) model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

Transformer model. The Transformer model is based on the optimized implementation in Facebook’s Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

ResNet50 v1.5 model. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper. This model script is available on GitHub as well as NVIDIA GPU Cloud (NGC).

Automatic Mixed Precision (AMP)

NVIDIA’s Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation. For more information about AMP, see the Training With Mixed Precision Guide.

Known Issues

Performance of Mask R-CNN in FP32 precision is up to 20% slower in the 19.07 container compared to the previous release. For best performance on Mask R-CNN,
it is recommended to use automatic mixed precision training. This can easily be done using the float16 option with the MaskRCNN example included in this container.

- Due to recent changes on batch norm multiplier initialization (PyTorch commit: c60465873c5cf8f1a36da39f7875224d4c48d7ca), all batch norm multiplier is initialized as constant 1, instead of uniformly distributed between 0 and 1, as it was previously. This has caused accuracy issue for our TACOTRON2 model. If similar accuracy regression is observed during an update from 19.06 to 19.08, we recommend to re-initialize the batch norm multiplier using uniformed distribution. This could be done by passing your model to the following function:

```python
def init_bn(module):
    if isinstance(module, torch.nn.modules.batchnorm._BatchNorm):
        if module.affine:
            module.weight.data.uniform_
        for child in module.children():
            init_bn(child)
```
Chapter 59. PyTorch Release 19.07

The NVIDIA container image for PyTorch, release 19.07, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch/`) in the container image.

The container also includes the following:

- **Ubuntu 18.04** including **Python 3.6** environment
- **NVIDIA CUDA 10.1.168** including **cuBLAS 10.2.0.168**
- **NVIDIA cuDNN 7.6.1**
- **NVIDIA NCCL 2.4.7** (optimized for NVLink™)
- **APEX**
- **MLNX_OFED +3.4**
- **OpenMPI 3.1.3**
- **TensorBoard 1.14.0+nv**
- **TensorRT 5.1.5**
- **DALI 0.11.0 Beta**

- Tensor Core optimized examples:
  - **BERT**
  - **Mask R-CNN**
  - **Tacotron 2 and WaveGlow v1.1**
  - **SSD300 v1.1**
  - **Neural Collaborative Filtering (NCF)**
  - **Transformer**
  - **ResNet50 v1.5**
  - **GNMT v2**

- Jupyter and JupyterLab:
Driver Requirements

Release 19.07 is based on NVIDIA CUDA 10.1.168, which requires NVIDIA Driver release 418.67. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384.111+ or 410. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.07 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 19.07 is based on PyTorch 1.2.0a0 including upstream commits up through commit f6aac41 from June 19, 2019.
- Latest version of NVIDIA cuDNN 7.6.1
- Latest version of MLNX_OFED +3.4
- Added TensorBoard 1.14.0+nv to the container.
- Latest versions of Jupyter Client 5.3.1, Jupyter Core 4.5.0, JupyterLab 1.0.2 and JupyterLab Server 1.0.0, including Jupyter-TensorBoard integration.
- Latest version of DALI 0.11.0 Beta
- Latest version of Ubuntu 18.04

Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against...
each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- **BERT model.** BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy.

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper.

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model.

- **NCF model.** The Neural Collaborative Filtering (NCF) focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

- **Transformer model.** The Transformer model is based on the optimized implementation in Facebook’s Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB.

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a slightly modified version of the original ResNet50 v1 model that trains to a greater accuracy.

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
Automatic Mixed Precision (AMP)

NVIDIA’s Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.

Known Issues

- Performance of Mask R-CNN in FP32 precision is up to 20% slower in the 19.07 container compared to the previous release. For best performance on Mask R-CNN, it is recommended to use automatic mixed precision training. This can easily be done using the float16 option with the MaskRCNN example included in this container.

- Due to recent changes on batch norm multiplier initialization (PyTorch commit: c60465873c5cf8f1a36da39f7875224d4c48d7ca), all batch norm multiplier is initialized as constant 1, instead of uniformly distributed between 0 and 1, as it was previously. This has caused accuracy issue for our TACOTRON2 model. If similar accuracy regression is observed during an update from 19.06 to 19.07, we recommend to re-initialize the batch norm multiplier using uniformed distribution. This could be done by passing your model to the following function:

```python
def init_bn(module):
    if isinstance(module, torch.nn.modules.batchnorm._BatchNorm):
        if module.affine:
            module.weight.data.uniform_()
        for child in module.children():
            init_bn(child)```
Chapter 60. PyTorch Release 19.06

The NVIDIA container image for PyTorch, release 19.06, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch/`) in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.1.168** including **cuBLAS 10.2.0.168**
- **NVIDIA cuDNN 7.6.0**
- **NVIDIA NCCL 2.4.7** (optimized for **NVLink™**)
- **APEX**
- **OpenMPI 3.1.3**
- **TensorRT 5.1.5**
- **DALI 0.10.0 Beta**
- Tensor Core optimized examples:
  - **BERT**
  - **Mask R-CNN**
  - **Tacotron 2 and WaveGlow v1.1**
  - **SSD300 v1.1**
  - **Neural Collaborative Filtering (NCF)**
  - **Transformer**
  - **ResNet50 v1.5**
  - **GNMT v2**
- Jupyter and JupyterLab:
  - **Jupyter Client 5.2.4**
  - **Jupyter Core 4.4.0**
Driver Requirements

Release 19.06 is based on NVIDIA CUDA 10.1.168, which requires NVIDIA Driver release 418.xx. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384.111+ or 410. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.06 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 19.06 is based on PyTorch 1.1.0commit 0885dd28 from May 28, 2019
‣ Added the BERT Tensor Core example
‣ Latest version of NVIDIA CUDA 10.1.168 including cuBLAS 10.2.0.168
‣ Latest version of NVIDIA NCCL 2.4.7
‣ Latest version of DALI 0.10.0 Beta
‣ Latest version of JupyterLab 0.35.6
‣ Ubuntu 16.04 with May 2019 updates (see Announcements)

Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

‣ BERT model, BERT, or Bidirectional Encoder Representations from Transformers, is a new method of pre-training language representations which obtains state-of-the-
art results on a wide array of Natural Language Processing (NLP) tasks. This model is based on the BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding paper. NVIDIA’s implementation of BERT is an optimized version of the Hugging Face implementation, leveraging mixed precision arithmetic and Tensor Cores on V100 GPUs for faster training times while maintaining target accuracy.

- **Mask R-CNN model.** Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found [here](#). NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy.

- **Tacotron 2 and WaveGlow v1.1 model.** This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper.

- **SSD300 v1.1 model.** The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model.

- **NCF model.** The Neural Collaborative Filtering (NCF) focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

- **Transformer model.** The Transformer model is based on the optimized implementation in Facebook’s Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB.

- **ResNet50 v1.5 model.** The ResNet50 v1.5 model is a slightly modified version of the original ResNet50 v1 model that trains to a greater accuracy.

- **GNMT v2 model.** The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
Automatic Mixed Precision (AMP)

NVIDIA’s Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 3 lines of Python to an existing FP32 (default) script. Amp will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.

Announcements

In the next release, we will no longer support Ubuntu 16.04. Release 19.07 will instead support Ubuntu 18.04.

Known Issues

- There is a known issue when running certain tests in PyTorch 19.06 on systems with Skylake CPUs, such as DGX-2, that is due to OpenBLAS version 0.3.6. If you are impacted, run:

  ```bash
  /opt/conda/bin/conda install openblas!=0.3.6
  ```
Chapter 61. PyTorch Release 19.05

The NVIDIA container image for PyTorch, release 19.05, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 16.04 including Python 3.6 environment
- NVIDIA CUDA 10.1 Update 1 including cuBLAS 10.1 Update 1
- NVIDIA cuDNN 7.6.0
- NVIDIA NCCL 2.4.6 (optimized for NVLink™)
- APEX
- OpenMPI 3.1.3
- TensorRT 5.1.5
- DALI 0.9.1 Beta
- Tensor Core optimized examples:
  - Mask R-CNN
  - Tacotron 2 and WaveGlow v1.1
  - SSD300 v1.1
  - Neural Collaborative Filtering (NCF)
  - Transformer
  - ResNet50 v1.5
  - GNMT v2
- Jupyter and JupyterLab:
  - Jupyter Client 5.2.4
  - Jupyter Core 4.4.0
  - JupyterLab 0.35.4
Driver Requirements

Release 19.05 is based on CUDA 10.1 Update 1, which requires NVIDIA Driver release 418.xx. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384.111+ or 410. The CUDA driver's compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.05 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 19.05 is based on PyTorch 1.0.1 commit 828a6a3b from March 31, 2019
- Latest version of NVIDIA CUDA 10.1 Update 1 including cuBLAS 10.1 Update 1
- Latest version of NVIDIA cuDNN 7.6.0
- Latest version of TensorRT 5.1.5
- Latest version of DALI 0.9.1 Beta
- Ubuntu 16.04 with April 2019 updates

Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- An implementation of the Mask R-CNN model. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy.
An implementation of the Tacotron 2 and WaveGlow v1.1 model. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper.

An implementation of the SSD300 v1.1 model. The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model.

An implementation of the Neural Collaborative Filtering (NCF) model. The NCF model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

An implementation of the Transformer model architecture. The Transformer model is based on the optimized implementation in Facebook's Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB.

An implementation of the ResNet50 model. The ResNet50 v1.5 model is a slightly modified version of the original ResNet50 v1 model that trains to a greater accuracy.

An implementation of the GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.

Automatic Mixed Precision (AMP)

NVIDIA's Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 2 lines of Python to an existing FP32 (default) script. AMP will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.
Known Issues

- Persistent batch normalization kernels are enabled by default in this build. This will provide a performance boost to many networks, but in rare cases may cause a network to fail to train properly. We expect to address this in the 19.06 container.

The NVIDIA container image for PyTorch, release 19.04, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in Conda default environment (`/opt/conda/lib/python3.6/site-packages/torch`) in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.1.105** including **cuBLAS 10.1.0.105**
- **NVIDIA cuDNN 7.5.0**
- **NVIDIA NCCL 2.4.6** (optimized for NVLink™)
- **APEX**
- **OpenMPI 3.1.3**
- **TensorRT 5.1.2**
- **DALI 0.8.1 Beta**
- Tensor Core optimized examples:
  - **Mask R-CNN**
  - **Tacotron 2 and WaveGlow v1.1**
  - **SSD300 v1.1**
  - **Neural Collaborative Filtering (NCF)**
  - **Transformer**
  - **ResNet50 v1.5**
  - **GNMT v2**
- Jupyter and JupyterLab:
  - **Jupyter Client 5.2.4**
  - **Jupyter Core 4.4.0**
  - **JupyterLab 0.35.4**
JupyterLab Server 0.2.0

Driver Requirements
Release 19.04 is based on CUDA 10.1, which requires NVIDIA Driver release 418.xx.x. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384.111+ or 410. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements
Release 19.04 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements
This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 19.04 is based on PyTorch 1.0.1 commit 9eb0f43 from March 28, 2019
‣ Latest version of NVIDIA NCCL 2.4.6
‣ Latest version of DALI 0.8.1 Beta
‣ Latest version of cuBLAS 10.1.0.105
‣ Added the Mask R-CNN Tensor Core example
‣ Ubuntu 16.04 with March 2019 updates

Tensor Core Examples
These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

‣ An implementation of the Mask R-CNN model. Mask R-CNN is a convolution based neural network for the task of object instance segmentation. The paper describing the model can be found here. NVIDIA’s Mask R-CNN model is an optimized version of Facebook’s implementation, leveraging mixed precision arithmetic using Tensor Cores on NVIDIA Tesla V100 GPUs for 1.3x faster training time while maintaining target accuracy.
An implementation of the Tacotron 2 and WaveGlow v1.1 model. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper.

An implementation of the SSD300 v1.1 model. The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model.

An implementation of the Neural Collaborative Filtering (NCF) model. The NCF model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

An implementation of the Transformer model architecture. The Transformer model is based on the optimized implementation in Facebook’s Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB.

An implementation of the ResNet50 model. The ResNet50 v1.5 model is a slightly modified version of the original ResNet50 v1 model that trains to a greater accuracy.

An implementation of the GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.

Automatic Mixed Precision (AMP)

NVIDIA’s Automatic Mixed Precision (AMP) for PyTorch is available in this container through a preinstalled release of Apex. AMP enables users to try mixed precision training by adding only 2 lines of Python to an existing FP32 (default) script. AMP will choose an optimal set of operations to cast to FP16. FP16 operations require 2X reduced memory bandwidth (resulting in a 2X speedup for bandwidth-bound operations like most pointwise ops) and 2X reduced memory storage for intermediates (reducing the overall memory consumption of your model). Additionally, GEMMs and convolutions with FP16 inputs can run on Tensor Cores, which provide an 8X increase in computational throughput over FP32 arithmetic.

Comprehensive guidance and examples demonstrating AMP for PyTorch can be found in the documentation.

For more information about AMP, see the Training With Mixed Precision Guide.
Known Issues

- Persistent batch normalization kernels are enabled by default in this build. This will provide a performance boost to many networks, but in rare cases may cause a network to fail to train properly. We expect to address this in the 19.05 container.
Chapter 63. PyTorch Release 19.03

The NVIDIA container image for PyTorch, release 19.03, is available on NGC.

Contents of the PyTorch container

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.1.105** including **cuBLAS 10.1.105**
- **NVIDIA cuDNN 7.5.0**
- **NVIDIA NCCL 2.4.3** (optimized for NVLink™)
- **APEX**
- **OpenMPI 3.1.3**
- **TensorRT 5.1.2**
- **DALI 0.7 Beta**
- Tensor Core optimized examples:
  - **Tacotron 2 and WaveGlow v1.1**
  - **SSD300 v1.1**
  - **Neural Collaborative Filtering (NCF)**
  - **Transformer**
  - **ResNet50 v1.5**
  - **GNMT v2**
- Jupyter and JupyterLab:
  - **Jupyter Client 5.2.4**
  - **Jupyter Core 4.4.0**
  - **JupyterLab 0.35.4**
  - **JupyterLab Server 0.2.0**
Driver Requirements

Release 19.03 is based on CUDA 10.1, which requires NVIDIA Driver release 418.xx+. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384.111+ or 410. The CUDA driver’s compatibility package only supports particular drivers. For a complete list of supported drivers, see the CUDA Application Compatibility topic. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.03 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 19.03 is based on PyTorch commit 81e025d from March 9th, 2019
- Latest version of NVIDIA CUDA 10.1.105 including cuBLAS 10.1.105
- Latest version of NVIDIA cuDNN 7.5.0
- Latest version of NVIDIA NCCL 2.4.3
- Latest version of DALI 0.7 Beta
- Latest version of TensorRT 5.1.2
- Added the Tacotron 2 and WaveGlow v1.1 and SSD300 v1.1 Tensor Core examples
- Ubuntu 16.04 with February 2019 updates

Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training.

Each example model trains with mixed precision Tensor Cores on Volta, therefore you can get results much faster than training without tensor cores. This model is tested against each NGC monthly container release to ensure consistent accuracy and performance over time. This container includes the following tensor core examples.

- An implementation of the Tacotron 2 and WaveGlow v1.1 model. This text-to-speech (TTS) system is a combination of two neural network models: a modified Tacotron 2 model from the Natural TTS Synthesis by Conditioning WaveNet on Mel Spectrogram Predictions paper and a flow-based neural network model from the WaveGlow: A Flow-based Generative Network for Speech Synthesis paper.
An implementation of the SSD300 v1.1 model. The SSD300 v1.1 model is based on the SSD: Single Shot MultiBox Detector paper. The main difference between this model and the one described in the paper is in the backbone. Specifically, the VGG model is obsolete and is replaced by the ResNet50 model.

An implementation of the Neural Collaborative Filtering (NCF) model. The NCF model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

An implementation of the Transformer model architecture. The Transformer model is based on the optimized implementation in Facebook’s Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB.

An implementation of the ResNet50 model. The ResNet50 v1.5 model is a slightly modified version of the original ResNet50 v1 model that trains to a greater accuracy.

An implementation of the GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.

Known Issues

Persistent batch normalization kernels have been disabled due to a known bug during validation. Batch normalization provides correct results and work as expected from users, however, this may cause up to 10% regression in time to solution performance on networks using batch normalization.

If using or upgrading to a 3-part-version driver, for example, a driver that takes the format of xxx.yy.zz, you will receive a Failed to detect NVIDIA driver version. message. This is due to a known bug in the entry point script’s parsing of 3-part driver versions. This message is non-fatal and can be ignored. This will be fixed in the 19.04 release.
Chapter 64. PyTorch Release 19.02

The NVIDIA container image for PyTorch, release 19.02, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in Conda™ default environment (/opt/conda/lib/python3.6/site-packages/torch/) in the container image.

The container also includes the following:

- Ubuntu 16.04 including Python 3.6 environment
- NVIDIA CUDA 10.0.130 including CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 10.0.130
- NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.4.2
- NVIDIA Collective Communications Library (NCCL) 2.3.7 (optimized for NVLink™)
- APEX
- OpenMPI 3.1.3
- TensorRT 5.0.2
- DALI 0.6.1 Beta
- Tensor Core optimized examples:
  - Neural Collaborative Filtering (NCF)
  - Transformer
  - ResNet50 v1.5
  - GNMT v2
- Jupyter and JupyterLab:
  - Jupyter Client 5.2.4
  - Jupyter Core 4.4.0
  - JupyterLab 0.35.4
  - JupyterLab Server 0.2.0
Driver Requirements

Release 19.02 is based on CUDA 10, which requires NVIDIA Driver release 410.xx. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384. For more information, see CUDA Compatibility and Upgrades.

GPU Requirements

Release 19.02 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- PyTorch container image version 19.02 is based on PyTorch Version 1.1.0a0+c42431b.
- Latest version of DALI 0.6.1 Beta
- Added Jupyter and JupyterLab software in our packaged container.
- Latest version of jupyter_client 5.2.4
- Latest version of jupyter_core 4.4.0
- Ubuntu 16.04 with January 2019 updates

Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training. This container includes the following Tensor Core examples.

- An implementation of the Neural Collaborative Filtering (NCF) model. The NCF model focuses on providing recommendations, also known as collaborative filtering, with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

- An implementation of the Transformer model architecture. The Transformer model is based on the optimized implementation in Facebook's Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB.
An implementation of the ResNet50 model. The ResNet50 v1.5 model is a slightly modified version of the original ResNet50 v1 model that trains to a greater accuracy.

An implementation of the GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.

Known Issues

- Persistent batch normalization kernels have been disabled due to a known bug during validation. Batch normalization provides correct results and work as expected from users, however, this may cause up to 10% regression in time to solution performance on networks using batch normalization.

- If using or upgrading to a 3-part-version driver, for example, a driver that takes the format of xxx.yy.zz, you will receive a Failed to detect NVIDIA driver version message. This is due to a known bug in the entry point script’s parsing of 3-part driver versions. This message is non-fatal and can be ignored. This will be fixed in the 19.04 release.
Chapter 65. PyTorch Release 19.01

The NVIDIA container image for PyTorch, release 19.01, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in the `pytorch-py3.6` Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.0.130** including CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 10.0.130
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.4.2**
- **NCCL** 2.3.7 (optimized for NVLink™)
- **OpenMPI 3.1.3**
- **Caffe2**
- **TensorRT 5.0.2**
- **DALI 0.6 Beta**
- Tensor Core optimized examples:
  - **Neural Collaborative Filtering (NCF)**
  - **Transformer**
  - **ResNet50 v1.5**
  - **GNMT v2**

Driver Requirements

Release 19.01 is based on CUDA 10, which requires NVIDIA Driver release 410.xx. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384. For more information, see CUDA Compatibility and Upgrades.
GPU Requirements

Release 19.01 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 19.01 is based on PyTorch v1.0.0 with up-to-date features.
- Latest version of **DALI 0.6 Beta**
- Latest version of **NVIDIA cuDNN 7.4.2**
- Latest version of **OpenMPI 3.1.3**
- Added the Neural Collaborative Filtering (NCF) and Transformer Tensor Core examples.
- Ubuntu 16.04 with December 2018 updates

Tensor Core Examples

These examples focus on achieving the best performance and convergence from NVIDIA Volta Tensor Cores by using the latest deep learning example networks for training. This container includes the following Tensor Core examples.

- An implementation of the Neural Collaborative Filtering (NCF) model. The NCF model focuses on providing recommendations, also known as collaborative filtering; with implicit feedback. The training data for this model should contain binary information about whether a user interacted with a specific item. NCF was first described by Xiangnan He, Lizi Liao, Hanwang Zhang, Liqiang Nie, Xia Hu and Tat-Seng Chua in the Neural Collaborative Filtering paper.

- An implementation of the Transformer model architecture. The Transformer model is based on the optimized implementation in Facebook's Fairseq NLP Toolkit and is built on top of PyTorch. The original version in the Fairseq project was developed using Tensor Cores, which provides significant training speedup. Our implementation improves the performance and is tested on a DGX-1V 16GB.

- An implementation of the ResNet50 model. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model.

- An implementation of the GNMT v2 model. The GNMT v2 model is similar to the one discussed in the Google's Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
Known Issues

- Persistent batch normalization kernels have been disabled due to a known bug during validation. Batch normalization provides correct results and work as expected from users, however, this may cause up to 10% regression in time to solution performance on networks using batch normalization.

- If using or upgrading to a 3-part-version driver, for example, a driver that takes the format of `xxx.yy.zz`, you will receive a `Failed to detect NVIDIA driver version.` message. This is due to a known bug in the entry point script’s parsing of 3-part driver versions. This message is non-fatal and can be ignored. This will be fixed in the 19.04 release.
Chapter 66. PyTorch Release 18.12

The NVIDIA container image for PyTorch, release 18.12, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py3.6 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.0.130** including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS)** 10.0.130
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN)** 7.4.1
- **NCCL** 2.3.7 (optimized for **NVLink™**)
- **APEX**
- **OpenMPI 3.1.2**
- **Caffe2**
- **TensorRT 5.0.2**
- **DALI 0.5.0 Beta**
- Tensor Core Optimized Examples:
  - **ResNet50 v1.5**
  - **GNMT v2**

Driver Requirements

Release 18.12 is based on CUDA 10, which requires **NVIDIA Driver** release 410.xx. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384. For more information, see **CUDA Compatibility and Upgrades**.
GPU Requirements

Release 18.12 supports CUDA compute capability 6.0 and higher. This corresponds to GPUs in the Pascal, Volta, and Turing families. Specifically, for a list of GPUs that this compute capability corresponds to, see CUDA GPUs. For additional support details, see Deep Learning Frameworks Support Matrix.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ PyTorch container image version 18.12 is based on PyTorch v0.4.1+ with up-to-date features from the PyTorch v1.0 preview (main branch up to PR 12303). PyTorch 0.4.1+ is released and included with this container.
‣ Performance improvement for PyTorch’s native batch normalization.
‣ Mixed precision SoftMax enabling FP16 inputs, FP32 computations and FP32 outputs.
‣ Latest version of DALI 0.5.0 Beta.
‣ Ubuntu 16.04 with November 2018 updates

Tensor Core Examples

‣ An implementation of ResNet50. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model.
‣ An implementation of GNMT v2. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.

Known Issues

Persistent batch normalization kernels have been disabled due to a known bug during validation. Batch normalization provides correct results and work as expected from users, however, this may cause up to 10% regression in time to solution performance on networks using batch normalization.
Chapter 67. PyTorch Release 18.11

The NVIDIA container image for PyTorch, release 18.11, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py3.6 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.0.130** including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 10.0.130**
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.4.1**
- **NCCL 2.3.7** (optimized for **NVLink™**)
- **APEX**
- **OpenMPI 3.1.2**
- **Caffe2**
- **TensorRT 5.0.2**
- **DALI 0.4.1 Beta**
- Tensor Core Optimized Examples:
  - **ResNet50 v1.5**
  - **GNMT v2**

Driver Requirements

Release 18.11 is based on CUDA 10, which requires **NVIDIA Driver** release 410.xx.

However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384. For more information, see **CUDA Compatibility and Upgrades**.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.
- **PyTorch** container image version 18.11 is based on PyTorch v0.4.1+ with up-to-date features from the PyTorch v1.0 preview (main branch up to PR 11834). PyTorch 0.4.1+ is released and included with this container.
- Latest version of **NCCL 2.3.7**.
- Latest version of **NVIDIA cuDNN 7.4.1**.
- Latest version of **TensorRT 5.0.2**.
- Latest version of **DALI 0.4.1 Beta**.
- Ubuntu 16.04 with October 2018 updates

**Tensor Core Examples**

- An implementation of ResNet50. The **ResNet50 v1.5 model** is a modified version of the original **ResNet50 v1 model**.
- An implementation of GNMT v2. The **GNMT v2 model** is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.

**Known Issues**

There is a known bug when using persistent batch normalization kernels. If you are experiencing a drop in predictive power during testing and validation, the recommended workaround is to not add the .eval() flag on your model when doing testing or validation.
Chapter 68. PyTorch Release 18.10

The NVIDIA container image of PyTorch, release 18.10, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py3.6 Conda environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.0.130** including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS)** 10.0.130
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN)** 7.4.0
- **NCCL** 2.3.6 (optimized for **NVLink™**)
- **Caffe2**
- **APEX**
- **OpenMPI 3.1.2**
- **TensorRT 5.0.0 RC**
- **DALI 0.4 Beta**
- Tensor Core Optimized Examples:
  - **ResNet50 v1.5**
  - **GNMT v2**

Driver Requirements

Release 18.10 is based on CUDA 10, which requires **NVIDIA Driver** release 410.xx. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384. For more information, see **CUDA Compatibility and Upgrades**.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.
PyTorch container image version 18.10 is based on PyTorch v0.4.1+ with up-to-date features from the PyTorch v1.0 preview (main branch up to PR 11834). PyTorch 0.4.1+ is released and included with this container.

- When possible PyTorch will now automatically use cuDNN persistent RNN’s providing improved speed for smaller RNN’s.
- Improved multi-GPU performance in both PyTorch c10d and Apex’s DDP.
- Faster weight norm with improved mixed-precision accuracy used through torch.nn.utils.weight_norm.
- Improved functionality of the torch.jit.script and torch.jit.tracepreview features including better support for pointwise operations in fusion.
- Added support for a C++ only API (new PyTorch 1.0 preview feature).
- Dataloader may still throw a benign error when stopping iterations early, however, it is no longer preventing the process from ending.
- Latest version of DALI 0.4 Beta.
- Latest version of NCCL 2.3.6.
- Added support for OpenMPI 3.1.2
- Ubuntu 16.04 with September 2018 updates

Tensor Core Examples

- An implementation of ResNet50. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model.
- An implementation of GNMT v2. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.

Known Issues

There are no new issues in this release.
Chapter 69. PyTorch Release 18.09

The NVIDIA container image of PyTorch, release 18.09, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in the `pytorch-py3.6 Conda™ environment` in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 10.0.130** including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 10.0.130**
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.3.0**
- **NCCL 2.3.4** (optimized for **NVLink™**)  
- **Caffe2**
- **TensorRT 5.0.0 RC**
- **DALI 0.2 Beta**
- Tensor Core Optimized Examples:
  - **ResNet50 v1.5**
  - **GNMT v2**

Driver Requirements

Release 18.09 is based on CUDA 10, which requires **NVIDIA Driver** release 410.xx. However, if you are running on Tesla (Tesla V100, Tesla P4, Tesla P40, or Tesla P100), you may use NVIDIA driver release 384. For more information, see **CUDA Compatibility and Upgrades**.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.
PyTorch container image version 18.09 is based on PyTorch 0.4.1+. PyTorch 0.4.1 is released and included with this container.

- Latest version of cuDNN 7.3.0.
- Latest version of CUDA 10.0.130 which includes support for DGX-2, Turing, and Jetson Xavier.
- Latest version of cuBLAS 10.0.130.
- Latest version of NCCL 2.3.4.
- Latest version of TensorRT 5.0.0 RC.

Note: All 18.09 containers inherit TensorRT 5.0.0 RC from the base container, however, some containers may not use TensorRT if there is no support for TensorRT in the given framework.

- An implementation of ResNet50. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model.
- Stream pool: PyTorch now uses per GPU stream pools behind the scenes. This means that CUDA streams are created when first used on a GPU and destroyed on exit. As a result, networks that use multiple streams may see the same stream used repeatedly in their profiles, and networks that retain streams for long periods may accidentally schedule parallelizable work to the same stream. It’s recommended that streams be acquired, used, and released as needed.
- Reliability: Some cases where a dataloader could hang if shutdown during its iteration has been fixed.
- Fusion: Tensor and constant scalar operations, like \( \text{add}(t, 1) \), and chunk operations are now fusible.
- Performance improvements: dropout, 1x1 convolutions for NCHW, and weightnorm should be faster in a majority of scenarios.
- Latest version of DALI 0.2 Beta
- Ubuntu 16.04 with August 2018 updates

Tensor Core Examples

- An implementation of ResNet50. The ResNet50 v1.5 model is a modified version of the original ResNet50 v1 model.
- An implementation of GNMT v2. The GNMT v2 model is similar to the one discussed in the Google’s Neural Machine Translation System: Bridging the Gap between Human and Machine Translation paper.
Known Issues

- The DALI integrated ResNet-50 samples in the 18.09 NGC TensorFlow and PyTorch containers may result in lower than expected performance results. We are working to address the issue in the next release.

- There is a chance that PyTorch will hang on exit when running multi-gpu training. This hang does not affect any results of the run; however, the process will have to be terminated manually.
Chapter 70. PyTorch Release 18.08

The NVIDIA container image of PyTorch, release 18.08, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in the `pytorch-py3.6` Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 9.0.176** (see Errata section and 2.1) including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.425**
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.2.1**
- **NCCL 2.2.13** (optimized for **NVLink™**)
- **Caffe2 0.8.1**
- **DALI 0.1.2 Beta**
- Tensor Core Optimized Examples:
  - **GNMT v2**

Driver Requirements

Release 18.08 is based on CUDA 9, which requires **NVIDIA Driver** release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 18.08 is based on **PyTorch 0.4.1**. PyTorch 0.4.1 is released and included with this container. See the release notes at **https://github.com/pytorch/pytorch/releases** for significant changes from PyTorch 0.4.

- Apex is now entirely Python for improved compatibility. Previous versions of Apex will not work with PyTorch 0.4.1 or newer versions.
- **New ops in 18.08**: torch.pinverse, torch.unique, torch.erfc, torch.isinf, torch.isfinite, torch.reshape_as.
- Support for cross-device gradient clipping.
- torch.svd and torch.eig in CUDA have been fixed, previously they could return incorrect results.
- Latest version of [cuDNN 7.2.1](https://www.nvidia.com/content/cudnn.html).
- Latest version of [DALI 0.1.2 Beta](https://github.com/triton/dali).
- Added support for the [Tensor Core Optimized Example: PyTorch GNMT model](https://pytorch.org/docs/stable/tensor_core_examples.html).
- Ubuntu 16.04 with July 2018 updates

**Usage**

The PYTHONPATH environment variable in this container version has been updated to include all packages installed in the Conda environment and all PyTorch related packages. Users that rely on PYTHONPATH to point to local modules are advised to carefully check and set their PYTHONPATH variable in this container and moving forward.

**Tensor Core Examples**


**Known Issues**

The DALI integrated ResNet-50 samples in the 18.08 NGC TensorFlow and PyTorch containers may result in lower than expected performance results. We are working to address the issue in the next release.
Chapter 71. PyTorch Release 18.07

The NVIDIA container image of PyTorch, release 18.07, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py3.6 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including Python 3.6 environment
- **NVIDIA CUDA 9.0.176** (see Errata section and 2.1) including CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.425
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.1.4**
- **NCCL** 2.2.13 (optimized for NVLink™)
- **Caffe2** 0.8.1
- **DALI** 0.1 Beta

Driver Requirements

Release 18.07 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 18.07 is based on PyTorch 0.4.0 upstream main branch post commit cca2476.
- Clip grads can be used on a single tensor directly.
- The precision of MSELoss with half inputs has been improved.
- PyTorch’s JIT (still in Alpha) now supports FP16 inputs and outputs, comparisons, the exp operator, and ReLU gates.
- Added support for DALI 0.1 Beta.
- Latest version of CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.425.
Ubuntu 16.04 with June 2018 updates

Known Issues

When importing Caffe2 after importing Torch, there is an issue which causes GPU support for Caffe2 to be disabled. For users affected by this bug, it is recommended to either use the PyTorch 18.06 or 18.08 container.
Chapter 72. PyTorch Release 18.06

The NVIDIA container image of PyTorch, release 18.06, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in the `pytorch-py3.6` Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 9.0.176** (see Errata section and 2.1) including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.333** (see section 2.3.1)
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.1.4**
- **NCCL 2.2.13** (optimized for **NVLink™**)
- **Caffe2 0.8.1**

Driver Requirements

Release 18.06 is based on CUDA 9, which requires **NVIDIA Driver** release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 18.06 is based on **PyTorch 0.4.0** upstream main branch post **commit 0e9613c**.
- Improved data loader pipeline in the ImageNet example, see `/opt/pytorch/examples/imagenet` within the container.
- Data loader pipeline now uses pillow-simd and jpeg-turbo.
- Improved FP16 support, specifically, reductions like `sum()` are now more accurate when using FP16.
- Improved distributed performance, specifically, gradient communication can now overlap with gradient computation in `backwards()`.
- Compatibility changes, specifically, Magma 1 is no longer supported.
Ubuntu 16.04 with May 2018 updates

Known Issues
There are no known issues in this release.
Chapter 73. PyTorch Release 18.05

The NVIDIA container image of PyTorch, release 18.05, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in the `pytorch-py3.6` Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 9.0.176** (see Errata section and 2.1) including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.333** (see section 2.3.1)
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.1.2**
- **NCCL 2.1.15** (optimized for **NVLink™**)
- **Caffe2 0.8.1**

Driver Requirements

Release 18.05 is based on CUDA 9, which requires **NVIDIA Driver** release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 18.05 is based on **PyTorch 0.4.0**.
- Includes **Caffe2 0.8.1**. For more information, see PyTorch and Caffe2 repos getting closer together.
- APEX, an extension providing utilities for FP16 and multi-gpu training. For more information, see APEX: A PyTorch Extension and APEX.
- Ubuntu 16.04 with April 2018 updates
Known Issues

- Some mixed-precision models might encounter a crash due to a new FP16 overflow check added in PyTorch. We have an upstream fix submitted with PR 7382 and should be resolved in a future container.

- There is a minor performance regression with the imagenet sample in /opt/pytorch/examples/imagenet for some network architectures on multi-gpu cases. This regression will be fixed in the next release.
Chapter 74. PyTorch Release 18.04

The NVIDIA container image of PyTorch, release 18.04, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in `/opt/pytorch`. It is pre-built and installed in the `pytorch-py3.6` Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu 16.04** including **Python 3.6** environment
- **NVIDIA CUDA 9.0.176** (see Errata section and 2.1) including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.333** (see section 2.3.1)
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.1.1**
- **NCCL 2.1.15** (optimized for **NVLink™**)

Driver Requirements

Release 18.04 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 18.04 is based on **PyTorch 0.3.1**.
- Incorporated all upstream changes from the PyTorch main branch, specifically up to and including commit `2f27c1b5`.
- Latest version of NCCL 2.1.15
- Ubuntu 16.04 with March 2018 updates

Known Issues

Some mixed-precision models might encounter a crash due to a new FP16 overflow check added in PyTorch. We have an upstream fix submitted with PR 7382 and should be resolved in a future container.
The NVIDIA container image of PyTorch, release 18.03, is available.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py3.6 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including **Python 3.6** environment
- **NVIDIA CUDA 9.0.176** (see Errata section and 2.1) including **CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.333** (see section 2.3.1)
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.1.1**
- **NCCL 2.1.2** (optimized for **NVLink™**)

Driver Requirements

Release 18.03 is based on CUDA 9, which requires **NVIDIA Driver release 384.xx**.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- **PyTorch** container image version 18.03 is based on **PyTorch 0.3.0**.
- Incorporated all upstream changes from the PyTorch main branch, specifically, **PR 5327**.
- Latest version of cuBLAS 9.0.333
- Latest version of cuDNN 7.1.1
- Ubuntu 16.04 with February 2018 updates

Known Issues

There are no known issues in this release.
Chapter 76. PyTorch Release 18.02

The NVIDIA container image of PyTorch, release 18.02, is available. PyTorch container image version 18.02 is based on PyTorch 0.3.0.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py3.6 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including Python 3.6 environment
- **NVIDIA CUDA** 9.0.176 including:
  - CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.282 Patch 2 which is installed by default
  - cuBLAS 9.0.234 Patch 1 as a debian file. Installing Patch 1 by issuing the `dpkg -i /opt/cuda-cublas-9-0_9.0.234-1_amd64.deb` command is the workaround for the known issue described below.
  - NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.0.5
  - NCCL 2.1.2 (optimized for NVLink™)

Driver Requirements

Release 18.02 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Improved multi-GPU performance on image networks shown in /opt/pytorch/examples/imagenet. You can run this example for multi-GPU by issuing the `python -m multiproc main.py` command.
- Latest version of cuBLAS
- Ubuntu 16.04 with January 2018 updates
Known Issues

cuBLAS 9.0.282 regresses RNN seq2seq FP16 performance for a small subset of input sizes. This issue should be fixed in the next update. As a workaround, install cuBLAS 9.0.234 Patch 1 by issuing the `dpkg -i /opt/cuda-cublas-9-0_9.0.234-1_amd64.deb` command.
Chapter 77. PyTorch Release 18.01

The NVIDIA container image of PyTorch, release 18.01, is available.

PyTorch container image version 18.01 is based on PyTorch 0.3.0.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py3.6 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04 including Python 3.6 environment
- **NVIDIA CUDA** 9.0.176 including CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.282
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN)** 7.0.5
- **NCCL** 2.1.2 (optimized for NVLink™)

Driver Requirements

Release 18.01 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Latest version of cuBLAS
- Latest version of cuDNN
- Latest version of NCCL
- Ubuntu 16.04 with December 2017 updates

Known Issues

cuBLAS 9.0.282 regresses RNN seq2seq FP16 performance for a small subset of input sizes. As a workaround, revert back to the 11.12 container.
Chapter 78. PyTorch Release 17.12

The NVIDIA container image of PyTorch, release 17.12, is available. PyTorch container image version 17.12 is based on PyTorch 0.2.0.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py35 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04
- **NVIDIA CUDA** 9.0.176 including CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.234
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN)** 7.0.5
- **NCCL** 2.1.2 (optimized for NVLink™)

Driver Requirements

Release 17.12 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Latest version of CUDA
- Latest version of cuDNN
- Latest version of NCCL
- Ubuntu 16.04 with November 2017 updates

Known Issues

There are no known issues in this release.
Chapter 79. PyTorch Release 17.11

The NVIDIA container image of PyTorch, release 17.11, is available. PyTorch container image version 17.11 is based on PyTorch 0.2.0.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py35 Conda™ environment in the container image.

The container also includes the following:

- **Ubuntu** 16.04
- **NVIDIA CUDA** 9.0.176 including CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) 9.0.234
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN)** 7.0.4
- **NCCL** 2.1.2 (optimized for NVLink™)

Driver Requirements

Release 17.11 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Tensor Op accelerated RNNs for Volta architecture
- Improved depthwise separable convolution performance
- Improved automatic differentiation engine latency
- Latest version of CUDA
- Latest version of cuDNN
- Latest version of NCCL
- Ubuntu 16.04 with October 2017 updates
Known Issues

There are no known issues in this release.
Chapter 80. PyTorch Release 17.10

The NVIDIA container image of PyTorch, release 17.10, is available. PyTorch container image version 17.10 is based on PyTorch 0.2.0.

Contents of PyTorch
This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py35 Conda™ environment in the container image.

The container also includes the following:

- Ubuntu 16.04
- NVIDIA CUDA® 9.0
- NVIDIA CUDA®-Deep Neural Network library™ (cuDNN) 7.0.3
- NVIDIA® Collective Communications Library™ (NCCL) 2.0.5 (optimized for NVLink™)

Driver Requirements
Release 17.10 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements
This PyTorch release includes the following key features and enhancements.

- Latest version of CUDA
- Latest version of cuDNN
- Latest version of NCCL
- Ubuntu 16.04 with September 2017 updates

Known Issues
There are no known issues in this release.
Chapter 81. PyTorch Release 17.09

The NVIDIA container image of PyTorch, release 17.09, is available. PyTorch container image version 17.09 is based on PyTorch 0.2.0.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed in the pytorch-py35 Conda environment in the container image.

The container also includes the following:

- Ubuntu 16.04
- NVIDIA CUDA® 9.0
- NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 7.0.2
- NVIDIA® Collective Communications Library™ (NCCL) 2.0.5 (optimized for NVLink™)

Driver Requirements

Release 17.09 is based on CUDA 9, which requires NVIDIA Driver release 384.xx.

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Supports Tensor Core operations for convolutions and GEMMs on Volta hardware
- The examples directory contains examples of ImageNet and LSTM training scripts that use FP16 data, as well as show how to train with FP16
- Matrix multiplication on FP16 inputs uses Tensor Core math when available
- A custom batch normalization layer is implemented to use cuDNN for batch normalization with FP16 inputs
- Latest version of CUDA
- Latest version of cuDNN with support for Tensor Core math when available
- Latest version of NCCL
- Ubuntu 16.04 with August 2017 updates
Known Issues

There are no known issues in this release.
Chapter 82. PyTorch Release 17.07

The NVIDIA container image of PyTorch, release 17.07, is available. PyTorch container image version 17.07 is based on PyTorch 0.1.12.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed into the /usr/local/[bin,share,lib] directories in the container image.

The container also includes the following:

- **Ubuntu** 16.04
- **NVIDIA CUDA®** 8.0.61.2 including CUDA® Basic Linear Algebra Subroutines library™ (cuBLAS) Patch 2
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 6.0.21**
- **NVIDIA® Collective Communications Library™ (NCCL) 2.0.3** (optimized for NVLink™)

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Support for advanced tensor indexing
- Support multi-node or multi-process mode on the same node
- Support for double backward for most functions, including convolution
- Ubuntu 16.04 with June 2017 updates

Known Issues

There are no known issues in this release.
Chapter 83. PyTorch Release 17.06

The NVIDIA container image of PyTorch, release 17.06, is available.

PyTorch container image version 17.06 is based on PyTorch 0.1.12.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed into the /usr/local/[bin,share,lib] directories in the container image.

The container also includes the following:

- **Ubuntu** 16.04
- **NVIDIA CUDA®** 8.0.61
- **NVIDIA CUDA® Deep Neural Network library™ (cuDNN)** 6.0.21
- **NVIDIA® Collective Communications Library™ (NCCL)** 1.6.1 (optimized for NVLink™)

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Ubuntu 16.04 with May 2017 updates

Known Issues

The NCCL library version 1.6.1 included in this image, modifies the output buffers on all GPUs during in-place `ncclReduce()` operations, whereas normally only the "root" (target) device’s output buffer should be modified. This is fixed in later versions of NCCL, as will be packaged in later versions of this image. As a workaround, either use `ncclAllReduce()`, which correctly modifies output buffers of all GPUs to the same values, or use out-of-place `ncclReduce()`, wherein the output buffer is distinct from the input buffer.
Chapter 84. PyTorch Release 17.05

The NVIDIA container image of PyTorch, release 17.05, is available. PyTorch container image version 17.05 is based on PyTorch 0.1.12.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed into the /usr/local/[bin,share,lib] directories in the container image.

The container also includes the following:

‣ Ubuntu 16.04
‣ NVIDIA CUDA® 8.0.61
‣ NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 6.0.21
‣ NVIDIA® Collective Communications Library™ (NCCL) 1.6.1 (optimized for NVLink™)

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

‣ Latest cuDNN release
‣ Ubuntu 16.04 with April 2017 updates

Known Issues

The NCCL library version 1.6.1 included in this image, modifies the output buffers on all GPUs during in-place ncclReduce() operations, whereas normally only the "root" (target) device's output buffer should be modified. This is fixed in later versions of NCCL, as will be packaged in later versions of this image. As a workaround, either use ncclAllReduce(), which correctly modifies output buffers of all GPUs to the same values, or use out-of-place ncclReduce(), wherein the output buffer is distinct from the input buffer.
Chapter 85. PyTorch Release 17.04

The NVIDIA container image of PyTorch, release 17.04, is available. PyTorch container image version 17.04 is based on PyTorch 0.1.10.

Contents of PyTorch

This container image contains the complete source of the version of PyTorch in /opt/pytorch. It is pre-built and installed into the /usr/local/[bin,share,lib] directories in the container image.

The container also includes the following:

- Ubuntu 16.04
- NVIDIA CUDA® 8.0.61
- NVIDIA CUDA® Deep Neural Network library™ (cuDNN) 6.0.20
- NVIDIA® Collective Communications Library™ (NCCL) 1.6.1 (optimized for NVLink™)

Key Features and Enhancements

This PyTorch release includes the following key features and enhancements.

- Reduce DataParallel overhead on more than 4 GPUs
- cuDNN v6 integration
- Synced to upstream PyTorch version as of March 2017
- Ubuntu 16.04 with March 2017 updates

Known Issues

There are no known issues in this release.
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