



SUPPORT MATRIX FOR TENSORRT

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Support Guide



TABLE OF CONTENTS

Chapter 1. Features For Platforms And Software.....	1
Chapter 2. Layers And Features.....	2
Chapter 3. Layers And Precision.....	5
Chapter 4. Hardware And Precision.....	7
Chapter 5. Software Versions Per Platform.....	8

Chapter 1.

FEATURES FOR PLATFORMS AND SOFTWARE

The following table lists the supported platforms and which version supports a particular feature.

Table 1

	Linux x86-64	Linux AArch64	QNX AArch64	Windows x64
Supported CUDA versions	9.0, 10.0	10.0	10.0	10.0
Supported cuDNN versions	7.3.1	7.3.1	7.3.1	7.3.1
TensorRT Python API	Yes	No	No	No
NvUffParser	Yes	Yes	Yes	Yes
NvOnnxParser	Yes	Yes	Yes	No



Serialized engines are not portable across platforms or TensorRT versions.

Chapter 2.

LAYERS AND FEATURES

The following table lists the TensorRT layers, specifically, which features are supported by a particular layer.

Table 2

Layer	Dimensions of input tensor	Dimensions of output tensor	Does the operation apply to only the innermost 3 dimensions?	Supports broadcast ¹	Supports broadcast across batch ²
Activation	0-7 dimensions	0-7 dimensions	No	No	No
Concatenation	1-7 dimensions	1-7 dimensions	No	No	No
Constant	0-7 dimensions	0-7 dimensions	No	No	Always
Convolution	3 or more dimensions	3 or more dimensions	Yes	No	No
Deconvolution	3 or more dimensions	3 or more dimensions	Yes	No	No
ElementWise	0-7 dimensions	0-7 dimensions	No	Yes	Yes
FullyConnected	3 or more dimensions	3 or more dimensions	Yes	No	No
Gather	▶ Input1: 1-7 dimensions	0-7 dimensions	No	No	Yes

Layer	Dimensions of input tensor	Dimensions of output tensor	Does the operation apply to only the innermost 3 dimensions?	Supports broadcast ¹	Supports broadcast across batch ²
	▶ Input2: 0-7 dimensions				
Identity	0-7 dimensions	0-7 dimensions	No	No	No
IPluginV2	User defined	User defined	User defined	User defined	User defined
LRN	3 or more dimensions	3 or more dimensions	Yes	No	No
MatrixMultiply	2 or more dimensions	2 or more dimensions	No	Yes	Yes
Padding	3 or more dimensions	3 or more dimensions	Yes	No	No
Plugin	User defined	User defined	User defined	User defined	User defined
Pooling	3 or more dimensions	3 or more dimensions	Yes	Yes	Yes
RaggedSoftMax	▶ Input: 2 dimensions ▶ Bounds: 2 dimensions	2 or more dimensions	No	No	Yes
Reduce	1-7 dimensions	0-7 dimensions	No	No	No
RNN	3 dimensions	3 dimensions	No	No	No
RNNv2	▶ Data/Hidden/Cell: 2 or more dimensions ▶ SeqLen: 0 or more dimensions	Data/Hidden/Cell: 2 or more dimensions	No	No	No

Layer	Dimensions of input tensor	Dimensions of output tensor	Does the operation apply to only the innermost 3 dimensions?	Supports broadcast ¹	Supports broadcast across batch ²
Scale	3 or more dimensions	3 or more dimensions	Yes	No	No
Shuffle	0-7 dimensions	0-7 dimensions	No	No	No
SoftMax	1-7 dimensions	1-7 dimensions	No	No	No
TopK	1-7 dimensions	<ul style="list-style-type: none"> ▶ Output1: 1-7 dimensions ▶ Output2: 1-7 dimensions 	Yes	No	Yes
Unary	0-7 dimensions	0-7 dimensions	No	No	No

For more information about each of the TensorRT layers, see [TensorRT Layers](#).

Chapter 3.

LAYERS AND PRECISION

The following table lists the TensorRT layers and the precision modes that each layer supports. It also lists the ability of the layer to run on Deep Learning Accelerator (DLA). For more information about additional constraints, see [DLA Supported Layers](#).

For more information about each of the TensorRT layers, see [TensorRT Layers](#). To view a list of the specific attributes that are supported by each layer, refer to the [TensorRT API documentation](#).

Table 3

Layer	FP32	FP16	INT32	DLA ³
Activation	Yes	Yes	No	Yes
Concatenation	Yes	Yes	Yes	Yes
Constant	Yes	Yes	Yes	No
Convolution	Yes	Yes	No	Yes
Deconvolution	Yes	Yes	No	Yes
ElementWise	Yes	Yes	No	Yes
FullyConnected	Yes	Yes	No	Yes
Gather	Yes	Yes	Yes	No
Identity	Yes	Yes	Yes	No
IPluginV2	Yes	Yes	No	No
LRN	Yes	Yes	No	Yes
MatrixMultiply	Yes	Yes	No	No
Padding	Yes	Yes	No	No

Layer	FP32	FP16	INT32	DLA ³
Plugin	Yes	Yes	No	No
Pooling	Yes	Yes	No	Yes
RaggedSoftMax	Yes	No	No	No
Reduce	Yes	Yes	No	No
RNN	Yes	Yes	No	No
RNNv2	Yes	Yes	No	No
Scale	Yes	Yes	No	Yes
Shuffle	Yes	Yes	Yes	No
SoftMax	Yes	Yes	No	No
TopK	Yes	Yes	No	No
Unary	Yes	Yes	No	No

Chapter 4.

HARDWARE AND PRECISION

The following table lists NVIDIA hardware and which precision modes each hardware supports. It also lists availability of Deep Learning Accelerator (DLA) on these hardware.

Table 4

SM Version	Example Device	FP32	FP16	INT8	FP16 Tensor Cores	INT8 Tensor Cores	DLA
7.5	Tesla T4	Yes	Yes	Yes	Yes	Yes	No
7.2	Jetson AGX Xavier	Yes	Yes	Yes	Yes	Yes	Yes
7.0	Tesla V100	Yes	Yes	Yes	Yes	No	No
6.2	Jetson TX2	Yes	Yes	No	No	No	No
6.1	Tesla P4	Yes	No	Yes	No	No	No
6.0	Tesla P100	Yes	Yes	No	No	No	No
5.3	Jetson TX1	Yes	Yes	No	No	No	No
5.2	Tesla M4	Yes	No	No	No	No	No

Chapter 5.

SOFTWARE VERSIONS PER PLATFORM

The following table lists the software versions that are supported by each platform.

Table 5

	Ubuntu 14.04	Ubuntu 16.04	Ubuntu 18.04	CentOS 7.5	Linux AArch64	QNX	Windows 10
Compiler version	gcc 4.8.4	gcc 5.4.0	gcc 7.3.0	gcc 4.8.5	gcc 5.3.1	gcc 5.4.0	MSVC 2017u5
Python versions	2.7, 3.4	2.7, 3.5	2.7, 3.6	2.7			



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