NVIDIA DGX GB300 NVL72 Release Notes

Release Notes v1.0.6 for Multi-Node System Stack Firmware and Software Components for NVIDIA DGX GB300 System

NVIDIA Corporation

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Contents

1 Document History **1**

2 Overview **3**

3 NVIDIA DGX GB300 NVL72 Features **5**

4 Multi-Node System Software Stack Package Contents **7**
4.1 Compute Tray Components . . . 7
4.2 Host Software Components . . . 7
4.2.1 BMC . . . 7
4.2.2 CX8 N/S . . . 8
4.2.3 HMC . . . 8
4.3 GB300 Switch Tray . . . 9
4.3.1 NVOS . . . 9
4.3.2 BMC+FPGA+EROT . . . 9
4.3.3 SBIOS+EROT . . . 10
4.3.4 CPLD . . . 10
4.4 PowerShelf FW . . . 11
4.5 NVIDIA/gdrcopy . . . 11

5 System Libraries, Tools, and Drivers **13**
5.1 Table 1. Public Release Links Associated with this Release . . . 13
5.2 Table 2. cuDNN . . . 13
5.3 Table 3. NCCL . . . 13
5.4 Table 4. NVSHMEM . . . 14
5.5 Table 5. DLFW Containers . . . 14
5.6 Table 6. NVIDIA TensorRT . . . 15
5.7 Table 7. MFT/Tools Firmware Package Contents . . . 15

6 Improvements **17**
6.1 Release 1.0.6 . . . 17
6.2 Release 1.0.1 . . . 18

7 Known Issues **21**

8 Notices **25**
8.1 Notice . . . 25
8.2 Trademarks . . . 26
8.3 Copyright . . . 26

Chapter 1. Document History

RN-11874-001_1.0.6

Version	Date	Description of Change
01	Oct 10, 2025	Release 1.0.0
02	Dec 19, 2025	Release 1.0.1 ▶ Added improvements and known issues
03	Mar 18, 2026	Release 1.0.6 ▶ Added improvements. ▶ Updated firmware tables. ▶ Added new documentation links to support this release.

Chapter 2. Overview

This document contains detailed information for the NVIDIA® DGX GB300 NVL72 v1.0.6 software/firmware release.

This document includes dependencies and instructions that are specific to this release, the versions provided as part of the release, and a list of known issues and improvements.

Legal Disclaimer: The System SW 1.0.6 GA Release is only for the target product DGX GB300 NVL72 and cannot be used for any other product.

Important

Action required: Upgrade for NVLink Recovery

In release **1.0.6** NVLink Recovery remains enabled by default. To ensure proper functionality and system stability, you must upgrade *all* components in the rack to release **1.0.6** or later, including compute nodes and NVSwitch.

Failure to upgrade the entire rack may lead to incompatibility issues and unexpected behavior during NVLink Recovery operations.

Important

Action required: SBIOS patch for Hynix and Micron memory support

In release **1.0.6**, the SBIOS version is rolled back to **02.05.05**. This version does not natively support Micron and Hynix memory modules. To ensure proper support for HPMs with these memory modules, apply the SBIOS patch (NVOnline **1151413** PG548 Memory Support SBIOS Patch for 02.05.05).

Without this patch, systems with Micron or Hynix memory modules fail to boot with SBIOS version 02.05.05.

Important

Action required: DOCA GPG-Key Update

In release **1.0.6**, the DOCA version is updated to 3.2.1-044413 which requires a new GPG key. This key update is specific to this release until DGX OS 7.5.0 becomes public on 3/31/26. Please refer to the FW Update guide for the detailed install steps.

Without this patch, DOCA will not work.

Chapter 3. NVIDIA DGX GB300 NVL72 Features

This section outlines the DGX GB300 NVL72 features included in this release, which provides essential operational capabilities to support end-to-end system qualification and validation.

This release includes the following features:

- NVLink Domain
  - 72x1 GPUs
- Fabric Manager
  - Support for NVLink multi-node deployment.
  - Peer-to-Peer Traffic routing/configuration.
  - Single default partition and multiple user partitions, with all available GPUs.
- IMEX Service
  - IMEX Service Peer-to-Peer Memory Import/Export support.
  - IMEX Dynamic Reconnect support.
  - IMEX Service multicast (NVLink SHARP) Import/Export support.
- RM Multi-User/Multi-Job Isolation support.
- Platform information for the compute and switch trays.
- NVIDIA multi-node CUDA®
  - Unicast traffic and multicast.
  - OpenMPI will be supported with NVLink Intranode and MNNVL internode.
  - Multi-Node NvBandwidth.
- NCCL
  - NCCL: All2All, AllReduce, and Unicast.
  - NVSHMEM
- nvidia.nvlink Ansible Collection
  - v1.6.9
- CX8
  - CX8 Telemetry

- DCGM with multi-node testing support

Chapter 4. Multi-Node System Software Stack Package Contents

4.1. Compute Tray Components

Refer to the *NVIDIA* FW update guide for more information about the HMC and HMC NO-BIOS firmware bundle and installation instructions.

> **Note**
>
> The following are the host software versions tested with DGX GB300 Release 1.0.6.

4.2. Host Software Components

Component	Version
GPU Driver	580.126.20
IMEX	580.126.20
Kernel Module Source	`NVIDIA-kernel-module-source-580.126.20.tar.xz`
MFT Tools	4.34.1-12
DOCA_Host	3.2.1-044413
CUDA	13.0.2
BF3_BFB	3.2.1-42_25.11
Host OS	DGX OS 7.4.0

4.2.1. BMC

Bundle file: `nvfw_DGX-GBX00_0026_251112.1.0_custom_prod-signed.fwpkg`

Component	Version
EROT	01.04.0031.0000_n04
BMC	GB200Nvl-25.08-9
CX8 SMA	0003.00.0220.0001

Component	Filename
Recovery Firmware	`nvfw_DGX-GBX00_0101_251112.1.0_custom_recovery_prod-signed.fwpkg`
CoRIM	`DGX-GBX00_0026_251112.1.0_custom_prod-signed.corim`
CRT	`nvfw_GB300-p4058_corim_prod_cert.crt`
PEM	`nvfw_GB300-p4058_corim_prod_pub.pem`

4.2.2. CX8 N/S

Bundle file: `fw-ConnectX8-rel-40_47_2526-900-9X86E-00SX-SPA_Ax-UEFI-14.40.10-FlexBoot-3.8.201-MT_0000001228.fwpkg`

Component	Filename
CoRIM	`fw-ConnectX8-rel-40_47_2526-900-9X86E-00CX-SP0_Ax-UEFI-14.40.10-FlexBoot-3.8.201.signed.corim`

4.2.3. HMC

Bundle file: `nvfw_HGX-GBX00_0026_260319.1.0_custom_prod-signed.fwpkg`

Component	Version
CPLD	0.22
GPU	97.10.4A.00.1F
EROT	01.04.0031.0000_n04
HMC	GB200Nvl-25.08-B
SBIOS	02.05.06_alt
FPGA	1.60

Component	Filename
Recovery Firmware	`nvfw_HGX-GBX00_0026_260319.1.0_custom_recovery_prod-signed.fwpkg`
CoRIM	`HGX-GBX00_0026_260319.1.0_custom_prod-signed.corim`
CRT	`nvfw_GB300-P4059_fwpkg_prod_cert.crt`
PEM	`nvfw_GB300-P4059_fwpkg_prod_pub.pem`

4.3. GB300 Switch Tray

4.3.1. NVOS

Version: 25.02.4347

Component	Version
SM	2025.06.16
NMX-C	4.021.34
GFM	580.105.18
NMX-T	3.6.1
Switch ASIC	35_2014_4784

Component	Filename
CoRIM	`GB300-P4093_0005_260205.1.0_prod-signed.corim`

4.3.2. BMC+FPGA+EROT

Version: GB300-P4093_0004_260127.1.0
Bundle file: `nvfw_GB300-P4093_0004_260127.1.0_prod-signed.fwpkg`

Component	Version
EROT	01.04.0031.0000_n04
BMC	88.0002.1961
FPGA	0.24

Component	Filename
CoRIM	GB300-P4093_0004_260127.1.0_prod-signed.corim
Recovery Firmware	nvfw_GB300-P4093_0004_260127.1.0_recovery_prod-signed.fwpkg

4.3.3. SBIOS+EROT

Version: GB300-P4093_0006_251218.1.0
Bundle file: nvfw_GB300-P4093_0006_251218.1.0_prod-signed.fwpkg

Component	Version
EROT	01.04.0031.0000_n04
SBIOS	0ACTV_00.01.020

Component	Filename
CoRIM	GB300-P4093_0006_251218.1.0_prod-signed.corim
Recovery Firmware	nvfw_GB300-P4093_0006_251218.1.0_recovery_prod-signed.fwpkg

4.3.4. CPLD

Version: GB300-P4093_0007_251218.1.0
Bundle file: nvfw_GB300-P4093_0007_251218.1.0_prod-signed.fwpkg

Component	Version
CPLD1	CPLD000420_REV0300
CPLD2	CPLD000419_REV0301
CPLD3	CPLD000418_REV0200

4.4. PowerShelf FW

Vendor	Component	Version
Delta	PSU	0104
	PMC	3.2.4
LiteON	PSU	0101
	PMC	1.3.10

4.5. NVIDIA/gdrcopy

NVIDIA/gdrcopy is a fast GPU memory copy library-based on NVIDIA GPUDirect RDMA technology.

GDRCOPY version 2.5 is a publicly available release.

- The code is released on NVIDIA/gdrcopy.
- The prebuilt packages release is available on Index of /compute/redist/gdrcopy.
- Refer to Magnum IO GDRCopy for more information.

Component	Version
Stars	1006
Language	C++

Chapter 5. System Libraries, Tools, and Drivers

5.1. Table 1. Public Release Links Associated with this Release

The following table provides a list of the public links associated with this release.

Description	Link
Datacenter Driver Version **580.126.20**	https://developer.nvidia.com/datacenter-driver-580-126-20-download-arch
CUDA Toolkit **13.0.2**	https://developer.nvidia.com/cuda-13-0-2-download-archive

5.2. Table 2. cuDNN

The following table provides the download information for the cuDNN release.

Component	Version	Location
cuDNN	**9.18.1**	NVIDIA cuDNN release notes

5.3. Table 3. NCCL

The following table provides information about where the NCCL release can be downloaded and links to the latest documentation.

Component	Location
NCCL Homepage	NVIDIA Collective Communications Library (NCCL) Download Page
PIP Wheels	NVIDIA Collective Communication Library (NCCL) Runtime
GitHub release	NVIDIA NCCL GitHub release
DevZone Binaries	NVIDIA NCCL download page (current release) NVIDIA NCCL legacy download page (legacy releases)
Current Release Notes	Overview and NCCL release notes **Note**: GB300 support is available from NCCL 2.27.5

5.4. Table 4. NVSHMEM

The following table provides information about where the NVSHMEM release can be downloaded and links to the latest documentation.

Component	Location
NVSHMEM Homepage	NVIDIA NVSHMEM Homepage
Current Release	NVIDIA NVSHMEM Downloads
Legacy releases	NVIDIA NVSHMEM Archive of legacy releases
Documentation	▶ Release Notes/Installation Guide: NVIDIA NVSHMEM Release Notes ▶ API Documentation: NVIDIA OpenSHMEM Library (NVSHMEM) Documentation ▶ Best Practice Guide: NVIDIA NVSHMEM Best Practice Guide

5.5. Table 5. DLFW Containers

The following table provides a list of the DL Frameworks containers.

Component	Versions	Location
PyTorch	Latest tag from NGC catalog.	NGC catalog for PyTorch
TensorRT	Latest tag from NGC catalog.	NGC catalog for TensorRT
JAX	Latest tag from NGC catalog.	NGC catalog for JAX

5.6. Table 6. NVIDIA TensorRT

Attention

TensorRT version 10.13 includes functional support for GB300. TensorRT (to be released at a future date) will offer performance improvements that utilize GB300 system capabilities.

The following table provides information about where TensorRT can be downloaded and links to the latest documentation.

Component	Location
TensorRT Homepage	NVIDIA TensorRT Getting Started Documentation
Release **10.14.1**	NVIDIA TensorRT current releases
Legacy releases	NVIDIA TensorRT legacy releases
Documentation	▶ Release Notes/Installation Guide: NVIDIA TensorRT Overview and Release Notes ▶ API Documentation: NVIDIA TensorRT Documentation

TRT-LLM	`nvcr.io/nvidia/tensorrt-llm/release:gb300-ea`
	`nvcr.io/nvidia/tensorrt-llm/devel:gb300-ea`
	Source code: https://github.com/NVIDIA/TensorRT-LLM/tree/feat/gb300-ea
	Early build with Beta quality — expect functional and performance issues since the support is in progress.

5.7. Table 7. MFT/Tools Firmware Package Contents

The following table provides the versions of the necessary MFT/tools to download the firmware bundle.

Component	Version	Location
DCGM	**4.5.0**	NVIDIA DCGM
nvdebug	**2.0.0**	Installation Guide — NVIDIA Debug Tool for Datacenter Products
nvfwupd	**2.0.8**	nvfwupd tool version v2.0.8
MSTflint	**4.34.1-4**	MSTflint on GitHub

Chapter 6. Improvements

This section provides information about the improvements in each release.

6.1. Release 1.0.6

1. **MSE Uptime crash ~67 days (XID 150)**

   After extended uptime (approximately 60+ days), systems can exhibit NVLink task scheduling behavior that leads to GPU driver hangs and, when using R580 driver or later, appearance of XID errors 150 and 154 in the kernel log. The overflow handling in the GPU's NVLink management microcode within the vBIOS firmware was fixed to avoid this issue going forward.

2. **Intermittent CX8 to GPU link instability causing CTO and other PCIe errors.**

   Stabilized the CX8 and GPU Gen6 link operation with the application of improved PHY settings for receiver gain and PLL stability.

3. **GPU PM L1 stability improvement.**

   Improved the GPU implementation of PCI-PM L1 flow through setting changes that now ensure continuous PLL stability.

4. **Tray swaps no longer leave stale GPU state with the system manager, so newly added GPUs are correctly detected and appear in the partition.**

5. **Resolved an issue where, after a tray swap, the partition showed only 72 GPUs and nv_cli showed 0.0.0.0.**

   Fixed issues around identifying GPUs belonging to a Compute Node.

6. **Fixed switch configuration on NVLink systems that incorrectly allowed short transient congestion conditions to cause spurious timeout events and unnecessary port disables (XID149.33).**

7. **Elevated rate of GPU PMU halted and thermal issues reported.**

   Resolved a race condition between the driver and VBIOS that could cause communication failures, resulting in elevated rates of GPU PMU halted and thermal issues. The VBIOS update implements locking between response paths to prevent this condition.

8. **Improved NVLink reliability with updated NVLink reduction and accounting for HLL.**

   This fix prevents unnecessary analysis of false positives (where an error is indicated but no actual fault exists), reducing wasted effort and improving diagnostic accuracy.

9. **Enable DHCP6 on eth0.**

   This fix ensures that DHCP6 is enabled and running.

6.2. Release 1.0.1

1. **Fixed an issue preventing modification of the Rsyslog TransportProtocol in the NVIDIA Switch BMC from default UDP to TCP or other protocols.** Transport protocol configuration is now supported.
2. **Resolved an authentication issue that led to a DoS-like state, blocking BMC access and affecting firmware updates and other functionality.**
3. **The method for identifying and managing GPUs that belong to the same compute tray has been updated.**
4. **IMEX issue in where outgoing gRPC connections were not reliably detected as lost, is improved by event handling and connection recovery, enabling nvidia-imex to promptly reestablish communication and resume processing when a disconnect occurs.**
5. **Excessive partition API requests can fill GFM logs.**

   Logging has been optimized to capture entries only during actual topology changes, ensuring the preservation of critical information.
6. **eth0/1 routing was not separated, causing incorrect routing when eth0 was disconnected.**

   Added a fix to separate the routing per interface (eth0/1).
7. **Resolved an intermittent issue where a deadlock in GFM would result in CUDA error "All CUDA-capable devices are busy or unavailable".**
8. **Resolved an occasional failure with NVLS bind operation (cuMulticastBindMem).**
9. **FLR reset hangs during GPU engine transaction.**

   Fixed by leveraging the hardware engine reset state machine to reliably reset the engine.
10. **Fixed multicast reference count computation.**

    Fixed an issue that caused multicast reference count to be computed incorrectly.
11. **In rare instances, the PKEY request could be rejected by the subnet Manager.**

    Added a fix to remove the race condition and the PKEY database is always cleared before sending the response to the GFM.
12. **Partitions are deleted from the domain.**

    This update addresses the root cause of the GFM crash, ensuring stability and preventing recurrence of the issue.
13. **Allow GPUs that are part of a multicast group to be removed from the partition.**

    Added a fix to allow GPUs that are part of a multicast group to be removed from a partition. The removed GPU is set to a "reset required" state and can't be used to run workloads until the GPU is reset.
14. **Resolved intermittent C2C link training failure during GPU reset.**

    During GPU reset, the Grace<->Blackwell GPU C2C link training process intermittently fails, leading to unrecoverable host errors and, in some instances, Grace firmware crashes causing the host to reboot. This issue has been resolved in the updated GPU firmware, which ensures reliable C2C link training during reset.
15. **Resolved SGPI_D0 and SGPI_E0 status does not reflect MCIO J80.B12.**

    Added a fix to report the second presence pin, compared to using just one of the pins.

16. **This release includes PCIe link quality improvements that will resolve an occasional PCIe CTO (Completion Timeout) error that could result in a kernel crash.**
17. **Aligning the behavior of NMX-C API where duplicate partition creation returns RESOURCE_USED on GB300 vs. PARTITION_EXISTS in GB200.**

    With v1.0.1, GB300 will return NMX_ST_PARTITION_EXISTS (same as GB200).
18. **Updated NMX-C to return partitionId in UpdatePartitionResponse for RemoveGpusFromPartition ("User partition") and AddGpusToPartition ("User partition").**

    Earlier versions failed to return partitionId.
19. **Resolved incorrect GPU THERM_WARN_INT error message in SEL logs while the GPU was operating still under normal temperature range.**

    Such an error was occasionally produced during stress test workloads. This was due to an incorrect temperature threshold in some of the code paths.
20. **Resolves an intermittent issue where a NCCL all-reduce throughput drops.** This occurs only when NVLink SHARP is enabled and control messages from GPU to GFM were dropped (for example, buffer contention), resulting in multicast setup failure. This version adds fixes across the GPU driver, Fabric Manager, and GPU firmware to improve the reliability of NVLS initialization.

Chapter 7. Known Issues

This section provides a list of the known issues.

1. **Partition will show max 72 GPUs only along with nv CLI shows 0.0.0.0 for replaced GPUs after tray swap.**

   Workaround: Following the tray replacement (old tray with a new one), run the `GetGpuInfoList()` NMX-C API. The API output:

   a. Includes GPUs that are currently in the domain, including GPUs that were added as part of the new tray.

   b. Excludes GPUs that were removed from the domain (with location 0.0.0.0).

   API call example:

   ```
   grpcurl -plaintext -d '{"context": {}, "loc": {}, "gatewayId": "myGateway
   ↪"}' 127.0.0.1:9371 nmx_c.NMX_Controller/GetGpuInfoList
   ```

2. **FundamentalResetExitCount is zero after Function Level Reset (FLR).**

   FundamentalResetExitCount is not updated correctly when a Function Level Reset (FLR) occurs. The counter remains 0 because the logic that records the reset exit runs only in non-FLR reset paths. This is a logging issue only; FLR resets work as expected.

   Workaround: There is currently no workaround, and this will be resolved in future VBIOS release.

3. **CPU core soft lockup.**

   The system may experience higher rates of CPU core softlocks during OS runtime on this firmware version.

   Workaround: 1.3.6 has downgraded to SBIOS 2.05.06. This issue will be fixed in a future release.

4. **MC powercycle of compute and switch fail to bring up Fabric Manager.**

   In rare cases, following an unexpected power failure or a switch node crash, Fabric Manager may fail to start due to database (Fabric Manager resource information) corruption.

   Workaround: There is currently no method to recover the corrupted data. To restore functionality, perform an NMX-C reset to reinitialize the cluster and Fabric Manager.

5. **Segmentation faults occur when running some applications.**

   During process teardown, applications may encounter segmentation faults under some circumstances.

   Workaround: There is currently no workaround, and this issue will be fixed in TRD5.

6. **In rare instances (< 0.01%), BMC management interface may fail to train phy link.**

In rare instances (< 0.01%), the BMC management interface phy may not link correctly with a switch.

Workaround: Reboot the BMC through the host via ipmi (`ipmitool mc reset cold`) or through Redfish via the BMC to Host USB Ethernet interface:

```
curl -k -u "user:password" -X POST https://${bmcip}/redfish/v1/Managers/
↪BMC_0/Actions/Manager.Reset -d '{"ResetType": "ForceRestart"}'
```

7. **Unexpected data (PCB sensor) was detected at "/redfish/v1/Chassis/HGX_Chassis_0/Sensors".**

   Renamed the PCB-Temp sensor to Exhaust-Temp sensor. While the PCB sensor no longer exists in the RF sensor list, the PCB temp events still exist and increment on exhaust temp event occurrence.

   Workaround: There is currently no workaround, and this issue will be fixed in a future release.

8. **When updating VBIOS, percent complete still shows 0, after the task is complete.**

   In some cases, the task service indicates a success condition while the progress property remains at '0'. In this case, ignore the progress property and monitor the TaskState and TaskStatus to determine update status.

   Workaround: There is currently no workaround, and this issue will be fixed in a future release.

9. **Grace needs to clear the dpc_trigger_status.**

   In hotplug scenarios that involve CX8 downstream devices that enable DPC, some DPC events that are triggered by the hotplug attempts are not correctly handled by RAS firmware. This causes the DPC trigger status to remain set after a hotplug DPC event, which results in the downstream link remaining disabled.

   Workaround: Disable DPC for CX8 downstream links before you attempt to hotplug the devices. This issue will be fixed in the next release.

10. **NeighborMTUDiscards property is not defined in MetricReportDefinitions.**

    NeighborMTUDiscards property is not defined in MetricReportDefinitions but exists in the Metric Reports.

    Workaround: This property will be removed in a future release. Property read will fail — do not use.

11. **When an incorrect static topology file (for example 2x36) is specified on a 1x72 topology, FM log displays duplicate control plane error states.**

    When an incorrect static topology file (for example 2x36) is specified on a 1x72 topology, both CONFIG_ERROR_ADDITIONAL_CHASSIS_DETECTED and CONFIG_ERROR_MISSING_CHASSIS are set.

    Workaround: There is currently no workaround, and this issue will be fixed in a future NVOS release.

12. **GPU Status state missing from metric reports.**

    Status/Status URI is missing from `/redfish/v1/TelemetryService/MetricReports/HGX_ProcessorMetrics_0`.

    Workaround: Status can be read from telemetry service processor metrics (`/redfish/v1/Systems/HGX_Baseboard_0/Processors/GPU_{GpuId}#/Status/State`). This issue will be fixed in a future release.

13. **Disable access link retraining.**

    The GPU Access Link Retraining feature is disabled in this release. After upgrading, a compute tray reboot or GPU reset is required. With this release, a compute tray reboot or GPU reset is also required to bring GPU NVLink back online when a switch tray is rebooted or power-cycled for any reason. This feature will be re-enabled in a future NVOS release.

14. **Inconsistent GPU memory reported by NSM, nvidia-smi, and Redfish.**

    Multiple management interfaces report different total GPU memory values: NSM Type 3 (0x0C) and nvidia-smi (-q -d MEMORY) return 284,208 MiB, while Redfish TotalMemorySizeMiB returns 285,324 MiB. This occurs when querying inventory or memory capacity from NSM, nvidia-smi, MODS, and Redfish.

    Reporting inconsistency may cause confusion in monitoring, inventory reconciliation, or capacity planning. There is no data-loss or security impact identified.

    Workaround: There is currently no workaround, and this issue will be fixed in a future release.

15. **BMC PCIe link reset causes SBIOS exception.**

    When the PCIe link between the Grace CPU and BMC is reset at runtime, the system might take a fatal exception, which generates a Fatal CPER event. This issue only impacts the NVIDIA reference design but might impact partners.

    Workaround: Customers should avoid ungraceful PCIe link resets to the BMC while systems are operational. Partners that do not route PCIe to the BMC are not impacted by this issue.

16. **No mechanism to disable host IPMI interface.**

    This firmware release does not provide a mechanism to disable the host IPMI interface and any privileged user running on a compute tray can send IPMI commands to the BMC on the same compute tray.

    Workaround: A future BMC firmware release will provide an interface to restrict host IPMI commands. In the meantime, necessary access controls need to be implemented on the compute trays to limit host privileged access by users not authorized to access the BMC.

17. **Fan Control and Leak Detector user configured settings that are modified through Redfish PATCH API will reset to default after BMC firmware update.**

    Some Fan Control and Leak Detector properties are configurable through the PATCH method on Redfish. For example, the user is able to modify whether the BMC will shutdown the Chassis when a leak is detected through this API:

    ```
    curl -s -k -u ${USER}:${PASSWORD} https://${BMCIP}/redfish/v1/Chassis/
    ↪Chassis_0/Oem/Nvidia/Policies/LeakDetectionPolicy --request PATCH -d '{
    ↪"PolicyEnabled":true}'
    ```

    This setting will persist through BMC resets and tray power cycles, but will not currently survive a BMC firmware update.

    Workaround: Check and reapply any desired settings after a BMC firmware update.

18. **The Redfish Firmware Inventory API intermittently fails to fetch certain firmware endpoints.**

    The following Redfish API to retrieve the current FirmwareInventory:

    ```
    curl -s -k -u ${USER}:${PASSWORD} https://${BMCIP}/redfish/v1/
    ↪UpdateService/FirmwareInventory
    ```

Would be missing some firmware inventory endpoints on rare occasions. The cause has been identified and the fix will be included in a future BMC release.

Workaround: Re-run the same Redfish API.

19. **Liteon PowerShelf FW Update Task Monitoring Does Not Fully Track Progress for PMC and PSU Firmware.**

    During Liteon PMC firmware updates performed in Immediate mode, the update process does not report task progress. While the update is in progress, the BMC becomes temporarily inaccessible, which prevents accurate monitoring.

    A similar limitation exists during PSU firmware updates, where task monitoring is not fully reliable due to an indexing issue from 0–5 to 1–6 in PMC FW 1.3.10.

    It's important to note that the firmware updates themselves complete successfully for both PSU and PMC FW. The issue is specifically with task monitoring, which can affect users who rely on automation or scripted checks. This limitation is known in Liteon PMC firmware versions 1.3.10 and 1.3.9.

    Workaround: There is currently no definitive workaround. Users must wait approximately 10 minutes for the PMC to reboot and the BMC to become accessible again after initiating the update. The task monitoring issue is expected to be resolved in the upcoming Liteon PMC firmware release 1.3.11.

20. **[5770595] Leak detector voltage values read through the compute tray BMC via ipmi are clamped to 0.165 and 1.815.**

    Workaround: In order to read the correct voltage value if the sensor is outside of that range, read the sensor value through Redfish under `Chassis_0/Sensors`.

    Customers are always advised to use the standard Leak Detection methods under `/redfish/v1/Chassis/Chassis_0/ThermalSubsystem/LeakDetection/LeakDetectors` for determining if a leak is present rather than reading the voltage value of the sensor directly.

Chapter 8. Notices

8.1. Notice

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