

# **NVIDIA DOCA Core**

Programming Guide

# **Table of Contents**

Chapter 1. Introduction	1
Chapter 2. Prerequisites	3
Chapter 3. Architecture	4
3.1. General	4
3.1.1. doca_error_t	4
3.1.2. Generic Structures/Enum	4
3.2. DOCA Device	
3.2.1. Local Device and Representor	5
3.2.1.1. Local Device and Representor Matching	7
3.2.2. Expected Flow	7
3.3. DOCA Memory Subsystem	8
3.3.1. Requirements and Considerations	9
3.3.2. doca_buf/doca_buf_inventory	10
3.3.3. Example Flow	10
3.3.4. doca_mmap	12
3.4. DOCA Execution Model	13
3.4.1. Requirements and Considerations	
3.4.2. DOCA Context	15
3.4.3. DOCA WorkQ	16
3.4.4. Polling Mode	16
3.4.5. Event-driven Mode	
3.4.6. Job Error Handling	
3.5. Object Life Cycle	19
Chapter 4. Compatibility	21

# Chapter 1. Introduction

DOCA core objects provide a unified and holistic interface for application developers to interact with various DOCA libraries. The DOCA Core API and objects bring a standardized flow and building blocks for applications to build upon while hiding the internal details of dealing with hardware and other software components. DOCA core is designed to give the right level of abstraction while maintaining performance.

DOCA core exposes C-language API to applications writes and users must include the right header file to use according to the DOCA core facilities needed for their application.

DOCA core can be divided into the following software modules:

DOCA Core Module	Description
General	<ul> <li>DOCA core enumerations and basic structures</li> <li>Header files - doca_error.h, doca_types.h</li> </ul>
Device Handling	<ul> <li>Queries device information (host-side and DPU) and device capabilities (e.g., device's PCIe BDF address)</li> </ul>
	► On DPU
	<ul><li>Gets local DPU devices</li></ul>
	<ul><li>Gets representors list (representing host local devices)</li><li>On host</li></ul>
	► Gets local devices
	<ul> <li>Queries device capabilities and library capabilities</li> </ul>
	<ul> <li>Opens and uses the selected device representor</li> </ul>
	<ul> <li>Relevant entities - doca_devinfo, doca_devinfo_rep, doca_dev, doca_dev_rep</li> </ul>
	► Header files - doca_dev.h
	Note: There is a symmetry between device entities on host and its

DOCA Core Module	Description		
	representor (on the DPU). The convention of adding rep to the API or the object hints that it is representor-specific.		
Memory Management	<ul> <li>Handles optimized memory pools to be used by applications and enables sharing resources between DOCA libraries (while hiding HW related technicalities)</li> <li>Data buffer services (e.g., linked list of buffers to support scatter-gather list)</li> </ul>		
	Maps host memory to the DPU for direct access		
	<ul> <li>Relevant entities - doca_buf, doca_mmap, doca_inventory</li> </ul>		
	Header files - doca_buf.h, doca_buf_inventory.h, doca_mmap.h		
Progress Engine and Job Execution	<ul> <li>Enables submitting jobs to DOCA libraries and track job progress (supports both polling mode and event-driven mode)</li> </ul>		
	Relevant entities - doca_ctx, doca_job, doca_event, doca_event_handle_t, doca_workq		
	► Header files - doca_ctx.h		

The following sections describe DOCA core's architecture and sub-systems along with some basic flows that help users get started using DOCA Core.

NVIDIA DOCA Core MLNX-15-060542 \_v1.5.1 | 2

# Chapter 2. Prerequisites

DOCA Core objects are supported on the DPU target and the host machine. Both must meet the following prerequisites:

- ▶ DOCA version 1.5 or greater
- ▶ BlueField software 3.9.3 or greater
- ► Firmware version 24.35.1012 or greater

NVIDIA DOCA Core MLNX-15-060542 \_v1.5.1 | 3

# Chapter 3. Architecture

The following sections describe the architecture for the various DOCA core software modules.

## 3.1. General

#### 3.1.1. doca error t

All DOCA APIs return the status in the form of doca error.

```
typedef enum doca error {
          DOCA SUCCESS,
          DOCA_ERROR_UNKNOWN,
          DOCA ERROR NOT PERMITTED,
                                                   /**< Operation not permitted */
         DOCA ERROR IN USE,
                                                   /**< Resource already in use */
                                                  /**< Operation not supported */
         DOCA ERROR NOT SUPPORTED,
         DOCA ERROR AGAIN,
                                                   /**< Resource temporarily unavailable, try
 again */
         DOCA ERROR INVALID VALUE,
                                                   /**< Invalid input */
         DOCA ERROR NO MEMORY,
                                                   /**< Memory allocation failure */
         DOCA ERROR INITIALIZATION, /**< Resource initialization failure */
DOCA ERROR TIME OUT, /**< Timer expired waiting for resource
                                                  /**< Timer expired waiting for resource */
         DOCA_ERROR_SHUTDOWN, /**< Shut down in process or completed */
DOCA_ERROR_CONNECTION_RESET, /**< Connection reset by peer */
DOCA_ERROR_CONNECTION_ABORTED, /**< Connection aborted */
         DOCA_ERROR_CONNECTION_INPROGRESS, /**< Connection in progress */
         DOCA_ERROR_NOT_CONNECTED, /**< Not Connected */
         DOCA_ERROR_NO_LOCK,
DOCA_ERROR_NOT_FOUND,
DOCA_ERROR_IO_FAILED,
                                                    /**< Unable to acquire required lock */
                                                   /**< Resource Not Found */
                                                   /**< Input/Output Operation Failed */</pre>
                                                  /**< Bad State */
         DOCA ERROR BAD STATE,
         DOCA_ERROR_UNSUPPORTED_VERSION, /**< Unsupported version */
         DOCA ERROR OPERATING SYSTEM, /**< Operating system call failure */
DOCA ERROR DRIVER, /**< DOCA Driver call failure */
         DOCA_ERROR_DRIVER,
DOCA_ERROR_UNEXPECTED,
                                                   /**< DOCA Driver call failure */
                                                    /**< An unexpected scenario was detected
} doca error t;
```

#### Generic Structures/Enum

The following types are common across all device types in the DOCA Core API.

```
union doca data {
        void *ptr;
        uint64 t u64;
};
enum doca access flags {
```

```
DOCA_ACCESS_LOCAL_READ = 0,
DOCA_ACCESS_LOCAL_WRITE = 1,
DOCA_ACCESS_REMOTE_WRITE = (1 << 1),
DOCA_ACCESS_REMOTE_READ = (1 << 2),
DOCA_ACCESS_REMOTE_ATOMIC = (1 << 3),

};

enum doca_pci_func_type {
    DOCA_PCI_FUNC_PF = 0, /* physical function */
    DOCA_PCI_FUNC_VF, /* virtual function */
    DOCA_PCI_FUNC_SF, /* sub function */
};
```

## 3.2. DOCA Device

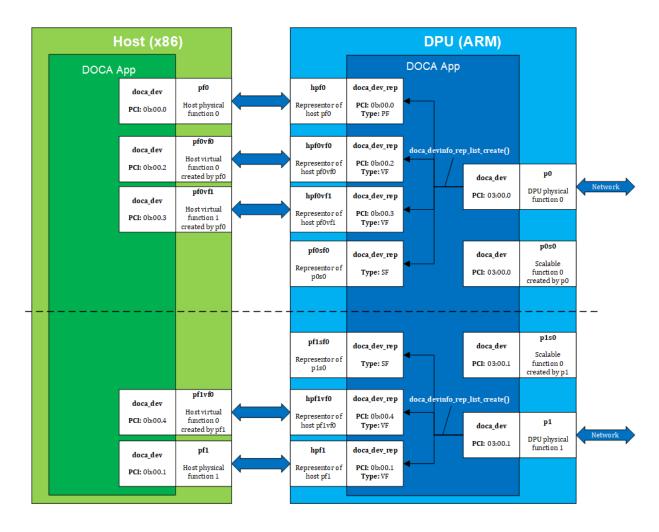
### 3.2.1. Local Device and Representor

The DOCA device represents an available processing unit backed by hardware or software implementation. The DOCA device exposes its properties to help an application in choosing the right device(s). DOCA Core supports two device types:

- Local device this is an actual device exposed in the local system (DPU or host) and can perform DOCA library processing jobs (can be a hardware device or device emulation)
- ▶ Representor device this is a representation of a local device. The local device is usually on the host (except for SFs) and the representor is always on the DPU side (a proxy on the DPU for the host-side device).

The following figure provides an example topology:

NVIDIA DOCA Core MLNX-15-060542 v1.5.1 | 5



The diagrams shows a DPU (on the right side of the figure) connected to a host (on the left side of the figure). The host topology consists of two physical functions (PF0 and PF1). Furthermore, PF0 has two child virtual functions, VF0 and VF1. PF1 has only one VF associated with it, VF0. Using the DOCA SDK API, the user gets these five devices as local devices on the host.

The DPU side has a representor-device per each host function in a 1-to-1 relation (e.g., hpf0 is the representor device for the host's pf0 device and so on) as well as a representor for each SF function such that both the SF and its representor reside in the DPU.

If the user queries local devices on the DPU side (not representor devices), they get the two (in this example) DPU PFs, p0 and p1. These two DPU local devices are the parent devices for:

- 7 representor devices
  - ▶ 5 representor devices shown as arrows to/from the host (devices with the prefix hpf\*) in the diagram
  - ▶ 2 representor devices for the SF devices, pf0sf0 and pf1sf0
- 2 local SF devices (not the SF representors), p0s0 and p1s0

In the diagram, the topology is split into 2 parts (see dotted line), each part is represented by a DPU physical device, p0 and p1, each of which is responsible for creating all other local devices (host PFs, host VFs, and DPU SFs). As such, the DPU physical device can be referred to as the parent device of the other devices and would have access to the representor of every other function (via doca\_devinfo\_rep\_list\_create).

### 3.2.1.1. Local Device and Representor Matching

Based on the diagram in section <u>Local Device and Representor</u>, the mmap export APIs can be used as follows:

Device to Select on Host When Using doca_mmap_export()	DPU Matching Representor	Device to Select on DPU When Using doca_mmap_from_export()
pf0 - 0b:00.0	hpf0 - 0b:00.0	p0 - 03:00.0
pf0vf0 - 0b:00.2	hpf0vf0 - 0b:00.2	
pf0vf1 - 0b:00.3	hpf0vf1 - 0b:00.3	
pf1 - 0b:00.1	hpf1 - 0b:00.1	p1 - 03:00.1
pf1vf0 - 0b:00.4	hpf1vf0 - 0b:00.4	

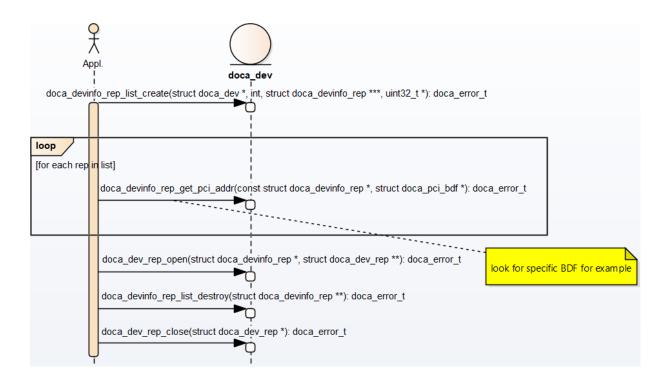
## 3.2.2. Expected Flow

To work with DOCA libraries or DOCA Core objects, the application must open and use a representor device on the DPU. Before it can open the representor device and use it, the application needs tools to allow it to select the appropriate representor device with the necessary capabilities. The DOCA Core API provides a wide range of device capabilities to help the application select the right device pair (device and its DPU representor). The flow is as follows:

- 1. List all representor devices on DPU.
- 2. Select one with the required capabilities.
- 3. Open this representor and use it.

As mentioned previously, the DOCA Core API is able to identify devices and their representors that have a unique property (e.g., the BDF address, the same BDF for the device and its DPU representor).

NVIDIA DOCA Core MLNX-15-060542 v1.5.1 | 7



- 1. The application "knows" which device it wants to use (e.g., by its PCIe BDF address). On the host, it can be done using DOCA Core API or OS services.
- 2. On the DPU side, the application gets a list of device representors for a specific DPU local device.
- 3. Select a specific doca\_devinfo\_rep to work with according to one of its properties. This example looks for a specific PCIe address.
- 4. Once the doca devinfo rep that suites the user's needs is found, open doca dev rep.
- 5. After the user opens the right device representor, they can close the doca\_devinfo list and continue working with doca\_dev\_rep. The application eventually has to close doca\_dev\_too.



Note: Regarding device property caching, the functions <code>doca\_devinfo\_list\_create</code> and <code>doca\_devinfo\_rep\_list\_create</code> provide a snapshot of the DOCA device properties when they are called. If any device's properties are changed dynamically (e.g., BDF address may change on bus reset), the device properties that those functions return would not reflect this change. One should call them again to get the updated properties of the devices.

## 3.3. DOCA Memory Subsystem

DOCA memory subsystem is designed to optimize performance while keeping a minimal memory footprint (to facilitate scalability) as main design goals. DOCA memory is has two main components.

doca\_buf - this is the data buffer descriptor. That is, it is not the actual data buffer, rather it is a descriptor that holds metadata on the "pointed" data buffer.

NVIDIA DOCA Core MLNX-15-060542 v1.5.1 | 8

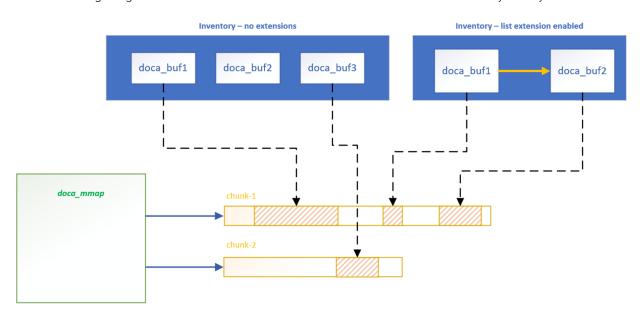
doca mmap - this is the data buffers pool (chunks) which are pointed at by doca buf. The application populates this memory pool with buffers/chunks and maps them to devices that must access the data.

As the doca mmap serves as the memory pool for data buffers, there is also an entity called doca buf inventorywhich serves as a pool of doca buf with same characteristics (see more in section <u>doca buf/doca buf inventory</u>). As all DOCA entities, memory subsystem objects are opaque and can be instantiated by DOCA SDK only.

One of the critical requirements from doca buf is to minimize its size so programs would not run into a lack of memory or scalability issues. For that purpose, DOCA features an extension support for doca buf inventory which means that the application can assign specific extensions to each doca buf inventoryit creates (can also bitwise OR extensions). By default, the minimal doca buf structure is used without any extensions.

An example for extension is LINKED\_LIST (see the doca buf extension enumeration in doca buf.h) which allows the application to chain several doca bufs and create a linked list of doca buf (which can be used for scatter/gather scenarios). All doca bufs originating from the same inventory have the same characteristics (i.e., extensions).

The following diagram shows the various modules within the DOCA memory subsystem.



In the diagram, you may see two doca\_buf\_inventorys. The left one has no extensions while right has a linked list extension enabled which enables chaining doca buf1 and doca buf2. Each doca buf points to a portion of the memory buffer which is part of a doca mmap. The MMAP is populated with two memory buffers, chunk-1 and chunk-2.

## Requirements and Considerations

- The DOCA memory subsystem mandates the usage of pools as opposed to dynamic allocation
  - Pool for doca buf → doca buf inventory

- ▶ Pool for data memory → doca mmap
- The memory buffers in the mmap can be mapped to one device or more
- doca bufpoints to a specific memory buffer (or part of it) and holds metadata for that buffer (e.q., 1key)
- ▶ The internals of mapping and working with the device (e.g., memory registrations) is hidden from the application
- ▶ The host-mapped memory chunk can be accessed by DPU

#### doca buf/doca buf inventory 3.3.2.

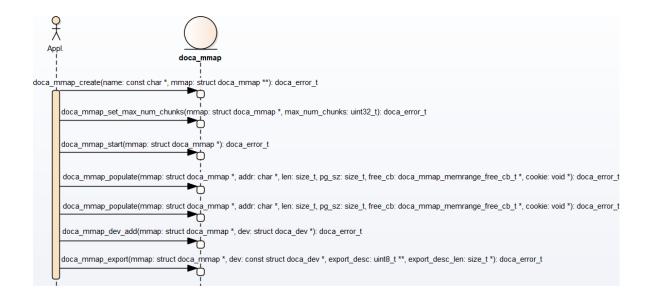
doca buf is opaque and can only be allocated using DOCA API. As previously mentioned, it is the descriptor that points to a specific (portion or entire) mmap buffer (chunk). doca buf inventory is a pool of doca bufs that the application creates. Still, the doca bufs in such an inventory are placeholders and do not point to the data. When the application desires to assign a doca buf to a specific data buffer, it calls the doca buf inventory buf by addr API.

When enabling specific extensions for an inventory, the application must check to make sure that the relevant contexts indeed support the relevant extension. If the context does not support the requested extensions, the application must not pass doca buf with these extensions to the context. For example, if the application wishes to use the linked list extension and concatenate several doca buf to a scatter-gather list, it is expected to make sure the application indeed supports a linked list extension by calling doca dma get max list buf num elem (this example checks linked-list support for DMA).

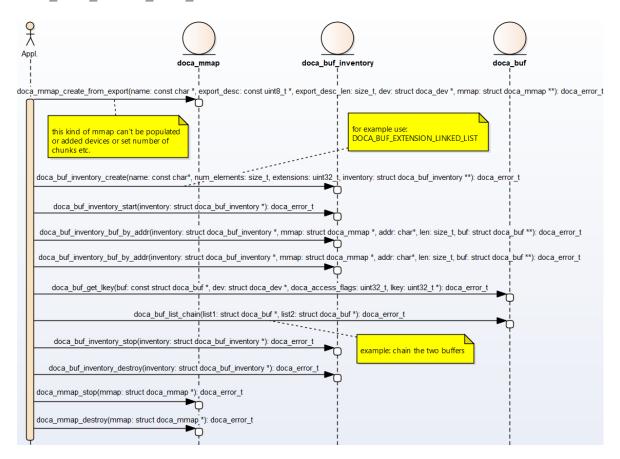
#### 3.3.3. Example Flow

The following is a simplified example of the steps expected for exporting the host mmap to the DPU to be used by DOCA for direct access to the host memory (e.g. for DMA):

1. Create mmap on the host (see section Local Device and Representor Matching for information on how to choose the doca dev to add to mmap). This example populates 2 data buffers and adds a single doca dev to the mmap and exports it so DPU can use it.



2. Import to the DPU (e.g., use the mmap descriptor output parameter as input to doca mmap create from export).



NVIDIA DOCA Core

## 3.3.4. doca\_mmap

doca\_mmap is more than just a pool for data buffers (or chunks), it hides a lot of details (e.g., RDMA technicalities, device handling, etc.) from the application developer in regards to while giving the right level of abstraction to software using it. doca\_mmap is the best way to share memory between the host and the DPU so the DPU can have direct access to the host-side memory.

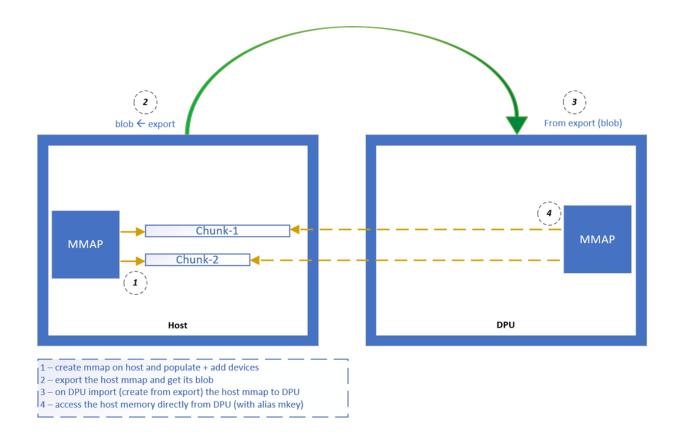
DOCA SDK supports several types of mmap that help with different use cases:

- ► Local mmap this is the basic type of mmap which maps local buffers to the local device(s)
  - 1. The application creates and starts doca mmap.
  - 2. The application adds devices and populates buffers to the mmap granting the devices access to the buffers.



**Note:** Populating and adding devices can be done at any order and the mmap will maintain the mapping of every buffer to every device.

- 3. If the application desires the DPU to have access (zero-copy) to mmap buffers, it must call doca\_mmap\_export() and pass the resulting blob to the DPU that could import this mmap and have direct access to the host memory.
- From the export mmap (imported) this mmap can be created on the DPU only
  - 1. The application calls doca\_mmap\_create\_from\_export and gives the blob it got by calling doca mmap export on the host-side mmap.
  - 2. Now the application can create doca\_buf to point to this imported mmap and have direct access to the host's memory.



#### **DOCA Execution Model** 34

In DOCA, the workload involves transforming source data to destination data. The basic transformation is a DMA operation on the data which simply moves data from one memory location to another. Other operations involve calculating the SHA value of the source data and writing it to the destination.

The workload can be broken into 3 steps:

- 1. Read source data (doca buf see memory subsystem).
- 2. Apply an operation on the read data (handled by a dedicated hardware accelerator).
- 3. Write the result of the operation to the destination (doca buf see memory subsystem).

Each such operation is referred to as a job (doca job).

Jobs describe operations that an application would like to submit to DOCA (hardware or DPU). To do so, the application requires a means of communicating with the hardware/DPU. This is where the doca workq comes into play. The WorkQ is a per-thread object used to queue jobs to offload to DOCA and eventually receive their completion status.

doca workq introduces three main operations:

- 1. Submission of jobs.
- 2. Checking progress/status of submitted jobs.
- 3. Querying job completion status.

A workload can be split into many different jobs that can be executed on different threads, each thread represented by a different WorkQ. Each job must be associated to some context, where the context defines the type of job to be done.

A context can be obtained from some libraries within the DOCA SDK. For example, to submit DMA jobs, a DMA context can be acquired from doca dma.h, whereas SHA context can be obtained using doca sha.h. Each such context may allow submission of several job types.

A job is considered asynchronous in that once an application submits a job, the DOCA execution engine (hardware or DPU) would start processing it, and the application can continue to do some other processing until the hardware finishes. To keep track of which job has finished, there are two modes of operation: polling mode and event-driven mode.

#### 3.4.1. Requirements and Considerations

In DOCA, the workload involves transforming source data to destination data. The basic transformation is a DMA operation on the data which simply moves data from one memory location to another. Other operations involve calculating the SHA value of the source data and writing it to the destination.

The workload can be broken into 3 steps:

- 1. Read source data (doca buf see memory subsystem).
- 2. Apply an operation on the read data (handled by a dedicated hardware accelerator).
- 3. Write the result of the operation to the destination (doca buf see memory subsystem).

Each such operation is referred to as a job (doca job).

Jobs describe operations that an application would like to submit to DOCA (hardware or DPU). To do so, the application requires a means of communicating with the hardware/DPU. This is where the doca workg comes into play. The WorkQ is a per-thread object used to queue jobs to offload to DOCA and eventually receive their completion status.

doca workq introduces three main operations:

- 1. Submission of jobs.
- 2. Checking progress/status of submitted jobs.
- 3. Querying job completion status.

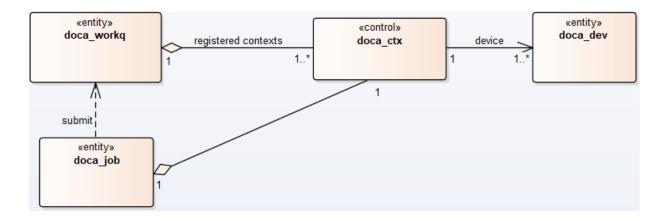
A workload can be split into many different jobs that can be executed on different threads, each thread represented by a different WorkQ. Each job must be associated to some context, where the context defines the type of job to be done.

A context can be obtained from some libraries within the DOCA SDK. For example, to submit DMA jobs, a DMA context can be acquired from doca dma.h, whereas SHA context can be obtained using doca sha.h. Each such context may allow submission of several job types.

A job is considered asynchronous in that once an application submits a job, the DOCA execution engine (hardware or DPU) would start processing it, and the application can continue to do some other processing until the hardware finishes. To keep track of which job has finished, there are two modes of operation: polling mode and blocking mode.

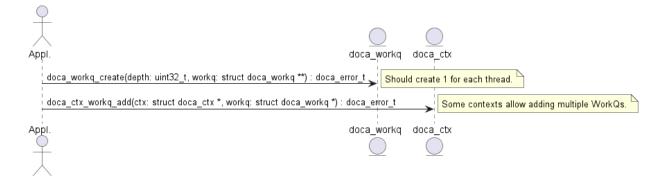
#### 3.4.2. DOCA Context

doca ctx represents an instance of a specific DOCA library (e.g., DMA, SHA). Before submitting jobs to the WorkQ for execution, the job must be associated to a specific context that executes the job. The application is expected to associate (i.e., add) WorkQ with that context. Adding a WorkQ to a context allows submitting a job to the WorkQ using that context. Context represents a set of configurations including the job type and the device that runs it such that each job submitted to the WorkQ is associated with a context that has already been added. The following diagram shows the high-level (domain model) relations between various DOCA core entities.



- 1. doca job is associated to a relevant doca ctx that executes the job (with the help of the relevant doca dev).
- 2. doca job, after it is initialized, is submitted to doca workg for execution.
- 3. doca ctxs are added to the doca workq, once a doca job is queued to doca workq, it is submitted to the doca ctx that is associated with that job type in this WorkQ.

The following diagram describes the initialization sequence of a context:



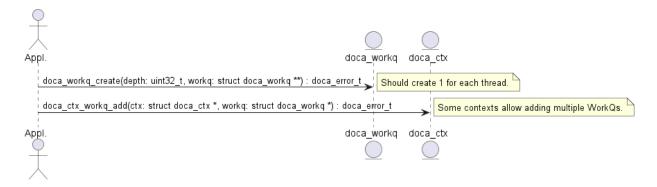
After the context is started, it can be used to enable the submission of jobs to a WorkQ based on the types of jobs that the context supports. See section TBD for more information.

Context is a thread-safe object. Some contexts can be used across multiple WorkQs while others can only be used on 1. Please refer to documentation of the specific context for specific information per context (e.g., doca dma).

#### 3 4 3 DOCA WorkQ

doca workq is a logical representation of DOCA thread of execution (non-thread-safe). WorkQ is used to submit jobs to the relevant context/library (hardware offload most of the time) and query the job's completion status. To start submitting jobs, however, the WorkQ must be configured to accept that type of job. Each WorkQ can be configured to accept any number of job types depending on how it initialized.

The following diagram describes the initialization flow of the WorkQ:



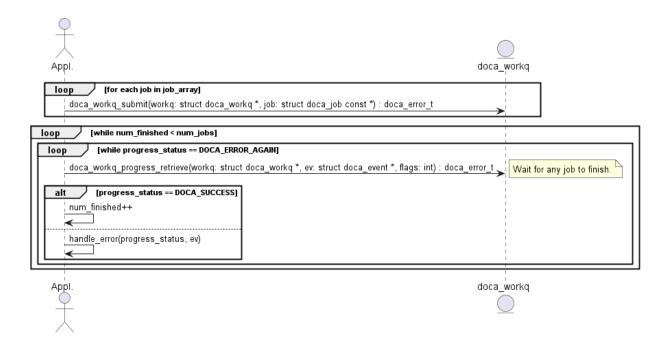
After the WorkQ has been created and added to a context, it can start accepting jobs that the context defines. Refer to the context documentation to find details such as whether the context supports adding multiple doca workgs to the same context and what jobs can be submitted using the context.

Please note that the WorkQ can be added to multiple contexts. Such contexts can be of the same type or of different types. This allows submitting different job types to the same WorkQ and waiting for any of them to finish from the same place/thread.

### 3.4.4. Polling Mode

In this mode, the application submits a job and then does busy-wait to find out when the job has completed. Polling mode is enabled by default.

The following diagram demonstrates this sequence:



- 1. The application submits all jobs (one or more) and tracks the number of completed jobs to know if all jobs are done.
- 2. The application waits for a job to finish.
  - a). If doca workq progress retrieve() returns DOCA ERROR AGAIN, it means that jobs are still running (i.e. no result).
  - b). Once a job is done, DOCA SUCCESS is returned from doca workq progress retrieve().
  - c). If another status is returned, that means an error has occurred (see section Job Error Handling).
- 3. Once a job has finished, the counter for tracking the number of finished jobs is updated.

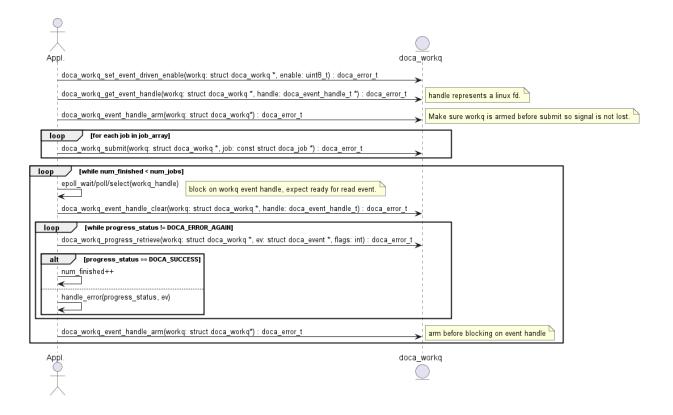


Note: In this mode the application is always using the CPU even when it is doing nothing (during busy-wait).

#### 3.4.5. **Event-driven Mode**

In this mode, the application submits a job and then waits for a signal to be received before querying the status.

The following diagram shows this sequence:



- 1. The application enables event-driven mode of the WorkQ. If this step fails (DOCA ERROR NOT SUPPORTED), it means that one or more of the contexts associated with the WorkQ (via doca ctx workq add) do not support this mode. To find out if a context supports this event-driven mode, refer to the context documentation. Alternatively, the API doca ctx get event driven supported() can be called during runtime.
- 2. The application gets an event handle from the doca workg representing a Linux file descriptor which is used to signal the application that some work has finished.
- 3. The application then arms the WorkQ.



Note: This must be done every time an application is interested in receiving a signal from the WorkQ.

- 4. The application submits a job to the WorkQ.
- 5. The application waits (e.g., Linux epoll/select) for a signal to be received on the workq-fd.
- 6. The application clears the received events, notifying the WorkQ that a signal has been received and allowing it to do some event handling.
- 7. The application attempts to retrieve a result from the WorkQ.



Note: There is no guarantee that the call to doca workq progress retrieve would return a job completion event but the WorkQ can continue the job.

- 8. Increment the number of finished jobs if successful or handle error.
- 9. Arm the WorkQ to receive the next signal.
- 10. Repeat steps 5-9 until all jobs are finished.

### 3.4.6. Job Error Handling

After a job is submitted successfully, consequent calls to doca workq progress retrieve may fail (i.e., return different status from DOCA SUCCESS OF DOCA ERROR AGAIN). In this case, the error is split into 2 main categories:

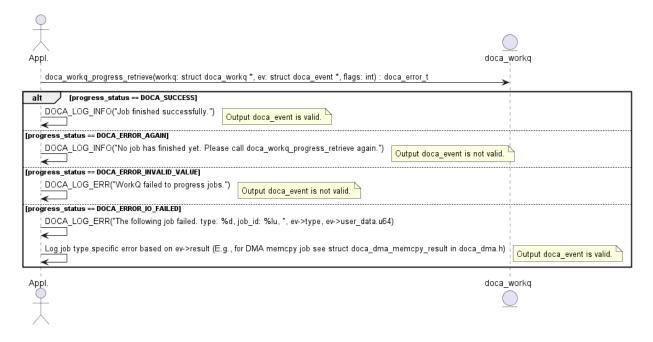
1. DOCA ERROR INVALID VALUE

This means that some error has occurred within the WorkQ that is not related to any submitted job. This can happen due to the application passing invalid arguments or to some objects that have been previously provided (e.g., a doca ctx that was associated using doca ctx workg add) getting corrupted. In this scenario, the output parameter of type doca event is not valid and no more information is given about the error.

2. DOCA ERROR IO FAILED

This means that a specific job has failed where the output variable of type doca event is valid and can be used to trace the exact job that failed. Additional error code explaining the exact failure reason is given. To find the exact error, refer to the documentation of the context that provides the job type (e.g., if the job is DMA memcpy, then refer to doca dma.h).

The following diagram shows how an application is expected to handle error from doca workq progress retrieve:



## Object Life Cycle

Most DOCA core objects share the same handling model in which:

- 1. The object is allocated by DOCA as it is opaque for the application (e.g., doca\_buf\_inventory\_create, doca\_mmap\_create).
- 2. The application initializes the object and sets the desired properties (e.g., doca\_mmap\_set\_max\_num\_chunks).
- 3. The object is started and no configuration or attribute change is allowed (e.g., doca\_buf\_inventory\_start, doca\_mmap\_start).
- 4. The object is used.
- 5. The object is stopped and deleted (e.g., doca\_buf\_inventory\_stop  $\rightarrow$  doca\_buf\_inventory\_destroy, doca\_mmap\_stop  $\rightarrow$  doca\_mmap\_destroy).

# Chapter 4. Compatibility

An application that uses the hardware relies on a subset of features to be present for it to be able to function. As such, it is customary to check if the subset of features exists. The application may also need to identify the specific hardware resource to work with based on specific properties. The same applies for an application that uses a DOCA library.

It is up to the application to:

- ▶ Check which library's APIs are supported for a given doca devinfo.
- Configure the library context through the dedicated API according to the library's limitations.
- Check library's configuration limitations.

DOCA capabilities is a set of APIs (DOCA library level) with a common look and feel to achieve this.

#### For example:

- A hotplug (of emulated PCIe functions) oriented application can check if a specific DOCA device information structure enables hotplugging emulated devices, by calling:
  - doca\_error\_t doca\_devinfo\_get\_is\_hotplug\_manager\_supported(const struct
    doca\_devinfo \*devinfo, uint8\_t \*is\_hotplug\_manager);
- An application that works with DOCA memory map to be shared between the host and DPU must export the doca mmap from the host and import it from the DPU. Before starting the workflow, the application can check if those operations are supported for a given doca devinfo using the following APIs:

```
doca_error_t doca_devinfo_get_is_mmap_export_supported(const struct doca_devinfo *devinfo, uint8_t *mmap_export);
doca_error_t doca_devinfo_get_is_mmap_from_export_supported(const struct doca_devinfo *devinfo, uint8_t *from_export);
```

▶ When working with DOCA inventory extensions (e.g., a LINKED LIST extension which allows chaining several doca bufs in a linked-list manner) an application can check if the list extension is supported by doca buf inventory with:

```
doca_error_t doca_buf_inventory_get_list_supported(struct doca_buf_inventory
*inventory, uint8 t *list supported);
```

#### Notice

This document is provided for information purposes only and shall not be regarded as a warranty of a certain functionality, condition, or quality of a product. NVIDIA Corporation nor any of its direct or indirect subsidiaries and affiliates (collectively: "NVIDIA") make no representations or warranties, expressed or implied, as to the accuracy or completeness of the information contained in this document and assume no responsibility for any errors contained herein. NVIDIA shall have no liability for the consequences or use of such information or for any infringement of patents or other rights of third parties that may result from its use. This document is not a commitment to develop, release, or deliver any Material (defined below), code, or functionality.

NVIDIA reserves the right to make corrections, modifications, enhancements, improvements, and any other changes to this document, at any time without notice.

Customer should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

NVIDIA products are sold subject to the NVIDIA standard terms and conditions of sale supplied at the time of order acknowledgement, unless otherwise agreed in an individual sales agreement signed by authorized representatives of NVIDIA and customer ("Terms of Sale"). NVIDIA hereby expressly objects to applying any customer general terms and conditions with regards to the purchase of the NVIDIA product referenced in this document. No contractual obligations are formed either directly or indirectly by this document.

NVIDIA products are not designed, authorized, or warranted to be suitable for use in medical, military, aircraft, space, or life support equipment, nor in applications where failure or malfunction of the NVIDIA product can reasonably be expected to result in personal injury, death, or property or environmental damage. NVIDIA accepts no liability for inclusion and/or use of NVIDIA products in such equipment or applications and therefore such inclusion and/or use is at customer's own risk.

NVIDIA makes no representation or warranty that products based on this document will be suitable for any specified use. Testing of all parameters of each product is not necessarily performed by NVIDIA. It is customer's sole responsibility to evaluate and determine the applicability of any information contained in this document, ensure the product is suitable and fit for the application planned by customer, and perform the necessary testing for the application in order to avoid a default of the application or the product. Weaknesses in customer's product designs may affect the quality and reliability of the NVIDIA product and may result in additional or different conditions and/or requirements beyond those contained in this document. NVIDIA accepts no liability related to any default, damage, costs, or problem which may be based on or attributable to: (i) the use of the NVIDIA product in any manner that is contrary to this document or (ii) customer product designs.

No license, either expressed or implied, is granted under any NVIDIA patent right, copyright, or other NVIDIA intellectual property right under this document. Information published by NVIDIA regarding third-party products or services does not constitute a license from NVIDIA to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property rights of the third party, or a license from NVIDIA under the patents or other intellectual property rights of NVIDIA.

Reproduction of information in this document is permissible only if approved in advance by NVIDIA in writing, reproduced without alteration and in full compliance with all applicable export laws and regulations, and accompanied by all associated conditions, limitations, and notices.

THIS DOCUMENT AND ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS, AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY, OR OTHERWISE WITH RESPECT TO THE MATERIALS, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES OF NONINFRINGEMENT, MERCHANTABILITY, AND FITNESS FOR A PARTICULAR PURPOSE. TO THE EXTENT NOT PROHIBITED BY LAW, IN NO EVENT WILL NVIDIA BE LIABLE FOR ANY DAMAGES, INCLUDING WITHOUT LIMITATION ANY DIRECT, INDIRECT, SPECIAL, INCIDENTAL, PUNITIVE, OR CONSEQUENTIAL DAMAGES, HOWEVER CAUSED AND REGARDLESS OF THE THEORY OF LIABILITY, ARISING OUT OF ANY USE OF THIS DOCUMENT, EVEN IF NVIDIA HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. Notwithstanding any damages that customer might incur for any reason whatsoever, NVIDIA's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms of Sale for the product.

#### Trademarks

NVIDIA, the NVIDIA logo, and Mellanox are trademarks and/or registered trademarks of Mellanox Technologies Ltd. and/or NVIDIA Corporation in the U.S. and in other countries. The registered trademark Linux® is used pursuant to a sublicense from the Linux Foundation, the exclusive licensee of Linus Torvalds, owner of the mark on a world-wide basis. Other company and product names may be trademarks of the respective companies with which they are associated.

#### Copyright

© 2022 NVIDIA Corporation & affiliates. All rights reserved.

