



NVIDIA DOCA

Glossary

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Chapter 1. Terms and Acronyms

Term	Description
ACE	AXI coherency extensions
ACPI	Advanced configuration and power interface
AMBA [®]	Advanced microcontroller bus architecture
ARB	Arbitrate
ATF	Arm-trusted firmware
AXI4	Advanced eXtensible Interface 4
BDF address	Bus, device, function address. This is the device's PCIe bus address to uniquely identify the specific device.
BERT	Boot error record table
BF_INST_DIR	The directory where the BlueField software is installed
BFB	BlueField bootstream
BMC	Board management controller
BSD	BlueField software distribution
BUF	Buffer
BSP	BlueField support package
CHI	Coherent hub interface; Arm [®] protocol used over the BlueField Skymesh specification
CL	Cache line
CMDQ	Command queue
CMO	Cache maintenance operation
COB	Collision buffer
DAT	Data
DEK	Data encryption key
DMA	Direct memory access
DOCA	DPU SDK
DPA	Data path accelerator; an auxiliary processor designed to accelerate data-path operations
DPDK	Data plane development kit
DPI	Deep packet inspection

Term	Description
DPU	Data processing unit, the third pillar of the data center with CPU and GPU
DVM	Distributed virtual memory
ECPF	Embedded CPU physical function
EMEM/EMI	External memory interface; block in the MSS which performs the actual read/write from the DDR device
eMMC	Embedded multi-media card
ESP	EFI system partition
ESP header	Encapsulating security payload
FIPS	Federal Information Processing Standards
FPGA	Field-programmable gate arrays
FS	File system
FW	Firmware
GDAKIN	GPUDirect Async Kernel-Initiated Network
GDB	GNU debugger
GPT	GUID partition table
HART	HW thread; a logical DPA processing unit
HCA	Host-channel adapter
HNF	Home node interface
Host	<p>When referring to "the host" this documentation is referring to the server host. When referring to the Arm based host, the documentation will specifically call out "Arm host".</p> <ul style="list-style-type: none"> ▶ Server host OS refers to the Host Server OS (Linux or Windows) ▶ Arm host refers to the AARCH64 Linux OS which is running on the BlueField Arm Cores
HW	Hardware
hwmon	Hardware monitoring
IB	InfiniBand
ICM	Interface configuration memory
IKE	Internet key exchange
IPMB	Intelligent platform management bus
IPMI	Intelligent platform management interface
KGDB	Kernel debugger
KGDBOC	Kernel debugger over console
LAN	Local area network

Term	Description
LAT	Latency
LCRD	Link credit
LSO	Large send offload
MMIO	Memory-mapped I/O
MSS	Memory subsystem
MST	Mellanox software tools
NAT	Network address translation
NIC	Network interface card
NIST	National Institute of Standards and Technology
OCD	On-chip debugger
OOB	Out-of-band
OS	Operating system
OVS	Open vSwitch
PCIe	PCI Express; peripheral component interconnect express
PF	Physical function
PK	Public key
PKA	Public key accelerator
POC	Point of coherence
RD	Read
RDMA	Remote direct memory access
RegEx	Regular expression
REQ	Request
RES	Response
RN	Request node
	RN-F – Fully coherent request node
	RN-D – IO coherent request node with DVM support
	RN-I – IO coherent request node
RNG	Random number generator/generation
RoCE	Ethernet and RDMA over Converged Ethernet
RQ	Receive queue
RShim	Random Shim
RX	Receive
SA	Security association
SBSA	Server base system architecture
SDK	Software development kit
SF	Sub-function or scalable function
SG	Scatter-gather

Term	Description
SHA	Secure hash algorithm
SMMU	System memory management unit
SNP	Snooping
SQ	Send queue
SR-IOV	Single-root IO virtualization
STL	Stall
Sync event	Synchronization event
TBU	Translation buffer unit
TIR	Transport interface receive
TIS	Transport interface send
TLS	Transport layer security
TRB	Trail buffer
TSO	TCP send offload
TSO	Total store order
TX	Transmit
UDS	Unix domain socket
UEFI	Unified extensible firmware interface
UPVS	UEFI persistent variable store
VF	Virtual function
VFE	Virtio full emulation
VM	Virtual machine
VPI	Virtual protocol interconnect
VST	Virtual switch tagging
WorkQ or workq	Work queue
WQE	Work queue element
WR	Write
WRDB	Write data buffer

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