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1.0 ABOUT THIS RELEASE

These Release Notes are for the following products:

- NVIDIA® DRIVE™ 5.0 Foundation Platform Development Kit (PDK) and Software Development Kit (SDK) for NVIDIA DRIVE™ PX 2

These release notes provide information on generic Virtualization and Foundation features. For a list of virtualization features, known issues, and implementation details specific to the various DRIVE 5.0 OS platforms in the current DRIVE 5.0 release, see the Release Notes for the corresponding DRIVE 5.0 OS.

Release Information

The following table describes the attributes of this product release. When you encounter these variables elsewhere in the release notes or in the Development Guide, use the provided value in place of the variable.

<table>
<thead>
<tr>
<th>Variable</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>&lt;os&gt;</td>
<td>linux</td>
<td>Designates the target’s operating system.</td>
</tr>
<tr>
<td>&lt;os_ver_host&gt;</td>
<td>16.04 (64-bit distributions)</td>
<td>Specifies the host Ubuntu operating system version.</td>
</tr>
<tr>
<td>&lt;ubuntu_codename&gt;</td>
<td>xenial</td>
<td>Specifies the code name for the host version of Ubuntu.</td>
</tr>
<tr>
<td>&lt;release_branch&gt;</td>
<td>embedded/5.0.10.3</td>
<td>Specifies the NVIDIA Git release branch used for the release.</td>
</tr>
<tr>
<td>&lt;branch&gt;</td>
<td>5.0.10.3</td>
<td>Specifies the NVIDIA release branch number used for the release.</td>
</tr>
<tr>
<td>&lt;build&gt;</td>
<td>12606092</td>
<td>Specifies the build ID.</td>
</tr>
<tr>
<td>&lt;product&gt;</td>
<td>drive-t186ref-foundation</td>
<td>Specifies the product name.</td>
</tr>
</tbody>
</table>
Platform Support with Quality Target

The following table lists the platforms supported in this release at the targeted quality level.

<table>
<thead>
<tr>
<th>Platform</th>
<th>Quality Target</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVIDIA DRIVE™ PX 2 ACr (P3407-a01)</td>
<td>Beta 3.0</td>
</tr>
<tr>
<td>NVIDIA DRIVE™ PX 2 ACh (P2379-C01/C02)</td>
<td>Beta 3.0</td>
</tr>
</tbody>
</table>

Hardware Information

The following tables describe the hardware attributes of the development platforms for this release. When you encounter these variables, elsewhere in the release notes or in the Development Guide, use the provided value in place of the variable.

**NVIDIA DRIVE™ PX 2 ACr (P3407-a01)**

<table>
<thead>
<tr>
<th>Variable</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>&lt;platform</td>
<td>ver&gt;</td>
<td>t186ref</td>
</tr>
<tr>
<td>&lt;board&gt;</td>
<td>p3407</td>
<td>Specifies the board name.</td>
</tr>
<tr>
<td>&lt;board_and_rev&gt;</td>
<td>p3407-t186</td>
<td>Specifies the board and revision. This information is used in flashing.</td>
</tr>
<tr>
<td>&lt;sku_id&gt;</td>
<td>0000</td>
<td>Specifies NVIDIA DRIVE PX 2 ACr. This variable appears in pathnames and may be a component in &lt;sku_info&gt;.</td>
</tr>
<tr>
<td>&lt;t-ver&gt; or &lt;t-arch&gt;</td>
<td>186</td>
<td>Specifies the Tegra version of Tegra architecture.</td>
</tr>
</tbody>
</table>

The NVIDIA DRIVE PX 2 ACr (P3407-a01) platform includes NVIDIA P3407 A01 SKU 0000. This board uses the NVIDIA® Tegra® Parker, and also supports:

- 8 GB LPDDR4, 64 GB eMMC, and 64 MB QSPI NOR memory
- Display output:
  - 1 mini-HDMI 2.0
- 8 GMSL camera inputs
- USB 2.0/3.0 Host ports
- Tegra – 2x CAN
- AURIX – 2x CAN, 2x LIN, and 2x Flex Ray

**NVIDIA DRIVE™ PX 2 ACh (P2379-C01/C02)**

<table>
<thead>
<tr>
<th>Variable</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>&lt;platform</td>
<td>ver&gt;</td>
<td>t186ref</td>
</tr>
<tr>
<td>&lt;board&gt;</td>
<td>p2379</td>
<td>Specifies the board name.</td>
</tr>
<tr>
<td>Variable</td>
<td>Value</td>
<td>Description</td>
</tr>
<tr>
<td>--------------------</td>
<td>------------------------------</td>
<td>-----------------------------------------------------------------------------</td>
</tr>
<tr>
<td><code>&lt;board_and_rev&gt;</code></td>
<td>p2379c01-t186a</td>
<td>Specifies the board and revision. This information is used in flashing.</td>
</tr>
<tr>
<td></td>
<td>p2379c01-t186b</td>
<td>The first expression is for flashing Tegra A; and the second is for flashing Tegra B.</td>
</tr>
<tr>
<td><code>&lt;sku_id&gt;</code></td>
<td>See list below</td>
<td>Specifies the P2379 SKU. This variable appears in pathnames and may be a component in <code>&lt;SKUINFO&gt;</code>.</td>
</tr>
<tr>
<td><code>&lt;SKUINFO&gt;</code></td>
<td>699-62379-0000-400</td>
<td>Identifies each variable component for a board. This value is used when flashing the board with the <code>-z</code> option. Check the label on your board to verify this value.</td>
</tr>
<tr>
<td><code>&lt;t-ver&gt;</code> or <code>&lt;t-arch&gt;</code></td>
<td>186</td>
<td>Specifies the Tegra version of Tegra architecture.</td>
</tr>
</tbody>
</table>

The NVIDIA DRIVE PX 2 ACh (P2379-C01/C02) platform (as opposed to PCB) SKUs include:

- P2379-C01 SKU 1001
- P2379-C02 SKU 1001
- P2379-C02 SKU 2001
- P2379-C02 SKU 2002
- P2379-C02 SKU 2121
- P2379-C02 SKU 2124

This board uses the NVIDIA® Tegra® Parker, and also supports:

- 8 GB LPDDR4, 64 GB eMMC, and 64 MB QSPI NOR memory per Tegra
- Display outputs:
  - 1 HDMI 2.0 per Tegra (2 total)
  - 1 DSI/FPDLink III (shared)
  - 1 HDMI/GMSL (shared)
- 12 GMSL camera inputs
- USB 2.0/3.0 Host ports
- Tegra – 1x CAN
- AURIX – 6x CAN, 4xLIN, and 2xFlexray ports
- NVIDIA P2779 Adapter Board to give access to Tegra and AURIX JTAG

### 1.1 WHAT’S NEW

- Use SDK Manager for updates.
1.2 TOP ISSUES FIXED SINCE LAST RELEASE

AURIX

The following Aurix related issues are resolved in this release.

<table>
<thead>
<tr>
<th>NVIDIA Bug Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>200438266</td>
<td>Issue with Aurix Firmware version name with respect to branch.</td>
</tr>
<tr>
<td>200439179</td>
<td>Aurix &quot;setip&quot; command fails to retain changed IP after issuing aurixreset on the target system.</td>
</tr>
</tbody>
</table>

Bootloader

The following bootloader related issues are resolved in this release.

<table>
<thead>
<tr>
<th>NVIDIA Bug Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>2099759</td>
<td>Bootburn should not erase emmc unconditionally.</td>
</tr>
</tbody>
</table>

Flashing

The following flashing related issues are resolved in this release.

<table>
<thead>
<tr>
<th>NVIDIA Bug Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>200433852</td>
<td>Error spews &quot;bootburn_sequencer.sh: line 350: [: missing `&quot;]&quot; observed while doing parallel flashing target system.</td>
</tr>
<tr>
<td>200340914</td>
<td>Parallel flash fails when AURIX console is released before timeout (20 seconds).</td>
</tr>
</tbody>
</table>

System

The following system related issues are resolved in this release.

<table>
<thead>
<tr>
<th>NVIDIA Bug Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>200418079</td>
<td>testing_client tests fail intermittently.</td>
</tr>
</tbody>
</table>

Virtualization

The following Virtualization related issues are resolved in this release.
<table>
<thead>
<tr>
<th>NVIDIA Bug Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>200400149</td>
<td>Intermittently target system stuck to boot while issuing reboot command in loop.</td>
</tr>
<tr>
<td>200382219</td>
<td>Issuing &quot;shutdown -h now&quot; command error seen &quot;Fatal programming error: mutex already held&quot; on tegra console.</td>
</tr>
<tr>
<td>200264246</td>
<td>Polling for watchdog timeout starts the watchdog timer.</td>
</tr>
<tr>
<td>200374910</td>
<td>testMultiTSG subtest fails on running nvgpu_sched_threads.</td>
</tr>
</tbody>
</table>

### 1.3 CONTENTS OF THIS RELEASE

This release includes SDK installer files archived in 7Zip files and additional platform-specific run files. Replace the `<branch>` and `<build>` variables below with the values shown in Section 1.0 above.

This release also includes SDK Manager installer files. For installation instructions see the SDK Manager documentation.

**SDK Releases**

- The DRIVE 5.0 Linux and DRIVE 5.0 Foundation Development Guides and References are in this ZIP. For guidance on installing the documentation, see To access the References content in these notes.

  NVIDIA_DRIVE_5.0_Linux_DPX_PDK_References.zip

- The following is the Foundation SDK installer run file:

  drive-t186ref-foundation-<branch>-<build>-release.run

- The following is the Open Source SDK tools installer run file:

  drive-t186ref-foundation-<branch>-<build>-toolchain.run

- The following is the Open Source code installer run file:

  drive-t186ref-foundation-oss-src.run

- AURIX firmware is available for the NVIDIA DRIVE PX 2 platform in the following files:

  Ach:
1.4 DOCUMENTATION IN THIS RELEASE

The following lists the documentation included in this release.

- **NVIDIA_DRIVE_5.0_Foundation_DPX2_Release_Notes_5.0.10.3.pdf**

  This document.

  In the SDK release, the document is available at [https://developer.nvidia.com/drive/](https://developer.nvidia.com/drive/). Go to the Download folder.

- **NVIDIA_DRIVE_5.0_Linux_DPX_SDK_References.zip**

  The ZIP file that contains HTML-based API documentation, the Development Guide, and as needed additional PDF documents including open-source and 3rd party license information.

- **drive-t186ref-foundation-<branch>-<build>-oss-src.run**

  The RUN file that contains HTML-based Open Source licensing information and source rebuild instructions.

- **drive-t186ref-foundation/ubuntu-rootfs-licenses.txt**

  The TXT file that contains the full text of the common licenses of the Ubuntu 14.04 root file system (based on ubuntu-core).

- **EB-DrivePX_Software_User_Guide_DPX2.pdf**


- **EB-DrivePX-BSL_Users_Guide.pdf**


- **DPX2-P2379-EB-V4.02.04_release.zip**
AURIX firmware for DRIVE PX 2 ACh is located on the host in the directory <install_dir>/drive-t186ref-foundation/.

- DPX2-P3407-EB-V4.02.04_release.zip

AURIX firmware for DRIVE PX 2 ACr is located on the host in the directory <install_dir>/drive-t186ref-foundation/.

To access the References content in the SDK release

1. Expand the NVIDIA_DRIVE_5.0_Linux_DPX_SDK_References.zip file.
2. Click the following HTML file to open the documentation page:

Start_DRIVE_5.0_Docs.html

The references open in your default browser, and all documents are accessible from within the same browser window.

1.4.1 Searching for Terms

You can search the Development Guide for a term by entering it in the Search field at the upper-right of the guide window. By default, you can search for whole words that are longer than two characters. By enclosing a term in asterisks (*), you can also find the term embedded in longer strings, even if the term is less than three characters.

For information on searching for APIs, see the Welcome page in the API Reference.

1.4.2 Issues Viewing References Documentation

Browser-related issues can affect your ability to view the References and Development Guide in this release.

1.4.2.1 References Require ActiveX on Internet Explorer

The following message can appear when you open the References or Development Guide on Internet Explorer:

Internet Explorer restricted this webpage from running scripts or ActiveX controls.

This message is due to Internet Explorer security restrictions that require approval to execute scripts and ActiveX and the Development Guide’s use of JavaScript and ActiveX controls to provide navigation and search features.

To workaround

In response to the Internet Explorer message, select the Allow blocked content button.
1.4.2.2 Cannot Open PDF Files in Internet Explorer 8

The following message can appear when you try to open PDF files in Internet Explorer 8.

Internet Explorer has stopped working. A problem caused the program to stop working correctly. Windows will close the program and notify you if a solution is available.

To workaround

1. Close your web browser.
2. Start Acrobat or Adobe Reader.
3. In the Edit tab, select Preferences.
4. Select Internet in the list on the left.
5. Deselect Display PDF in Browser.
6. Restart Internet Explorer.

1.4.3 Content Applying to Other Releases

Some content in the documentation may not apply to this DRIVE 5.0 Foundation release. We use the term *Applies to:* when specifying release-specific content. In those cases, be sure to follow the procedures for your release.
2.0 IMPLEMENTATION NOTES

This section provides additional implementation and support information specific to this release.

2.1 SUPPORTED CONFIGURATIONS

Release 5.0.10.3 of NVIDIA DRIVE 5.0 Foundation Software Development Kit for NVIDIA DRIVE PX 2 supports only the single Linux guest VM and Hypervisor configuration, and must be flashed accordingly. See the Development Guide for flashing instructions.

2.2 DEVICE TREE COMPILER BINARY/SOURCE INCOMPATIBILITY

The device tree compiler (DTC) binary version is 1.4.0, but the source packaged version is 1.3.0.

The locations of the binary and source are:

- **Binary**: `<top>/drive-t186ref-foundation/utils/dtc/dtc`
- **Source**: `<top>/drive-t186ref-foundation_src/3rdparty_dtc_src.tgz`

Where `<top>` is the host directory where you installed the SDK and Foundation release.

If you want the 1.4.0 DTC source, you can get it from:

```
git://git.kernel.org/pub/scm/utilis/dtc/dtc.git/
```

You can clone this repository with the following command:

```
git clone --branch v1.4.0 git://git.kernel.org/pub/scm/utilis/dtc/dtc.git/
```
2.3 MEMORY CARVEOUT DOCUMENTATION

Memory carveout allocations are documented in the Development Guides for DRIVE 5.0 Linux. Memory carveout allocations for QNX are documented in a separate Application Note. No documentation for memory carveouts in DRIVE 5.0 Android is available at this time.

2.4 DEPRECATED IVC ATTRIBUTES

The platform config.h, which is part of the partition configuration table (PCT), contains fields that configure inter-VM communication (IVC) queues and mempools. The deprecated IVC sub-fields are (per [2051864]):

```c
. ivc = {
    . mem_size = SZ_4MB,
    . irq_start = 320,
    . irq_size = 24,
    ....
};
```

Including:

- Setting `mem_size` has no effect.
- Setting `irq_start` and/or `irq_size` may result in a failure to boot.

To work around

Remove or comment out these lines from the partition configuration tables.
This section provides details about issues that were discovered during development and QA but not resolved prior to this release. In the following table, a check mark (✓) appears in the New or Past columns, meaning:

| New | Indicates issues found in this release. |
| Past | Indicates issues present in (and continuing to be present in) the last release for NVIDIA DRIVE PX 2. |

### 3.1 AURIX

The following AURIX related issues are noted in this release.

<table>
<thead>
<tr>
<th>Issue Number</th>
<th>Issue</th>
<th>New</th>
<th>Past</th>
</tr>
</thead>
<tbody>
<tr>
<td>200352896</td>
<td>Aurix to Tegra SW PTP sync : Tegra lags 35seconds by Aurix</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>200330224</td>
<td>AurixOTA is failing with tegra A IP changed to non-default value</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>1794817</td>
<td>The aurix_flash tool intermittently fails to mount on the target system.</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>200406310</td>
<td>Ping to AURIX fails from Tegra while experimenting with ping of different sizes. Later, ping -I eth0.200 10.42.0.146 itself doesn't work, and AURIX hangs.</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>200307066</td>
<td>CAN-FD is not supported on AURIX.</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>200296700</td>
<td>Performance issue in Fast EasyCAN.</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>200246011</td>
<td>Tegrawr on Tegra B (AURIX) is communicating to Tegra A instead of Tegra B (regression).</td>
<td>✓</td>
<td></td>
</tr>
</tbody>
</table>
3.2 FLASHING

The following flashing-related issue was noted in this release.

<table>
<thead>
<tr>
<th>Issue Number</th>
<th>Issue</th>
<th>New</th>
<th>Past</th>
</tr>
</thead>
<tbody>
<tr>
<td>200447452</td>
<td>create_bsp_images.sh script is failing to generate images on the first run</td>
<td></td>
<td>☑</td>
</tr>
</tbody>
</table>

3.3 SAFETY

The following safety-related issue was noted in this release.

<table>
<thead>
<tr>
<th>Issue Number</th>
<th>Issue</th>
<th>New</th>
<th>Past</th>
</tr>
</thead>
<tbody>
<tr>
<td>200418097</td>
<td>SDL tests fail on Tegra A during SCE boot sequence on fused target.</td>
<td>☑</td>
<td></td>
</tr>
<tr>
<td>200445880</td>
<td>Observing call trace while inserting module “tcrypt.ko” with mode=10</td>
<td>☑</td>
<td></td>
</tr>
</tbody>
</table>

3.4 SYSTEM

The following system-related issues were noted in this release.

<table>
<thead>
<tr>
<th>Issue Number</th>
<th>Issue</th>
<th>New</th>
<th>Past</th>
</tr>
</thead>
<tbody>
<tr>
<td>200403050</td>
<td>Tegra RCM driver is not installed when hot-plugged. There is a yellow bang on the device.</td>
<td></td>
<td>☑</td>
</tr>
<tr>
<td>200306757</td>
<td>System hang with “i2c: pio xfer timed out addr” and “bpmp_trywait() wait_for_completion_timeout” prints while running nvmipp_raw and gears in loop.</td>
<td></td>
<td>☑</td>
</tr>
</tbody>
</table>

3.5 VIRTUALIZATION

The following virtualization-related issues were noted in this release.

<table>
<thead>
<tr>
<th>Issue</th>
<th>Issue</th>
<th>New</th>
<th>Past</th>
</tr>
</thead>
<tbody>
<tr>
<td>200264489</td>
<td>Running the watchdog daemon causes IVC error messages to display on the console.</td>
<td></td>
<td>☑</td>
</tr>
<tr>
<td>200353605</td>
<td>“irq: type mismatch, failed to map hwirq” errors in boot spews.</td>
<td></td>
<td>☑</td>
</tr>
</tbody>
</table>
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