NVIDIA Grace CPU SuperChip

MGX System Quick Start Guide
# Document History

## DU-11600-001_03

<table>
<thead>
<tr>
<th>Version</th>
<th>Date</th>
<th>Description of Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>November 9, 2023</td>
<td>Initial release</td>
</tr>
<tr>
<td>02</td>
<td>December 6, 2023</td>
<td>• Updated the Ubuntu installation steps.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Added the SLES installation steps</td>
</tr>
<tr>
<td>03</td>
<td>January 22, 2024</td>
<td>Updated the post-install sequence for Ubuntu.</td>
</tr>
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Chapter 1. Introduction

This guide provides information about how to unbox and set up a Supermicro® 2U MGX NVIDIA® Grace CPU SuperChip system.

Figure 1. Front and Back View of the MGX System

Table 1. Details

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<thead>
<tr>
<th>Item Number</th>
<th>Part Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Front Panel Power</td>
</tr>
<tr>
<td>2</td>
<td>BMC Information Tag</td>
</tr>
<tr>
<td>3</td>
<td>NVMe Drive Bay</td>
</tr>
<tr>
<td>4</td>
<td>Host System RJ45 Ethernet Connection</td>
</tr>
<tr>
<td>5</td>
<td>BMC RJ45 Ethernet Connection</td>
</tr>
<tr>
<td>6</td>
<td>High Speed USB Ports</td>
</tr>
<tr>
<td>7</td>
<td>Mini Display Port</td>
</tr>
<tr>
<td>8</td>
<td>C13 Power Supply Cable Inserts</td>
</tr>
</tbody>
</table>
Chapter 2. Physical Install

Depending on location of installation, you need to install the server rail kit by following the manufacturer’s directions, which can be found on the manufacturer’s website. Install the rail kit into server rack using the rack manufacturer’s recommended instructions.

2.1 Installing the Cable

Note: The numbers in parentheses correspond to the numbers in Figure 1.

1. The server comes with dual-port 10GBase-T Ethernet card.
2. Connect the (4) Host RJ45 Ethernet ports to the network that will be used to communicate with the server.
3. Connect the (5) BMC RJ45 Ethernet to the network that will be used to manage the Baseboard Management Console (BMC).
4. (Optional) Connect the Keyboard and Video Monitor to ports (6) and (7). This step is optional because the BMC interface provides virtual KVM access, but it can be useful to configure or view the BMC’s IP address for the first time.
5. Connect three C13 power cables into the Power Supply Cable Inserts located on the rear of the server.
Chapter 3. First Boot

Note: The numbers in parentheses correspond to the numbers in Figure 1.

Now, you can boot the server by using the power button on the Front Panel (1). The server will initialize over the course of several minutes. After approximately five minutes, you will be able to log into the Baseboard Management Console (BMC) through its https interface.

- There are several methods to determine and set the IP address information you will use to access to the BMC.
- For your convenience, the manufacturer includes a tag (2) that includes the BMC’s MAC address, which can be used to create a static DHCP assignment to assign an IP of your choice, or to correlate network traffic with the BMC’s address.
- The BMC will default to requesting an IP address using DHCP.

Note: The boot process can take up to 20 minutes.

3.1 Method 1: Locate the BMC on Automatic DHCP Network

On a network that is already configured to use DHCP, you may use one of the following methods:

- Option A
  
a. Open the web interface of your router, or the log for your DHCP server.
  b. Locate the list of issued IP addresses (DHCP Leases) and look for the BMC’s MAC address on the list.
Option B
a. Request a static mapping from your router or DHCP server based on the server’s BMC MAC address.
b. Disconnect the server from power.
c. Re-connect it and the BMC will automatically obtain this new address.

Option C
a. Connect the server to a network on the same subnet as your administration machine.
b. Locate the server using the ARP table:
   - **Linux**: Run the `arp-scan -localnet` command.
   - **Windows PowerShell**: Run the `arp -a` command.

3.2 Method 2: View or Set the BMC IP Information Through a Local Display

With a keyboard and monitor attached to the server, you can determine and set the BMC IP settings and information through the System BIOS interface.

1. During initial boot, monitor for the BIOS options screen and follow instructions to enter the setup.
2. In the System BIOS menu, press **Tab** and select the **BMC** menu.
3. Select the **BMC network configuration** option and press enter.

Figure 1. The BMC network configuration Option
3.2.1 Setting a Static IP for the BMC through the System BIOS

For network configurations that require a static IP, this address can also be configured through the System BIOS settings.

1. Complete the instructions in “Method 2: View or Set the BMC IP Information” on page 7.

2. In **BMC Network Configuration**, select **Configuration Address source** and then select **Static**.

   This step allows you to alter the network and router information to correctly configure your BMC into your infrastructure.

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### Figure 2. Setting a Static IP Address

![Setting a Static IP Address](image)

---

3.3 Accessing the BMC Network Interface

After you locate the IP address that is assigned to the server BMC, open a web browser on the same network and navigate to the IP address. For instance, if your network assigned 192.168.1.100 to your BMC's MAC address, open a browser for a computer on the same network, and enter https://192.168.1.100 as the web address.

---

**Note:** To correctly load the page, https:// is required, and a certificate warning may be displayed. This is normal.
On the landing page, you will be prompted for a username and password:

- The default username from SuperMicro is **ADMIN** (case sensitive).
- The initial password is located on the BMC information tag (2) located on the front of the machine.

### 3.4 Installing the Operating System

Through the BMC’s Remote Control menu, use **both** of the following interfaces to complete the operating system installation:

- The HTML5 Viewer, which is a virtual monitor that is like a physical monitor and that is connected to the box.
- The Serial over Lan (SOL) interface.

1. Write the ISO image in a **bootable** way onto the USB drive and place it into one of the USB ports on the server.
2. Launch the HTML5 Viewer window.
3. Use the on-screen interface to power cycle the server and begin a cold boot sequence.
   - If the system drive is **completely blank**, it will eventually boot to the mounted media,
   - If the system drive is **not** blank, watch the BIOS output and select the option to choose boot media.
4. Select the available USB boot item.

NVIDIA provides guides for each of the major OS distributions that are available in the following links:

- [NVIDIA Grace Software with Ubuntu 22.04 Installation Guide](#).
- [NVIDIA Grace Software with Red Hat Enterprise Linux 9 Installation Guide](#).
- [NVIDIA Grace Software with SUSE Linux Enterprise Server 15 Installation Guide](#).

#### 3.4.1 Installing Ubuntu on the Grace Server

1. Download Ubuntu 22.04.03 LTS from [Downloading Ubuntu Server for ARM](#) and write out bootable media.
2. After starting the server and selecting the boot device, the GRUB bootloader’s Ubuntu installation menu will appear.
3. Select **Ubuntu Server with the HWE kernel**.
4. The HTML5 Viewer window will disappear and remain black for the rest of this process. This is a known issue that will be resolved in a future installer version that will incorporate newer versions of the kernel.

To complete the installation, open the Serial over LAN Console (SoL Console).
5. Complete the installation via the SoL viewer by selecting the options that are appropriate for you.
6. After the installation has completed, select Reboot Now.
7. After the system reboots, log in using the credentials that were specified during the installation.
8. Run the following commands to update the system and install the NVIDIA optimized Ubuntu kernel variant and reboot:
   ```
   sudo DEBIAN_FRONTEND=noninteractive apt purge linux-image-$\{uname -r\} linux-headers-$\{uname -r\} linux-modules-$\{uname -r\} -y
   sudo apt update
   sudo apt install linux-nvidia-64k-hwe-22.04 -y
   sudo reboot now
   ```

**Note:** If `linux-nvidia-64k-hwe-22.04` is not available, install `linux-nvidia-64k-6.2` instead.

9. The system will reboot and the installation of Ubuntu on the Grace server is complete.
3.4.2 Installing Red Hat Enterprise Linux 9.3 on a Grace Server

1. After booting the ISO image, through the BMC’s virtual media mount or through a physical media drive, the GRUB boot menu is displayed.
2. Select **Test this media & install Red Hat Enterprise Linux 9.3**.

![Testing the Media](image)

3. The HTML5 Viewer window will disappear and remain black for the rest of this process.
4. To complete the installation, use the SOL Console.
5. Follow the installer prompts to configure the manual installation.

   **Note:** There is a known GUI bug in the installer for RHEL version 9.2 that requires selecting “Use Text Mode” to successfully get through installation. This is not required for RHEL v 9.3 or above.

6. From the Software Selection menu, select the 4k or 64k kernel from the Kernel Options submenu. See Section 5.4.3.1 Page Size for system configuration recommendations.

   Begin the installation and allow the system to reboot.

6. After the system reboots, the installation of Red Hat Enterprise Linux 9.3 on the Grace server is complete.
3.4.3 Installing SUSE Linux Enterprise Server 15 SP5 on a Grace Server

1. After booting the ISO image, through the BMC’s virtual media mount, or through a physical media drive, the GRUB splash menu is displayed.
2. To display the GRUB boot menu, press t.

Figure 5. SLES 15 SP5 GRUB Boot Menu

3. Highlight Installation and press e to edit the boot entry.
4. Append modprobe.blacklist=ast to the end of the list of kernel boot parameters.
5. Boot the entry by clicking Ctrl-X or pressing F10.
6. The HTML5 Viewer window will disappear and remain black for the rest of this process.
7. To complete the installation, use the SOL Console.
   a. Complete the installations by selecting the options that are appropriate for you.
   b. From the Installation Settings summary, select Change menu.
   c. Select the Software submenu to alter the installation and install the 64k kernel as the sole, default kernel for the system.
   d. From the software change pane, navigate to the Search Phrase box, and search for kernel-64kb.
   e. Select the kernel-64kb package.
   f. Return to the Search Phrase box and search for kernel-default.
   g. Deselect the kernel-default package.
h. Navigate to and select **Accept** to update the planned installation.
i. Begin the installation and allow the system to reboot.

8. After the system reboots, the installation of SUSE Linux Enterprise Server 15 SP5 on the Grace server is complete.

### 3.4.4 Multiple Linux Distributions: Compiling the Linux Kernel from Source

You can install almost any variety of Linux on the Grace server as long as the distribution is derived from the mainline Linux kernel.

#### 3.4.4.1 Page Size

Grace supports 64K and 4K Linux kernel page sizes. To configure your Linux kernel with the page size that suits your business needs, change the following `kconfig` settings during the kernel compilation:

- **4K page size:** `CONFIG_ARM64_4K_PAGES=y`
- **64K page size:** `CONFIG_ARM64_64K_PAGES=y`

The 64K page size can benefit the applications that allocate a large amount of memory because there will be fewer page faults, better TLB hits, and efficiency.

**Note:** The recommended default value for the page size is 64K.

To build the kernel from the source:

1. Install the required minimum set of packages to build the kernel.
   ```
   sudo apt-get install git build-essential ncurses-dev xz-utils libssl-dev bc flex libelf-dev bison gcc-12-base
   ```
2. Download the desired version of the Linux Kernel Source from [www.kernel.org](http://www.kernel.org).
   ```
   wget https://cdn.kernel.org/pub/linux/kernel/v6.x/linux-6.6.0.tar.gz
   ```
3. Extract the Source Code.
   ```
   tar -xvf linux-6.6.0.tar.gz
   ```
4. Copy an existing version of the NVIDIA kernel config file into the Linux Source directory as `.config`.
   ```
   cp -v /boot/config-*nvidia* .config
   ```
   You also can copy the distribution default config into the Source directory and **make menuconfig**. Ensure that you make the required changes in Table 1. Error! Reference source not found.
   ```
   cp -v /boot/config-$(uname -r) .config
   ```
5. Open the .config file in a text editor, search for the CONFIG_ARM64_4K_PAGES variable.

6. Make one of the following changes based on the page size configuration that you want.

Figure 6. Possible Changes Based on the Page Size

7. Save the changes to the .config file.

8. Run the following command from the main Source Code directory.

```
make
```

   a. If you are compiling the kernel on Ubuntu, you might see the following error that interrupts the build process:

   ```
   No rule to make target 'debian/canonical-certs.pem'
   ```

   b. To disable the conflicting security certificates, run the following commands:

   ```
   ./scripts/config --disable SYSTEM_TRUSTED_KEYS
   ./scripts/config --disable SYSTEM_REVOCATION_KEYS
   ```

9. To finish the building process, run the `make` command again.

10. After the building process is complete, to install the required modules and then the kernel, run the following commands.

```
sudo make modules_install
sudo make install
```
Chapter 4. Using the System

4.1 Grace Customized Build Stack

As Arm-Neoverse V2 and Arm v9 systems become more widespread, commonly distributed software will begin to incorporate architectural optimizations. Until that occurs, to take advantage of the accelerations available in Arm v9 architectures, you need to compile the build chain from the source.

Tip: Environment management tools, such as Modules or Spack, can control build environments and allow for multiple versions of build stacks.

4.1.1 Building GCC 12.3 From the Source

1. Download GCC using one of the following methods:
   - Download the source using a source mirror.
   - Run the following command to download directly to a host server that is connected to the internet.
     
     ```
     wget https://ftp.gnu.org/gnu/gcc/gcc-12.3.0/gcc-12.3.0.tar.gz
     ```

2. Untar the downloaded source into a directory on the target system where the user has proper file permissions.

   ```
   tar -xzvf gcc-12.3.0.tar.gz
   ```

3. Enter the source directory.

   ```
   cd gcc-12.3.0/
   ```

4. To download the packages and build GCC, run the following commands.

   ```
   ./contrib/download_prerequisites
   ```

5. To correctly configure the installation, run the following command.

   ```
   ./configure --disable-multilib --enable-shared --enable-languages=c,c++
   ```

   (``--prefix can be used to change the default install location away from /usr/local/bin``)

6. To build GCC from source, run the following command.

   ```
   make -j144
   ```

You can exercise the cores in the Grace machine.
7. Install GCC.
   `sudo make install`

8. Verify the GCC installation.
   `which gcc # reports /usr/local/bin/gcc or the location specified with --prefix`
   `gcc --version # reports gcc (GCC) 12.3.0`

### 4.2 Performance Benchmarking Guide

A selection of tests have been developed to stress the Grace Superchip architecture. Refer to the [Grace CPU Benchmarking Guide](#).
## Appendix A. Required and Recommended Kernel Configurations for Grace

### Table 2. Required Bare Metal Configs

<table>
<thead>
<tr>
<th>Kernel Config</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CONFIG_NR_CPUS=512</td>
<td>Supports the maximum Grace configuration.</td>
</tr>
<tr>
<td>CONFIG_NODES_SHIFT=6</td>
<td>Supports the maximum Grace configuration.</td>
</tr>
<tr>
<td>CONFIG_ARM_SMMU_V3_SVA=y</td>
<td>Support shared virtual addressing.</td>
</tr>
<tr>
<td>CONFIG_ARM64_PMEM=y</td>
<td>Support persistent memory.</td>
</tr>
<tr>
<td>CONFIG_ARM_SDE_INTERFACE=y</td>
<td>Support RAS notifications.</td>
</tr>
<tr>
<td>CONFIG_BLK_DEV_PMEM=m</td>
<td>Enable persistent memory block device.</td>
</tr>
<tr>
<td>CONFIGDEVICE_MIGRATION=y</td>
<td>Enable device physical page migration.</td>
</tr>
<tr>
<td>CONFIGDEVICE_PRIVATE=y</td>
<td>Supports unaddressable device memory; only required when using NVIDIA TRD.</td>
</tr>
<tr>
<td>CONFIG_HOTPLUG_PCI_PCIE=y</td>
<td>Supports the PCIe native hotplug.</td>
</tr>
<tr>
<td>CONFIG_PCIE_DPC=y</td>
<td>Supports downstream port containment.</td>
</tr>
<tr>
<td>CONFIG_PCIE_EDR=y</td>
<td>Enables the error disconnect recover support.</td>
</tr>
<tr>
<td>CONFIG_SPI_TEGRA210_QUAD=m</td>
<td>Support the QSPI controller.</td>
</tr>
<tr>
<td>CONFIG_TCG_TIS_SPI=m</td>
<td>Supports the TPM SPI interface.</td>
</tr>
<tr>
<td>CONFIG_MTD_SPI_NOR=y</td>
<td>Support the SPI NOR flash device.</td>
</tr>
<tr>
<td>CONFIG_IPMI_SSIF=m</td>
<td>Supports the SMBus interface to BMC.</td>
</tr>
<tr>
<td>arch/arm64/include/asm/irq.h:</td>
<td>Supports the maximum Grace configuration. Not required when kernel carries</td>
</tr>
<tr>
<td>#if</td>
<td>721255b9826bd11c7a38b585905fc2dd0fb94e52</td>
</tr>
</tbody>
</table>
Table 3 contains the recommended config settings that provide performance improvements for certain workloads.

Table 3. Recommended Bare Metal Configs

<table>
<thead>
<tr>
<th>Kernel Config</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>defined(CONFIG_ARM_GIC_V3_ITS)</td>
<td></td>
</tr>
<tr>
<td>#define NR_IRQS (1 &lt;&lt; 19)</td>
<td></td>
</tr>
<tr>
<td>#endif</td>
<td></td>
</tr>
</tbody>
</table>

Table 3 contains the recommended config settings that provide performance improvements for certain workloads.

Table 4 contains optional config settings that enable the performance tooling functions on Grace platforms.

Table 4. Bare Metal Configs: Performance Tools

<table>
<thead>
<tr>
<th>Kernel Config</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CONFIG_ARM64_64K_PAGES=y</td>
<td>Use 64K page size; required when using NVIDIA TRD.</td>
</tr>
<tr>
<td>CONFIG_CPU_FREQ_DEFAULT_GOV_PERFORMANCE=y</td>
<td>Set default CPU frequency governor to “performance”.</td>
</tr>
<tr>
<td>CONFIG_CPU_FREQ_GOV_SCHEDUTIL=y</td>
<td>Supports the schedutil CPU frequency governor.</td>
</tr>
<tr>
<td>CONFIG_PREEMPT_DYNAMIC=y</td>
<td>Allows dynamic preemption tuning using preempt.</td>
</tr>
<tr>
<td>CONFIG_PREEMPT_NONE=y</td>
<td>Default dynamic preemption tuning to preempt=none for throughput.</td>
</tr>
<tr>
<td>CONFIG_DMABUF_HEAPS=y</td>
<td>Enables DMA-BUF memory heaps.</td>
</tr>
<tr>
<td>CONFIG_DMABUF_HEAPS_SYSTEM=y</td>
<td>Enables the system dmabuf heap.</td>
</tr>
<tr>
<td>CONFIG_DMI_SYSFS=y</td>
<td>Enables the export of raw DMI table data.</td>
</tr>
<tr>
<td>CONFIG_INIT_ON_ALLOC_DEFAULT_ON=n</td>
<td>Disables heap memory zeroing on allocation by default.</td>
</tr>
<tr>
<td>CONFIG_ARM_CORESIGHT_PMU_ARCH_SYSTEM_PMU=m</td>
<td>Enables the ARM CoreSight PMU driver.</td>
</tr>
<tr>
<td>CONFIG_SENS/ORS_ACPI_POWER=m</td>
<td>Enables power telemetry through hwmon.</td>
</tr>
<tr>
<td></td>
<td>Enable when sysfs endpoints for hardware power monitoring are not present.</td>
</tr>
</tbody>
</table>

Table 4 contains optional config settings that enable the performance tooling functions on Grace platforms.
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