

# PGI<sup>®</sup> COMPILERS & TOOLS

USER'S GUIDE FOR OPENPOWER CPUS

Version 2019



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# PREFACE

This guide is part of a set of manuals that describe how to use the PGI Fortran, C, and C++ compilers and program development tools. These compilers and tools include the *PGFORTRAN*, *PGC++*, *PGCC* compilers and the PGI profiler. They work in conjunction with an OpenPOWER assembler and linker. You can use the PGI compilers and tools to compile, debug, optimize, and profile serial and parallel applications for OpenPOWER processor-based systems.

The *PGI Compiler User's Guide* provides operating instructions for the PGI command-level development environment. The PGI Compiler Reference Manual contains details concerning the PGI compilers' interpretation of the Fortran language, implementation of Fortran language extensions, and command-level compilation. Users are expected to have previous experience with or knowledge of the Fortran programming language. Neither guide teaches the Fortran programming language.

## Audience Description

This manual is intended for scientists and engineers using the PGI compilers. To use these compilers, you should be aware of the role of high-level languages, such as Fortran, C, and C++, as well as assembly-language in the software development process; and you should have some level of understanding of programming. You also need to be familiar with the basic commands available on your system.

## Compatibility and Conformance to Standards

Your system needs to be running a properly installed and configured version of this PGI product. For information on installing PGI compilers and tools, refer to the Release Notes and Installation Guide included with your software.

For further information, refer to the following:

- ▶ *American National Standard Programming Language FORTRAN*, ANSI X3. -1978 (1978).
- ▶ *ISO/IEC 1539-1 : 1991, Information technology – Programming Languages – Fortran*, Geneva, 1991 (Fortran 90).
- ▶ *ISO/IEC 1539-1 : 1997, Information technology – Programming Languages – Fortran*, Geneva, 1997 (Fortran 95).

- ▶ *ISO/IEC 1539-1 : 2004, Information technology – Programming Languages – Fortran*, Geneva, 2004 (Fortran 2003).
- ▶ *ISO/IEC 1539-1 : 2010, Information technology – Programming Languages – Fortran*, Geneva, 2010 (Fortran 2008).
- ▶ *Fortran 95 Handbook Complete ISO/ANSI Reference*, Adams et al, The MIT Press, Cambridge, Mass, 1997.
- ▶ *The Fortran 2003 Handbook*, Adams et al, Springer, 2009.
- ▶ *OpenMP Application Program Interface, Version 3.1*, July 2011, <http://www.openmp.org>.
- ▶ *Programming in VAX Fortran, Version 4.0*, Digital Equipment Corporation (September, 1984).
- ▶ *IBM VS Fortran*, IBM Corporation, Rev. GC26-4119.
- ▶ Military Standard, Fortran, DOD Supplement to American National Standard Programming Language Fortran, ANSI x.3-1978, MIL-STD-1753 (November 9, 1978).
- ▶ *American National Standard Programming Language C*, ANSI X3.159-1989.
- ▶ *ISO/IEC 9899:1999, Information technology – Programming Languages – C*, Geneva, 1999 (C99).
- ▶ *ISO/IEC 9899:2011, Information Technology – Programming Languages – C*, Geneva, 2011 (C11).
- ▶ *ISO/IEC 14882:2011, Information Technology – Programming Languages – C++*, Geneva, 2011 (C++11).

## Organization

Users typically begin by wanting to know how to use a product and often then find that they need more information and facts about specific areas of the product. Knowing *how* as well as *why* you might use certain options or perform certain tasks is key to using the PGI compilers and tools effectively and efficiently. However, once you have this knowledge and understanding, you very likely might find yourself wanting to know much more about specific areas or specific topics.

This guide contains the essential information on how to use the compiler and is divided into these sections:

**Getting Started** provides an introduction to the PGI compilers and describes their use and overall features.

**Use Command-line Options** provides an overview of the command-line options as well as task-related lists of options.

**Optimizing and Parallelizing** describes standard optimization techniques that, with little effort, allow users to significantly improve the performance of programs.

**Using Function Inlining** describes how to use function inlining and shows how to create an inline library.

**Using OpenMP** provides a description of the OpenMP Fortran parallelization directives and of the OpenMP C and C++ parallelization pragmas, and shows examples of their use.

[Using MPI](#) describes how to use MPI with PGI products.

[Using an Accelerator](#) describes how to use the PGI Accelerator compilers.

[Using Directives and Pragmas](#) provides a description of each Fortran optimization directive and C/C++ optimization pragma, and shows examples of their use.

[Creating and Using Libraries](#) discusses PGI support libraries, shared object files, and environment variables that affect the behavior of the PGI compilers.

[Using Environment Variables](#) describes the environment variables that affect the behavior of the PGI compilers.

[Distributing Files – Deployment](#) describes the deployment of your files once you have built, debugged and compiled them successfully.

[Inter-language Calling](#) provides examples showing how to place C language calls in a Fortran program and Fortran language calls in a C program.

## Hardware and Software Constraints

This guide describes versions of the PGI compilers that produce assembly code for OpenPOWER processor-based systems. Details concerning environment-specific values and defaults and system-specific features or limitations are presented in the release notes delivered with the PGI compilers.

## Conventions

This guide uses the following conventions:

### *italic*

is used for emphasis.

### **Constant Width**

is used for filenames, directories, arguments, options, examples, and for language statements in the text, including assembly language statements.

### **Bold**

is used for commands.

### [ **item1** ]

in general, square brackets indicate optional items. In this case item1 is optional. In the context of p/t-sets, square brackets are required to specify a p/t-set.

### { **item2** | **item 3** }

braces indicate that a selection is required. In this case, you must select either item2 or item3.

### **filename ...**

ellipsis indicate a repetition. Zero or more of the preceding item may occur. In this example, multiple filenames are allowed.

### **FORTTRAN**

Fortran language statements are shown in the text of this guide using a reduced fixed point size.

**C/C++**

C/C++ language statements are shown in the test of this guide using a reduced fixed point size.

The PGI compilers and tools are supported on a wide variety of Linux, macOS and Windows operating systems running on 64-bit x86-compatible processors, and on Linux running on OpenPOWER processors. (Currently, the PGI debugger is supported on x86-64/x64 only.) See the [Compatibility and Installation](https://www.pgroup.com/products/index.htm?tab=compat) section on the PGI website at <https://www.pgroup.com/products/index.htm?tab=compat> for a comprehensive listing of supported platforms.



Support for 32-bit development was deprecated in PGI 2016 and is no longer available as of the PGI 2017 release. PGI 2017 is only available for 64-bit operating systems and does not include the ability to compile 32-bit applications for execution on either 32- or 64-bit operating systems.

## Terms

A number of terms related to systems, processors, compilers and tools are used throughout this guide. For example:

accelerator	FMA	-mcmmodel=small	shared library
AVX	host	MPI	SIMD
CUDA	large arrays	MPICH	static linking
device	license keys	multicore	
driver	LLVM	NUMA	
DWARF	-mcmmodel=medium	OpenPOWER	

For a complete definition of these terms and other terms in this guide with which you may be unfamiliar, please refer to the [PGI online glossary](https://www.pgroup.com/online-glossary) at [pgicompilers.com/definitions](https://www.pgroup.com/online-glossary).

The following table lists the PGI compilers and tools and their corresponding commands:

**Table 1 PGI Compilers and Commands**

Compiler or Tool	Language or Function	Command
PGFORTRAN	ISO/ANSI Fortran 2003	pgfortran
PGCC	ISO/ANSI C11 and K&R C	pgcc
PGC++	ISO/ANSI C++17 with GNU compatibility	pgc++
PGI Profiler	Performance profiler	pgprof

In general, the designation *PGI Fortran* is used to refer to the PGI Fortran 2003 compiler, and *pgfortran* is used to refer to the command that invokes the compiler. A similar convention is used for each of the PGI compilers and tools.

For simplicity, examples of command-line invocation of the compilers generally reference the `pgfortran` command, and most source code examples are written in Fortran. Usage of *PGC++* and *PGCC* is consistent with *PGFORTRAN*, though there are command-line options and features of these compilers that do not apply to *PGFORTRAN*, and vice versa.



Support for 32-bit development was deprecated in PGI 2016 and is no longer available as of the PGI 2017 release. PGI 2017 is only available for 64-bit operating systems and does not include the ability to compile 32-bit applications for execution on either 32-bit or 64-bit operating systems.

## Related Publications

The following documents contain additional information related to the OpenPOWER architecture, and the compilers and tools available from The Portland Group.

- ▶ [PGI Fortran Reference Manual, www.pgroup.com/resources/docs/19.1/pdf/pgi19fortref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19fortref-openpower.pdf) describes the FORTRAN 77, Fortran 90/95, Fortran 2003 statements, data types, input/output format specifiers, and additional reference material related to use of the PGI Fortran compilers.
- ▶ *System V Application Binary Interface Processor Supplement* by AT&T UNIX System Laboratories, Inc. (Prentice Hall, Inc.).
- ▶ *OpenPOWER ABI for Linux Supplement, Power Architecture 64-Bit ELF V2 ABI Specification*, [http://openpowerfoundation.org/wp-content/uploads/2016/03/ABI64BitOpenPOWERv1.1\\_16July2015\\_pub4.pdf](http://openpowerfoundation.org/wp-content/uploads/2016/03/ABI64BitOpenPOWERv1.1_16July2015_pub4.pdf).
- ▶ *Fortran 95 Handbook Complete ISO/ANSI Reference*, Adams et al, The MIT Press, Cambridge, Mass, 1997.
- ▶ *Programming in VAX Fortran, Version 4.0*, Digital Equipment Corporation (September, 1984).
- ▶ *IBM VS Fortran*, IBM Corporation, Rev. GC26-4119.
- ▶ *The C Programming Language* by Kernighan and Ritchie (Prentice Hall).
- ▶ *C: A Reference Manual* by Samuel P. Harbison and Guy L. Steele Jr. (Prentice Hall, 1987).
- ▶ *The Annotated C++ Reference Manual* by Margaret Ellis and Bjarne Stroustrup, AT&T Bell Laboratories, Inc. (Addison-Wesley Publishing Co., 1990).

# Chapter 1.

## GETTING STARTED

This section describes how to use the PGI compilers.

### 1.1. Overview

The command used to invoke a compiler, such as the `pgfortran` command, is called a *compiler driver*. The compiler driver controls the following phases of compilation: preprocessing, compiling, assembling, and linking. Once a file is compiled and an executable file is produced, you can execute, debug, or profile the program on your system. Executables produced by the PGI compilers are unconstrained, meaning they can be executed on any compatible OpenPOWER processor-based system, regardless of whether the PGI compilers are installed on that system.

In general, using a PGI compiler involves three steps:

1. Produce program source code in a file containing a `.f` extension or another appropriate extension, as described in [Input Files](#). This program may be one that you have written or one that you are modifying.
2. Compile the program using the appropriate compiler command.
3. Execute, debug, or profile the executable file on your system.

You might also want to deploy your application, though this is not a required step.

The PGI compilers allow many variations on these general program development steps. These variations include the following:

- ▶ Stop the compilation after preprocessing, compiling or assembling to save and examine intermediate results.
- ▶ Provide options to the driver that control compiler optimization or that specify various features or limitations.
- ▶ Include as input intermediate files such as preprocessor output, compiler output, or assembler output.

## 1.2. Creating an Example

Let's look at a simple example of using the PGI compiler to create, compile, and execute a program that prints:

```
hello
```

### 1. Create your program.

For this example, suppose you enter the following simple Fortran program in the file `hello.f`:

```
print *, "hello"
end
```

### 2. Compile the program.

When you created your program, you called it `hello.f`. In this example, we compile it from a shell command prompt using the default `pgfortran` driver option. Use the following syntax:

```
$ pgfortran hello.f
```

By default, the executable output is placed in the file `a.out`. However, you can specify an output file name by using the `-o` option.

To place the executable output in the file `hello`, use this command:

```
$ pgfortran -o hello hello.f
```

### 3. Execute the program.

To execute the resulting `hello` program, simply type the filename at the command prompt and press the **Return** or **Enter** key on your keyboard:

```
$ hello
```

Below is the expected output:

```
hello
```

## 1.3. Invoking the Command-level PGI Compilers

To translate and link a Fortran, C, or C++ program, the `pgfortran`, `pgcc` and `pgc++` commands do the following:

1. Preprocess the source text file.
2. Check the syntax of the source text.
3. Generate an assembly language file.
4. Pass control to the subsequent assembly and linking steps.

### 1.3.1. Command-line Syntax

The compiler command-line syntax, using `pgfortran` as an example, is:

```
pgfortran [options] [path]filename [...]
```

Where:

**options**

is one or more command-line options, all of which are described in detail in [Use Command-line Options](#).

**path**

is the pathname to the directory containing the file named by filename. If you do not specify the path for a filename, the compiler uses the current directory. You must specify the path separately for each filename not in the current directory.

**filename**

is the name of a source file, preprocessed source file, assembly-language file, object file, or library to be processed by the compilation system. You can specify more than one [path]filename.

## 1.3.2. Command-line Options

The command-line options control various aspects of the compilation process. For a complete alphabetical listing and a description of all the command-line options, refer to [Use Command-Line Options](#).

The following list provides important information about proper use of command-line options.

- ▶ Command-line options and their arguments are case sensitive.
- ▶ The compiler drivers recognize characters preceded by a hyphen (-) as command-line options. For example, the `-Mlist` option specifies that the compiler creates a listing file.



The convention for the text of this manual is to show command-line options using a dash instead of a hyphen; for example, you see `-Mlist`.

- ▶ The order of options and the filename is flexible. That is, you can place options before and after the filename argument on the command line. However, the placement of some options is significant, such as the `-l` option, in which the order of the filenames determines the search order.



If two or more options contradict each other, the last one in the command line takes precedence.

## 1.3.3. Fortran Directives and C/C++ Pragmas

You can insert Fortran directives and C/C++ pragmas in program source code to alter the effects of certain command-line options and to control various aspects of the compilation process for a specific routine or a specific program loop. For more information on Fortran directives and C/C++ pragmas, refer to [Using OpenMP](#) and [Using Directives and Pragas](#).

## 1.4. Filename Conventions

The PGI compilers use the filenames that you specify on the command line to find and to create input and output files. This section describes the input and output filename conventions for the phases of the compilation process.

### 1.4.1. Input Files

You can specify assembly-language files, preprocessed source files, Fortran/C/C++ source files, object files, and libraries as inputs on the command line. The compiler driver determines the type of each input file by examining the filename extensions.



For systems with a case-insensitive file system, use the `-Mpreprocess` option, described in ‘Command-Line Options Reference’ section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), under the commands for Fortran preprocessing.

The drivers use the following conventions:

**filename.f**

indicates a Fortran source file.

**filename.F**

indicates a Fortran source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.FOR**

indicates a Fortran source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.F90**

indicates a Fortran 90/95 source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.F95**

indicates a Fortran 90/95 source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.f90**

indicates a Fortran 90/95 source file that is in freeform format.

**filename.f95**

indicates a Fortran 90/95 source file that is in freeform format.

**filename.cuf**

indicates a Fortran 90/95 source file in free format with CUDA Fortran extensions.

**filename.CUF**

indicates a Fortran 90/95 source file in free format with CUDA Fortran extensions and that can contain macros and preprocessor directives (to be preprocessed).

**filename.c**

indicates a C source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.C**

indicates a C++ source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.i**

indicates a preprocessed C or C++ source file.

**filename.cc**

indicates a C++ source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.cpp**

indicates a C++ source file that can contain macros and preprocessor directives (to be preprocessed).

**filename.s**

indicates an assembly-language file.

**filename.o**

indicates an object file.

**filename.a**

indicates a library of object files.

**filename.so**

indicates a library of shared object files.

The driver passes files with `.s` extensions to the assembler and files with `.o`, `.so`, and `.a` extensions to the linker. Input files with unrecognized extensions, or no extension, are also passed to the linker.

Files with a `.F` (Capital F) or `.FOR` suffix are first preprocessed by the Fortran compilers and the output is passed to the compilation phase. The Fortran preprocessor functions like `cpp` for C programs, but is built in to the Fortran compilers rather than implemented through an invocation of `cpp`. This design ensures consistency in the preprocessing step regardless of the type or revision of operating system under which you are compiling.

Any input files not needed for a particular phase of processing are not processed. For example, if on the command line you specify an assembly-language file (`filename.s`) and the `-S` option to stop before the assembly phase, the compiler takes no action on the assembly language file. Processing stops after compilation and the assembler does not run. In this scenario, the compilation must have been completed in a previous pass which created the `.s` file. For a complete description of the `-S` option, refer to [Output Files](#).

In addition to specifying primary input files on the command line, code within other files can be compiled as part of include files using the `INCLUDE` statement in a Fortran source file or the preprocessor `#include` directive in Fortran source files that use a `.F` extension or C and C++ source files.

When linking a program with a library, the linker extracts only those library components that the program needs. The compiler drivers link in several libraries by default. For more information about libraries, refer to [Create and Use Libraries](#).

## 1.4.2. Output Files

By default, an executable output file produced by one of the PGI compilers is placed in the file `a.out`. As the [Hello example](#) shows, you can use the `-o` option to specify the output file name.

If you use option `-F` (Fortran only), `-P` (C/C++ only), `-S` or `-c`, the compiler produces a file containing the output of the last completed phase for each input file, as specified by the option supplied.

The output file is a preprocessed source file, an assembly-language file, or an unlinked object file respectively. Similarly, the `-E` option does not produce a file, but displays the preprocessed source file on the standard output. Using any of these options, the `-o` option is valid only if you specify a single input file. If no errors occur during processing, you can use the files created by these options as input to a future invocation of any of the PGI compiler drivers.

The following table lists the stop-after options and the output files that the compilers create when you use these options. It also indicates the accepted input files.

Table 2 Option Descriptions

Option	Stop After	Input	Output
<code>-E</code>	preprocessing	Source files	preprocessed file to standard out
<code>-F</code>	preprocessing	Source files. This option is not valid for <code>pgcc</code> or <code>pgc++</code> .	preprocessed file ( <code>.f</code> )
<code>-P</code>	preprocessing	Source files. This option is not valid for <code>pgfortran</code> .	preprocessed file ( <code>.i</code> )
<code>-S</code>	compilation	Source files or preprocessed files	assembly-language file ( <code>.s</code> )
<code>-c</code>	assembly	Source files, or preprocessed files, or assembly-language files	unlinked object file ( <code>.o</code> )
none	linking	Source files, or preprocessed files, assembly-language files, object files, or libraries	executable file ( <code>a.out</code> )

If you specify multiple input files or do not specify an object filename, the compiler uses the input filenames to derive corresponding default output filenames of the following form, where *filename* is the input filename without its extension:

**filename.f**

indicates a preprocessed file, if you compiled a Fortran file using the `-F` option.

**filename.i**

indicates a preprocessed file, if you compiled using the `-P` option.

**filename.lst**

indicates a listing file from the `-Mlist` option.

**filename.o**

indicates a object file from the `-c` option.

`filename.s`

indicates an assembly-language file from the `-S` option.



Unless you specify otherwise, the destination directory for any output file is the current working directory. If the file exists in the destination directory, the compiler overwrites it.

The following example demonstrates the use of output filename extensions.

```
$ pgfortran -c proto.f proto1.F
```

This produces the output files `proto.o` and `proto1.o`, all of which are binary object files. Prior to compilation, the file `proto1.F` is preprocessed because it has a `.F` filename extension.

## 1.5. Fortran, C, and C++ Data Types

The PGI Fortran, C, and C++ compilers recognize scalar and aggregate data types. A scalar data type holds a single value, such as the integer value 42 or the real value 112.6. An aggregate data type consists of one or more scalar data type objects, such as an array of integer values.

For information about the format and alignment of each data type in memory, and the range of values each type can have on OpenPOWER processor-based systems, refer to 'Fortran, C, and C++ Data Types' section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

For more information on OpenPOWER-specific data representation, refer to the *OpenPOWER ABI for Linux Supplement, Power Architecture 64-Bit ELF V2 ABI Specification* listed in the 'Related Publications' section in the [Preface](#).

## 1.6. Parallel Programming Using the PGI Compilers

The PGI compilers support many styles of parallel programming:

- ▶ Automatic shared-memory parallel programs compiled using the `-Mconcur` option to `pgfortran`, `pgcc` or `pgc++`. Parallel programs of this variety can be run on shared-memory parallel (SMP) systems such as dual-core or multi-processor workstations.
- ▶ OpenMP shared-memory parallel programs compiled using the `-mp` option to `pgfortran`, `pgcc` or `pgc++`. Parallel programs of this variety can be run on SMP systems. Carefully coded user-directed parallel programs using OpenMP directives can often achieve significant speed-ups on dual-core workstations or large numbers of processors on SMP server systems. [Using OpenMP](#) contains complete descriptions of user-directed parallel programming.
- ▶ Distributed computing using an MPI message-passing library for communication between distributed processes.

- ▶ Accelerated computing using either a low-level model such as CUDA Fortran or a high-level model such as the PGI Accelerator model or OpenACC to target a many-core GPU or other attached accelerator.

The first two types of parallel programs are collectively referred to as SMP parallel programs.

On a single silicon die, today's CPUs incorporate two or more complete processor cores – functional units, registers, level 1 cache, level 2 cache, and so on. These CPUs are known as multicore processors. For purposes of threads or OpenMP parallelism, these cores function as two or more distinct processors. However, the processing cores are on a single chip occupying a single socket on a system motherboard. For purposes of PGI software licensing, a multicore processor is treated as a single CPU.

### 1.6.1. Run SMP Parallel Programs

When you execute an SMP parallel program, by default it uses only one processor. To run on more than one processor, set the `OMP_NUM_THREADS` environment variable to the desired number of processors. For information on how to set environment variables, refer to [Setting Environment Variables](#).



If you set `OMP_NUM_THREADS` to a number larger than the number of physical processors, your program may execute very slowly.

## 1.7. Platform-specific considerations

The OpenPOWER Linux platform is supported by the PGI compilers and tools:

### 1.7.1. Using the PGI Compilers on Linux

#### Linux Header Files

The Linux system header files contain many GNU gcc extensions. PGI supports many of these extensions, thus allowing the PGI C and C++ compilers to compile most programs that the GNU compilers can compile. A few header files not interoperable with the PGI compilers have been rewritten. These files are included in `$PGI/linuxpower/include` and in `$PGI/linuxpower/include --gcc*`, such as `float.h`, `machine/_types.h`, `stddef.h`, `sys/cdefs.h` and others. Also, PGI's version of `stdarg.h` supports changes in newer versions of Linux.

If you are using the PGI C or C++ compilers, please make sure that the supplied versions of these include files are found before the system versions. This hierarchy happens by default unless you explicitly add a `-I` option that references one of the system `include` directories.

## Running Parallel Programs on Linux

You may encounter difficulties running auto-parallel or OpenMP programs on Linux systems when the per-thread stack size is set to the default (2MB). If you have unexplained failures, please try setting the environment variable `OMP_STACKSIZE` to a larger value, such as 8MB. For information on setting environment variables, refer to [Setting Environment Variables](#).

If your program is still failing, you may be encountering the hard 8 MB limit on main process stack sizes in Linux. You can work around the problem by issuing the following command:

In `csh`:

```
% limit stacksize unlimited
```

In `bash`, `sh`, `zsh`, or `ksh`, use:

```
$ ulimit -s unlimited
```

## 1.8. Site-Specific Customization of the Compilers

If you are using the PGI compilers and want all your users to have access to specific libraries or other files, there are special files that allow you to customize the compilers for your site.

### 1.8.1. Use `siterc` Files

The PGI compiler drivers utilize a file named `siterc` to enable site-specific customization of the behavior of the PGI compilers. The `siterc` file is located in the `bin` subdirectory of the PGI installation directory. Using `siterc`, you can control how the compiler drivers invoke the various components in the compilation tool chain.

### 1.8.2. Using User `rc` Files

In addition to the `siterc` file, user `rc` files can reside in a given user's home directory, as specified by the user's `HOME` environment variable. You can use these files to control the respective PGI compilers. All of these files are optional.

On Linux, these files are named `.mypgf90rc`, `.mypgccrc`, and `.mypgc++rc`.

The following examples show how you can use these `rc` files to tailor a given installation for a particular purpose.

**Table 3** Examples of Using `siterc` and User `rc` Files

To do this...	Add the line shown to the indicated file(s)
Make available to all linuxpower compilations the libraries found in <code>/opt/newlibs/64</code>	<code>set SITELIB=/opt/newlibs/64;</code>  <code>to /opt/pgi/linuxpower/19.1/bin/siterc</code>

To do this...	Add the line shown to the indicated file(s)
Add to all linuxpower compilations a new library path: /opt/local/fast	<code>append SITELIB=/opt/local/fast;</code>  to /opt/pgi/linuxpower/19.1/bin/siterc
With linuxpower compilations, change -Mmpi to link in /opt/mympi/64/libmpix.a	<code>set MPILIBDIR=/opt/mympi/64;</code>  <code>set MPILIBNAME=mpix;</code>  to /opt/pgi/linuxpower/19.1/bin/siterc
With linuxpower compilations, always add -DIS64BIT -DIBM	<code>set SITEDEF=IS64BIT IBM;</code>  to /opt/pgi/linuxpower/19.1/bin/siterc
Build an F90 or F95 executable for linuxpower or linuxpower that resolves PGI shared objects in the relative directory ./REDIST	<code>set set RPATH=./REDIST;</code>  to ~/ .mypgfortranrc  <b>Note.</b> This only affects the behavior of PGFORTRAN for the given user.

## 1.9. Common Development Tasks

Now that you have a brief introduction to the compiler, let's look at some common development tasks that you might wish to perform.

- ▶ When you compile code you can specify a number of options on the command line that define specific characteristics related to how the program is compiled and linked, typically enhancing or overriding the default behavior of the compiler. For a list of the most common command line options and information on all the command line options, refer to [Use Command-line Options](#).
- ▶ Code optimization and parallelization allows the compiler to organize your code for efficient execution. While possibly increasing compilation time and making the code more difficult to debug, these techniques typically produce code that runs significantly faster than code that does not use them. For more information on optimization and parallelization, refer to [Optimizing and Parallelizing](#).
- ▶ Function inlining, a special type of optimization, replaces a call to a function or a subroutine with the body of the function or subroutine. This process can speed up execution by eliminating parameter passing and the function or subroutine call and return overhead. In addition, function inlining allows the compiler to optimize the function with the rest of the code. However, function inlining may also result in much larger code size with no increase in execution speed. For more information on function inlining, refer to [Using Function Inlining](#).
- ▶ Directives and pragmas allow users to place hints in the source code to help the compiler generate better assembly code. You typically use directives and pragmas

to control the actions of the compiler in a particular portion of a program without affecting the program as a whole. You place them in your source code where you want them to take effect. A directive or pragma typically stays in effect from the point where included until the end of the compilation unit or until another directive or pragma changes its status. For more information on directives and pragmas, refer to [Using OpenMP](#) and [Using Directives and Pragmas](#).

- ▶ A library is a collection of functions or subprograms used to develop software. Libraries contain "helper" code and data, which provide services to independent programs, allowing code and data to be shared and changed in a modular fashion. The functions and programs in a library are grouped for ease of use and linking. When creating your programs, it is often useful to incorporate standard libraries or proprietary ones. For more information on this topic, refer to [Creating and Using Libraries](#).
- ▶ Environment variables define a set of dynamic values that can affect the way running processes behave on a computer. It is often useful to use these variables to set and pass information that alters the default behavior of the PGI compilers and the executables which they generate. For more information on these variables, refer to [Using Environment Variables](#).
- ▶ Deployment, though possibly an infrequent task, can present some unique issues related to concerns of porting the code to other systems. Deployment, in this context, involves distribution of a specific file or set of files that are already compiled and configured. The distribution must occur in such a way that the application executes accurately on another system which may not be configured exactly the same as the system on which the code was created. For more information on what you might need to know to successfully deploy your code, refer to [Distributing Files – Deployment](#).
- ▶ An intrinsic is a function available in a given language whose implementation is handled specially by the compiler. Intrinsic make using processor-specific enhancements easier because they provide a C/C++ language interface to assembly instructions. In doing so, the compiler manages details that the user would normally have to be concerned with, such as register names, register allocations, and memory locations of data.

# Chapter 2.

## USE COMMAND-LINE OPTIONS

A command line option allows you to control specific behavior when a program is compiled and linked. This section describes the syntax for properly using command-line options and provides a brief overview of a few of the more common options.



For a complete list of command-line options, their descriptions and use, refer to the 'Command-Line Options Reference' section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

## 2.1. Command-line Option Overview

Before looking at all the command-line options, first become familiar with the syntax for these options. There are a large number of options available to you, yet most users only use a few of them. So, start simple and progress into using the more advanced options.

By default, the PGI compilers generate code that is optimized for the type of processor on which compilation is performed, the compilation host. Before adding options to your command-line, review [Help with Command-line Options](#) and [Frequently-used Options](#).

### 2.1.1. Command-line Options Syntax

On a command-line, options need to be preceded by a hyphen (-). If the compiler does not recognize an option, you get an unknown switch error. The error can be downgraded to a warning by adding the `-noswitcherror` option.

This document uses the following notation when describing options:

**[item]**

Square brackets indicate that the enclosed item is optional.

**{item | item}**

Braces indicate that you must select one and only one of the enclosed items. A vertical bar (|) separates the choices.

...

Horizontal ellipses indicate that zero or more instances of the preceding item are valid.



Some options do not allow a space between the option and its argument or within an argument. When applicable, the syntax section of the option description in the ‘Command-Line Options Reference’ section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf) contains this information.

## 2.1.2. Command-line Suboptions

Some options accept several suboptions. You can specify these suboptions either by using the full option statement multiple times or by using a comma-separated list for the suboptions.

The following two command lines are equivalent:

```
pgfortran -Mvect=simd -Mvect=noaltcode
```

```
pgfortran -Mvect=simd,noaltcode
```

## 2.1.3. Command-line Conflicting Options

Some options have an opposite or negated counterpart. For example, both `-Mvect` and `-Mnovect` are available. `-Mvect` enables vectorization and `-Mnovect` disables it. If you used both of these commands on a command line, they would conflict.



**Rule:** When you use conflicting options on a command line, the last encountered option takes precedence over any previous one.

The conflicting options rule is important for a number of reasons.

- ▶ Some options, such as `-fast`, include other options. Therefore, it is possible for you to be unaware that you have conflicting options.
- ▶ You can use this rule to create makefiles that apply specific flags to a set of files, as shown in the following example.

### Example: Makefiles with Options

In this makefile fragment, `CCFLAGS` uses vectorization. `CCNOVECTFLAGS` uses the flags defined for `CCFLAGS` but disables vectorization.

```
CCFLAGS=c -Mvect=simd
CCNOVECTFLAGS=${CCFLAGS} -Mnovect
```

## 2.2. Help with Command-line Options

If you are just getting started with the PGI compilers and tools, it is helpful to know which options are available, when to use them, and which options most users find effective.

## Using -help

The `-help` option is useful because it provides information about all options supported by a given compiler.

You can use `-help` in one of three ways:

- ▶ Use `-help` with no parameters to obtain a list of all the available options with a brief one-line description of each.
- ▶ Add a parameter to `-help` to restrict the output to information about a specific option. The syntax for this usage is:

```
-help <command line option>
```

Suppose you use the following command to restrict the output to information about the `-fast` option:

```
$ pgfortran -help -fast
```

The output you see is similar to:

```
-fast Common optimizations; includes -O2 -Munroll=c:1 -Mnoframe -Mlre
```

In the following example, we add the `-help` parameter to restrict the output to information about the help command. The usage information for `-help` shows how groups of options can be listed or examined according to function.

```
$ pgfortran -help -help
-help[=groups|asm|debug|language|linker|opt|other|overall|phase|prepro|
suffix|switch|target|variable]
```

- ▶ Add a parameter to `-help` to restrict the output to a specific set of options or to a building process. The syntax for this usage is this:

```
-help=<subgroup>
```

For a complete description of subgroups, refer to the `-help` description in the *Command-line Options Reference* section of the *PGI Compiler Reference Manual*, [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

## 2.3. Getting Started with Performance

One of the top priorities of most users is performance and optimization. This section provides a quick overview of a few of the command-line options that are useful in improving performance.

### 2.3.1. Using -fast

PGI compilers implement a wide range of options that allow users a fine degree of control on each optimization phase. When it comes to optimization of code, the quickest way to start is to use the option `-fast`. These options create a generally optimal set of flags. They incorporate optimization options to enable use of vector streaming SIMD

instructions. They enable vectorization with SIMD instructions, cache alignment, and flush to zero mode.

 The contents of the `-fast` option are host-dependent. Further, you should use these options on both compile and link command lines.

The following table shows the typical `-fast` options.

Table 4 Typical `-fast` Options

Use this option...	To do this...
<code>-O2</code>	Specifies a code optimization level of 2.
<code>-Munroll=c:1</code>	Unrolls loops, executing multiple instances of the original loop during each iteration.
<code>-Mnoframe</code>	Indicates to not generate code to set up a stack frame. <b>Note.</b> With this option, a stack trace does not work.
<code>-Mlre</code>	Indicates loop-carried redundancy elimination.
<code>-Mpre</code>	Indicates partial redundancy elimination

`-fast` typically includes the options shown in this table:

Table 5 Additional `-fast` Options

Use this option...	To do this...
<code>-Mvect=simd</code>	Generates packed SIMD instructions.
<code>-Mcache_align</code>	Aligns long objects on cache-line boundaries.
<code>-Mflushz</code>	Sets flush-to-zero mode.
<code>-M[no]vect</code>	Controls automatic vector pipelining.

 For best performance on processors that support SIMD instructions, use the PGFORTRAN compiler and the `-fast` option.

To see the specific behavior of `-fast` for your target, use the following command:

```
$ pgfortran -help -fast
```

### 2.3.2. Other Performance-Related Options

While `-fast` is designed to be the quickest route to best performance, it is limited to routine boundaries. Depending on the nature and writing style of the source code, the compiler often can perform further optimization by knowing the global context of usage of a given routine. For instance, determining the possible value range of actual parameters of a routine could enable a loop to be vectorized; similarly, determining static occurrence of calls helps to decide which routine is beneficial to inline.

These types of global optimizations are under control of Interprocedural Analysis (IPA) in PGI compilers. Option `-Mipa` enables Interprocedural Analysis. `-Mipa=fast` is the recommended option to get best performances for global optimization. You can also add the suboption `inline` to enable automatic global inlining across files. You might consider using `-Mipa=fast,inline`. This option for interprocedural analysis and global optimization can improve performance.

You may also obtain further performance improvements by experimenting with the `-M<pgflag>` options described in the section ‘`-M Options by Category`’ section of the [PGI Compiler Reference Manual, www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf). These options include, but are not limited to, `-Mvect`, `-Munroll`, `-Minline`, `-Mconcur`, `-Mpmfi` and `-Mpmfo`. However, performance improvements using these options are typically application- and system-dependent. It is important to time your application carefully when using these options to ensure no performance degradations occur.

For more information on optimization, refer to [Optimizing and Parallelizing](#). For specific information about these options, refer to the ‘`Optimization Controls`’ section of the [PGI Compiler Reference Manual, www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

## 2.4. Frequently-used Options

In addition to overall performance, there are a number of other options that many users find useful when getting started. The following table provides a brief summary of these options.

For more information on these options, refer to the complete description of each option available in the ‘`Command-Line Options Reference`’ section of the [PGI Compiler Reference Manual, www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf). Also, there are a number of suboptions available with each of the `-M` options listed. For more information on those options, refer to the specific section on ‘`M Options by Category`’.

**Table 6 Commonly Used Command-Line Options**

Use this option...	To do this...
<code>-fast</code>	This options creates a generally optimal set of flags for targets that support SIMD capability. It incorporates optimization options to enable use of vector streaming SIMD instructions (64-bit targets) and enable vectorization with SIMD instructions, cache aligned and flushz.
<code>-g</code>	Instructs the compiler to include symbolic debugging information in the object module; sets the optimization level to zero unless a <code>-O</code> option is present on the command line.
<code>--gopt</code>	Instructs the compiler to include symbolic debugging information in the object file, and to generate optimized code identical to that generated when <code>-g</code> is not specified.
<code>-help</code>	Provides information about available options.

Use this option...	To do this...
<code>-mcmmodel=medium</code>	Enables medium= <code>model</code> core generation for 64-bit targets, which is useful when the data space of the program exceeds 4GB.
<code>-Mconcur</code>	Instructs the compiler to enable auto-concurrentization of loops. If specified, the compiler uses multiple processors to execute loops that it determines to be parallelizable; thus, loop iterations are split to execute optimally in a multithreaded execution context.
<code>-Minfo</code>	Instructs the compiler to produce information on standard error.
<code>-Minline</code>	Enables function inlining.
<code>-Mipa=fast,inline</code>	Enables interprocedural analysis and optimization. Also enables automatic procedure inlining.
<code>-Mpfi</code> or <code>-Mpfo</code>	Enable profile feedback driven optimizations
<code>-Mkeepasm</code>	Keeps the generated assembly files.
<code>-Munroll</code>	Invokes the loop unroller to unroll loops, executing multiple instances of the loop during each iteration. This also sets the optimization level to 2 if the level is set to less than 2, or if no <code>-O</code> or <code>-g</code> options are supplied.
<code>-M[no]vect</code>	Enables/Disables the code vectorizer.
<code>--[no_]exceptions</code>	Removes exception handling from user code. For C++, declares that the functions in this file generate no C++ exceptions, allowing more optimal code generation.
<code>-o</code>	Names the output file.
<code>-O &lt;level&gt;</code>	Specifies code optimization level where <code>&lt;level&gt;</code> is 0, 1, 2, 3, or 4.
<code>-Wl, &lt;option&gt;</code>	Compiler driver passes the specified options to the linker.

# Chapter 3.

## OPTIMIZING AND PARALLELIZING

Source code that is readable, maintainable, and produces correct results is not always organized for efficient execution. Normally, the first step in the program development process involves producing code that executes and produces the correct results. This first step usually involves compiling without much worry about optimization. After code is compiled and debugged, code optimization and parallelization become an issue.

Invoking one of the PGI compiler commands with certain options instructs the compiler to generate optimized code. Optimization is not always performed since it increases compilation time and may make debugging difficult. However, optimization produces more efficient code that usually runs significantly faster than code that is not optimized.

The compilers optimize code according to the specified optimization level. You can use a number of options to specify the optimization levels, including `-O`, `-Mvect`, `-Mipa`, and `-Mconcur`. In addition, you can use several of the `-M<pgflag>` switches to control specific types of optimization and parallelization.

This chapter describes these optimization options:

<code>-fast</code>	<code>-Minline</code>	<code>-O</code>	<code>-Munroll</code>
<code>-Mconcur</code>	<code>-Mvect</code>	<code>-Minfo</code>	<code>-Mneginfo</code>
<code>-Msafeptr</code>			
<code>-fast</code>	<code>-Minline</code>	<code>-O</code>	<code>-Munroll</code>
<code>-Mconcur</code>	<code>-Mphi</code>	<code>-Mvect</code>	<code>-Minfo</code>
<code>-Mneginfo</code>	<code>-Mpfo</code>		

This chapter also describes how to choose optimization options to use with the PGI compilers. This overview is helpful if you are just getting started with one of the PGI compilers, or wish to experiment with individual optimizations.

Complete specifications of each of these options is available in the *Command-Line Options Reference* section of the *PGI Compiler Reference Manual*, [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

## 3.1. Overview of Optimization

In general, optimization involves using transformations and replacements that generate more efficient code. This is done by the compiler and involves replacements that are independent of the particular target processor's architecture as well as replacements that take advantage of the OpenPOWER architecture, instruction set and registers.

For discussion purposes, we categorize optimization:

- Local Optimization
- Global Optimization
- Loop Optimization
- Optimization Through Function Inlining
- Profile Feedback Optimization (PFO)

### 3.1.1. Local Optimization

A basic block is a sequence of statements in which the flow of control enters at the beginning and leaves at the end without the possibility of branching, except at the end. Local optimization is performed on a block-by-block basis within a program's basic blocks.

The PGI compilers perform many types of local optimization including: algebraic identity removal, constant folding, common sub-expression elimination, redundant load and store elimination, scheduling, strength reduction, and peephole optimizations.

### 3.1.2. Global Optimization

This optimization is performed on a subprogram/function over all its basic blocks. The optimizer performs control-flow and data-flow analysis for an entire program unit. All loops, including those formed by ad hoc branches such as IFs or GOTOs, are detected and optimized.

Global optimization includes: constant propagation, copy propagation, dead store elimination, global register allocation, invariant code motion, and induction variable elimination.

### 3.1.3. Loop Optimization: Unrolling, Vectorization and Parallelization

The performance of certain classes of loops may be improved through vectorization or unrolling options. Vectorization transforms loops to improve memory access performance and make use of packed vector instructions which perform the same operation on multiple data items concurrently. Unrolling replicates the body of loops to reduce loop branching overhead and provide better opportunities for local optimization, vectorization and scheduling of instructions. Performance for loops on systems with multiple processors may also improve using the parallelization features of the PGI compilers.

### 3.1.4. Function Inlining

This optimization allows a call to a function to be replaced by a copy of the body of that function. This optimization will sometimes speed up execution by eliminating the function call and return overhead. Function inlining may also create opportunities for other types of optimization. Function inlining is not always beneficial. When used improperly it may increase code size and generate less efficient code.

### 3.1.5. Profile-Feedback Optimization (PFO)

Profile-feedback optimization (PFO) makes use of information from a trace file produced by specially instrumented executables which capture and save information on branch frequency, function and subroutine call frequency, semi-invariant values, loop index ranges, and other input data dependent information that can only be collected dynamically during execution of a program.

By definition, use of profile-feedback optimization is a two-phase process: compilation and execution of a specially-instrumented executable, followed by a subsequent compilation which reads a trace file generated during the first phase and uses the information in that trace file to guide compiler optimizations.

## 3.2. Getting Started with Optimization

The first concern should be getting the program to execute and produce correct results. To get the program running, start by compiling and linking without optimization. Add `-O0` to the compile line to select no optimization; or add `-g` to debug the program easily and isolate any coding errors exposed during porting to OpenPOWER platform.

To get started quickly with optimization, a good set of options to use with any of the PGI compilers is `-fast`. For example:

```
$ pgfortran -fast -Mipa=fast,inline prog.f
```

For all of the PGI Fortran, C, and C++ compilers, the `-fast -Mipa=fast,inline` options generally produce code that is well-optimized without the possibility of significant slowdowns due to pathological cases.

- ▶ The `-fast` option is an aggregate option that includes a number of individual PGI compiler options; which PGI compiler options are included depends on the target for which compilation is performed.
- ▶ The `-Mipa=fast,inline` option invokes interprocedural analysis (IPA), including several IPA suboptions. The `inline` suboption enables automatic inlining with IPA. If you do not wish to use automatic inlining, you can compile with `-Mipa=fast` and use several IPA suboptions without inlining.

Aggregate options incorporate a generally optimal set of flags that enable use of SIMD instructions .

The following table shows the typical `-fast` options.

Table 7 Typical -fast Options

Use this option...	To do this...
-O2	Specifies a code optimization level of 2 and -Mvect=SIMD.
-Munroll=c:1	Unrolls loops, executing multiple instances of the original loop during each iteration.
-Mlre	Indicates loop-carried redundancy elimination.
-Mautoinline	Enables automatic function inlining in C & C++.

By experimenting with individual compiler options on a file-by-file basis, further significant performance gains can sometimes be realized. However, depending on the coding style, individual optimizations can sometimes cause slowdowns, and must be used carefully to ensure performance improvements.

There are other useful command line options related to optimization and parallelization, such as `-help`, `-Minfo`, `-Mneginfo`, `-dryrun`, and `-v`.

### 3.2.1. -help

As described in [Help with Command-Line Options](#), you can see a specification of any command-line option by invoking any of the PGI compilers with `-help` in combination with the option in question, without specifying any input files.

For example, you might want information on `-O`:

```
$ pgfortran -help -O
```

The resulting output is similar to this:

```
-O Set opt level. All -O1 optimizations plus traditional scheduling and
  global scalar optimizations performed
```

Or you can see the full functionality of `-help` itself, which can return information on either an individual option or groups of options:

```
$ pgfortran -help -help
```

The resulting output is similar to this:

```
-help[=groups|asm|debug|language|linker|opt|other|overall|
phase|prepro|suffix|switch|target|variable]
Show compiler switches
```

### 3.2.2. -Minfo

You can use the `-Minfo` option to display compile-time optimization listings. When this option is used, the PGI compilers issue informational messages to standard error (stderr) as compilation proceeds. From these messages, you can determine which loops are optimized using unrolling, vector instructions, vectorization, parallelization, interprocedural optimizations and various miscellaneous optimizations. You can also see where and whether functions are inlined.

For more information on `-Minfo`, refer to ‘Optimization Controls’ section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

### 3.2.3. -Mneginfo

You can use the `-Mneginfo` option to display informational messages to standard error (stderr) that explain why certain optimizations are inhibited.

For more information on `-Mneginfo`, refer to 'Optimization Controls' section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

### 3.2.4. -dryrun

The `-dryrun` option can be useful as a diagnostic tool if you need to see the steps used by the compiler driver to preprocess, compile, assemble and link in the presence of a given set of command line inputs. When you specify the `-dryrun` option, these steps are printed to standard error (stderr) but are not actually performed. For example, you can use this option to inspect the default and user-specified libraries that are searched during the link phase, and the order in which they are searched by the linker.

### 3.2.5. -v

The `-v` option is similar to `-dryrun`, except each compilation step is performed and not simply printed.

### 3.2.6. PGI Profiler

The PGI profiler is a profiling tool that provides a way to visualize the performance of the components of your program. Using tables and graphs, the profiler associates execution time and resource utilization data with the source code and instructions of your program. This association allows you to see where a program's execution time is spent. Through resource utilization data and compiler analysis information, the profiler helps you to understand why certain parts of your program have high execution times. This information may help you with selecting which optimization options to use with your program.

The profiler also allows you to correlate the messages produced by `-Minfo` and `-Mneginfo`, described above, to your program's source code. This feature is known as the [Common Compiler Feedback Format \(CCFF\)](#).

For more information on the profiler, refer to the [Profiler User's Guide](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19profug.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19profug.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19profug.pdf).

## 3.3. Common Compiler Feedback Format (CCFF)

Using the Common Compiler Feedback Format (CCFF), PGI compilers save information about how your program was optimized, or why a particular optimization was not made, in the executable file. To append this information to the object file, use the compiler option `-Minfo=ccff`.

If you choose to use the PGI profiler to aid with your optimization, it can extract this information and associate it with source code and other performance data, allowing you to view all of this information simultaneously in one of the available profiler panels.

## 3.4. Local and Global Optimization

This section describes local and global optimization.

### 3.4.1. -Msafepttr

The `-Msafepttr` option can significantly improve performance of C/C++ programs in which there is known to be no pointer aliasing. For obvious reasons, this command-line option must be used carefully. There are a number of suboptions for `-Msafepttr`:

- ▶ `-Msafepttr=all` – All pointers are safe. Equivalent to the default setting: `-Msafepttr`.
- ▶ `-Msafepttr=arg` – Function formal argument pointers are safe. Equivalent to `-Msafepttr=dummy`.
- ▶ `-Msafepttr=global` – Global pointers are safe.
- ▶ `-Msafepttr=local` – Local pointers are safe. Equivalent to `-Msafepttr=auto`.
- ▶ `-Msafepttr=static` – Static local pointers are safe.

If your C/C++ program has pointer aliasing and you also want automating inlining, then compiling with `-Mipa=fast` or `-Mipa=fast,inline` includes pointer aliasing optimizations. IPA may be able to optimize some of the alias references in your program and leave intact those that cannot be safely optimized.

### 3.4.2. -O

Using the PGI compiler commands with the `-O<level>` option (the capital O is for Optimize), you can specify any integer level from 0 to 4.

#### **-O0**

Level zero specifies no optimization. A basic block is generated for each language statement. At this level, the compiler generates a basic block for each statement.

Performance will almost always be slowest using this optimization level. This level is useful for the initial execution of a program. It is also useful for debugging, since there is a direct correlation between the program text and the code generated. To enable debugging, include `-g` on your compile line.

#### **-O1**

Level one specifies local optimization. Scheduling of basic blocks is performed. Register allocation is performed.

Local optimization is a good choice when the code is very irregular, such as code that contains many short statements containing IF statements and does not contain loops (DO or DO WHILE statements). Although this case rarely occurs, for certain types of code, this optimization level may perform better than level-two (-O2).

### **-O**

When no level is specified, level two global optimizations are performed, including traditional scalar optimizations, induction recognition, and loop invariant motion. No SIMD vectorization is enabled.

### **-O2**

Level two specifies global optimization. This level performs all level-one local optimization as well as level two global optimization described in -O. In addition, more advanced optimizations such as SIMD code generation, cache alignment, and partial redundancy elimination are enabled.

### **-O3**

Level three specifies aggressive global optimization. This level performs all level-one and level-two optimizations and enables more aggressive hoisting and scalar replacement optimizations that may or may not be profitable.

### **-O4**

Level four performs all level-one, level-two, and level-three optimizations and enables hoisting of guarded invariant floating point expressions.

## **Types of Optimizations**

The PGI compilers perform many different types of local optimizations, including but not limited to:

- Algebraic identity removal
- Constant folding
- Common subexpression elimination
- Local register optimization
- Peephole optimizations
- Redundant load and store elimination
- Strength reductions

Level-two optimization (-O2 or -O) specifies global optimization. The `-fast` option generally specifies global optimization; however, the `-fast` switch varies from release to release, depending on a reasonable selection of switches for any one particular release. The `-O` or `-O2` level performs all level-one local optimizations as well as global optimizations. Control flow analysis is applied and global registers are allocated for

all functions and subroutines. Loop regions are given special consideration. This optimization level is a good choice when the program contains loops, the loops are short, and the structure of the code is regular.

The PGI compilers perform many different types of global optimizations, including but not limited to:

- Branch to branch elimination
- Constant propagation
- Copy propagation
- Dead store elimination
- Global register allocation
- Induction variable elimination
- Invariant code motion

You can explicitly select the optimization level on the command line. For example, the following command line specifies level-two optimization which results in global optimization:

```
$ pgfortran -O2 prog.f
```

The default optimization level changes depending on which options you select on the command line. For example, when you select the `-g` debugging option, the default optimization level is set to level-zero (`-O0`). However, if you need to debug optimized code, you can use the `-gopt` option to generate debug information without perturbing optimization. For a description of the default levels, refer to [Default Optimization Levels](#).

The `-fast` option includes `-O2` on all targets. If you want to override the default for `-fast` with `-O3` while maintaining all other elements of `-fast`, simply compile as follows:

```
$ pgfortran -fast -O3 prog.f
```

## 3.5. Loop Unrolling using `-Munroll`

This optimization unrolls loops, which reduces branch overhead, and can improve execution speed by creating better opportunities for instruction scheduling. A loop with a constant count may be completely unrolled or partially unrolled. A loop with a non-constant count may also be unrolled. A candidate loop must be an innermost loop containing one to four blocks of code.

The following example shows the use of the `-Munroll` option:

```
$ pgfortran -Munroll prog.f
```

The `-Munroll` option is included as part of `-fast` on all targets. The loop unroller expands the contents of a loop and reduces the number of times a loop is executed. Branching overhead is reduced when a loop is unrolled two or more times, since each iteration of the unrolled loop corresponds to two or more iterations of the original loop;

the number of branch instructions executed is proportionately reduced. When a loop is unrolled completely, the loop's branch overhead is eliminated altogether.

Loop unrolling may be beneficial for the instruction scheduler. When a loop is completely unrolled or unrolled two or more times, opportunities for improved scheduling may be presented. The code generator can take advantage of more possibilities for instruction grouping or filling instruction delays found within the loop.

### Examples Showing Effect of Unrolling

The following side-by-side examples show the effect of code unrolling on a segment that computes a dot product.



This example is only meant to represent how the compiler can transform the loop; it is not meant to imply that the programmer needs to manually change code. In fact, manually unrolling your code can sometimes inhibit the compiler's analysis and optimization.

Table 8 Example of Effect of Code Unrolling

Dot Product Code	Unrolled Dot Product Code
<pre>REAL*4 A(100), B(100), Z INTEGER I DO I=1, 100   Z = Z + A(i) * B(i) END DO END</pre>	<pre>REAL*4 A(100), B(100), Z INTEGER I DO I=1, 100, 2   Z = Z + A(i) * B(i)   Z = Z + A(i+1) * B(i+1) END DO END</pre>

Using the `-Minfo` option, the compiler informs you when a loop is being unrolled. For example, a message similar to the following, indicating the line number, and the number of times the code is unrolled, displays when a loop is unrolled:

```
dot:
  5, Loop unrolled 5 times
```

Using the `c:<m>` and `n:<m>` sub-options to `-Munroll`, or using `-Mnounroll`, you can control whether and how loops are unrolled on a file-by-file basis. [Using directives or pragmas](#), you can precisely control whether and how a given loop is unrolled. For more information on `-Munroll`, refer to [Use Command-line Options](#).

## 3.6. Vectorization using `-Mvect`

The `-Mvect` option is included as part of `-fast` on all targets. If your program contains computationally-intensive loops, the `-Mvect` option may be helpful. If in addition you specify `-Minfo`, and your code contains loops that can be vectorized, the compiler reports relevant information on the optimizations applied.

When a PGI compiler command is invoked with the `-Mvect` option, the vectorizer scans code searching for loops that are candidates for high-level transformations such as loop distribution, loop interchange, cache tiling, and idiom recognition (replacement of a recognizable code sequence, such as a reduction loop, with optimized code sequences or function calls). When the vectorizer finds vectorization opportunities, it internally rearranges or replaces sections of loops (the vectorizer changes the code generated; your source code's loops are not altered). In addition to performing these loop transformations, the vectorizer produces extensive data dependence information for use by other phases of compilation and detects opportunities to use vector or packed vector instructions on OpenPOWER processors where these are supported.

The `-Mvect` option can speed up code which contains well-behaved countable loops which operate on large floating point arrays in Fortran and their C/C++ counterparts. However, it is possible that some codes will show a decrease in performance when compiled with the `-Mvect` option due to the generation of conditionally executed code segments, inability to determine data alignment, and other code generation factors. For this reason, it is recommended that you check carefully whether particular program units or loops show improved performance when compiled with this option enabled.

### 3.6.1. Vectorization Sub-options

The vectorizer performs high-level loop transformations on countable loops. A loop is countable if the number of iterations is set only before loop execution and cannot be modified during loop execution. Some of the vectorizer transformations can be controlled by arguments to the `-Mvect` command line option. The following sections describe the arguments that affect the operation of the vectorizer. In addition, some of these vectorizer operations can be controlled from within code using directives and pragmas. For details on the use of directives and pragmas, refer to [Using Directives and Pragmas](#).

The vectorizer performs the following operations:

- ▶ Loop interchange
- ▶ Loop splitting
- ▶ Loop fusion
- ▶ Memory-hierarchy (cache tiling) optimizations
- ▶ Generation of SIMD instructions on processors where these are supported
- ▶ Generation of prefetch instructions on processors where these are supported
- ▶ Loop iteration peeling to maximize vector alignment
- ▶ Alternate code generation

By default, `-Mvect` without any sub-options is equivalent to:

```
-Mvect=assoc,cachesize=c
```

where `c` is the actual cache size of the machine.

This enables the options for nested loop transformation and various other vectorizer options. These defaults may vary depending on the target system. The following table lists and briefly describes some of the `-Mvect` suboptions.

Table 9 -Mvect Suboptions

Use this option ...	To instruct the vectorizer to do this ...
-Mvect=altcode	Generate appropriate code for vectorized loops.
-Mvect=[no]assoc	Perform[disable] associativity conversions that can change the results of a computation due to a round-off error. For example, a typical optimization is to change one arithmetic operation to another arithmetic operation that is mathematically correct, but can be computationally different and generate faster code. This option is provided to enable or disable this transformation, since a round-off error for such associativity conversions may produce unacceptable results.
-Mvect=cachesize:n	Tile nested loop operations, assuming a data cache size of n bytes. By default, the vectorizer attempts to tile nested loop operations, such as matrix multiply, using multi-dimensional strip-mining techniques to maximize re-use of items in the data cache.
-Mvect=fuse	Enable loop fusion.
-Mvect=gather	Enable vectorization of indirect array references.
-Mvect=idiom	Enable idiom recognition.
-Mvect=levels:<n>	Set the maximum next level of loops to optimize.
-Mvect=nocond	Disable vectorization of loops with conditions.
-Mvect=partial	Enable partial loop vectorization via inner loop distribution.
-Mvect=prefetch	Automatically generate prefetch instructions when vectorizable loops are encountered, even in cases where SIMD instructions are not generated.
-Mvect=short	Enable short vector operations.
-Mvect=simd	Automatically generate packed SIMD, and prefetch instructions when vectorizable loops are encountered. SIMD instructions, first introduced on Pentium III and AthlonXP processors, operate on single-precision floating-point data.
-Mvect=sizelimit:n	Limit the size of vectorized loops.
-Mvect=sse	Equivalent to -Mvect=simd.
-Mvect=tile	Enable loop tiling.
-Mvect=uniform	Perform consistent optimizations in both vectorized and residual loops. Be aware that this may affect the performance of the residual loop.



Inserting no in front of the option disables the option. For example, to disable the generation of vector instructions on OpenPOWER, compile with -Mvect=nosimd.

## 3.6.2. Vectorization Example Using SIMD Instructions

One of the most important vectorization options is `-Mvect=simd`. When you use this option, the compiler automatically generates SIMD vector instructions, where possible, when targeting processors on which these instructions are supported. This process can improve performance by several factors compared with the equivalent scalar code. All of the PGI Fortran, C and C++ compilers support this capability.

In the program in [Vector operation using SIMD instructions](#), the vectorizer recognizes the vector operation in subroutine 'loop' when either compiler switch `-Mvect=simd` or `-fast` is used. This example shows the compilation, informational messages, and runtime results using SIMD instructions on an IBM POWER9 system, along with issues that affect SIMD performance.

Loops vectorized using SIMD instructions operate much more efficiently when processing vectors that are aligned to a cache-line boundary. You can cause unconstrained data objects of size 16 bytes or greater to be cache-aligned by compiling with the `-Mcache_align` switch. An unconstrained data object is a data object that is not a common block member and not a member of an aggregate data structure.



For stack-based local variables to be properly aligned, the main program or function must be compiled with `-Mcache_align`.

The `-Mcache_align` switch has no effect on the alignment of Fortran allocatable or automatic arrays. If you have arrays that are constrained, such as vectors that are members of Fortran common blocks, you must specifically pad your data structures to ensure proper cache alignment. You can use `-Mcache_align` for only the beginning address of each common block to be cache-aligned.

The following examples show the results of compiling the sample code in [Vector operation using SIMD instructions](#) both with and without the option `-Mvect=simd`.

### Vector operation using SIMD instructions

```
program vector_op
  parameter (N = 9999)
  real*4 x(N), y(N), z(N), w(N)
  do i = 1, n
    y(i) = i
    z(i) = 2*i
    w(i) = 4*i
  enddo
  do j = 1, 200000
    call loop(x,y,z,w,1.0e0,N)
  enddo
  print *, x(1),x(771),x(3618),x(6498),x(9999)
end
```

```
subroutine loop(a,b,c,d,s,n)
  integer i, n
  real*4 a(n), b(n), c(n), d(n),s
  do i = 1, n
    a(i) = b(i) + c(i) - s * d(i)
  enddo
end
```

Assume the preceding program is compiled as follows, where `-Mvect=nosimd` disables SIMD vectorization:

```
% pgfortran -fast -Mvect=nosimd -Minfo vadd.f -Mfree -o vadd
vector_op:
  4, Loop unrolled 16 times
    Generated 1 prefetches in scalar loop
  9, Loop not vectorized/parallelized: contains call
loop:
  18, Loop unrolled 8 times
    FMA (fused multiply-add) instruction(s) generated
```

The following output shows a sample result if the generated executable is run and timed on an IBM POWER9 system:

```
$ /bin/time vadd
-1.000000    -771.0000    -3618.000    -6498.000
-9999.000
2.31user 0.00system 0:02.57elapsed 89%CPU (0avgtext+0avgdata 6976maxresident)k
8192inputs+0outputs (4major+149minor)pagefaults 0swaps
```

Now, recompile with vectorization enabled, and you see results similar to these:

```
% pgfortran -fast -Minfo vadd.f -Mfree -o vadd
vector_op:
  4, Loop not vectorized: may not be beneficial
    Unrolled inner loop 8 times
    Residual loop unrolled 7 times (completely unrolled)
    Generated 1 prefetches in scalar loop
  9, Loop not vectorized/parallelized: contains call
loop:
  18, Generated 2 alternate versions of the loop
    Generated vector simd code for the loop
    Generated 3 prefetch instructions for the loop
    Generated vector simd code for the loop
    Generated 3 prefetch instructions for the loop
    Generated vector simd code for the loop
    Generated 3 prefetch instructions for the loop
    FMA (fused multiply-add) instruction(s) generated
```

Notice the informational messages for the loop at line 18. The first line of the message indicates that two alternate versions of the loop were generated. The loop count and alignments of the arrays determine which of these versions is executed. The next several lines indicate the loop was vectorized and that prefetch instructions have been generated for three loads to minimize latency of data transfers from main memory.

Executing again, you should see results similar to the following:

```
$ /bin/time vadd-simd
-1.000000    -771.0000    -3618.000    -6498.000
-9999.000
0.62user 0.00system 0:00.65elapsed 95%CPU (0avgtext+0avgdata 6976maxresident)k
0inputs+0outputs (0major+151minor)pagefaults 0swaps
```

The SIMD result is 3.7 times faster than the equivalent non-SIMD version of the program.

Speed-up realized by a given loop or program can vary widely based on a number of factors:

- ▶ When the vectors of data are resident in the data cache, performance improvement using SIMD instructions is most effective.

- ▶ If data is aligned properly, performance will be better in general than when using SIMD operations on unaligned data.
- ▶ If the compiler can guarantee that data is aligned properly, even more efficient sequences of SIMD instructions can be generated.
- ▶ The efficiency of loops that operate on single-precision data can be higher. SIMD instructions can operate on four single-precision elements concurrently, but only two double-precision elements.



Compiling with `-Mvect=simd` can result in numerical differences from the executables generated with less optimization. Certain vectorizable operations, for example dot products, are sensitive to order of operations and the associative transformations necessary to enable vectorization (or parallelization).

## 3.7. Auto-Parallelization using `-Mconcur`

With the `-Mconcur` option the compiler scans code searching for loops that are candidates for auto-parallelization. `-Mconcur` must be used at both compile-time and link-time. When the parallelizer finds opportunities for auto-parallelization, it parallelizes loops and you are informed of the line or loop being parallelized if the `-Minfo` option is present on the compile line. For a complete specification of `-Mconcur`, refer to the ‘Optimization Controls’ section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

A loop is considered parallelizable if it doesn't contain any cross-iteration data dependencies. Cross-iteration dependencies from reductions and expandable scalars are excluded from consideration, enabling more loops to be parallelizable. In general, loops with calls are not parallelized due to unknown side effects. Also, loops with low trip counts are not parallelized since the overhead in setting up and starting a parallel loop will likely outweigh the potential benefits. In addition, the default is not to parallelize innermost loops, since these often by definition are vectorizable and it is seldom profitable to both vectorize and parallelize the same loop, especially on multicore processors. Compiler switches and directives are available to let you override most of these restrictions on auto-parallelization.

### 3.7.1. Auto-Parallelization Sub-options

The parallelizer performs various operations that can be controlled by arguments to the `-Mconcur` command line option. The following sections describe these arguments that affect the operation of the parallelizer. In addition, these parallelizer operations can be controlled from within code using directives and pragmas. For details on the use of directives and pragmas, refer to [Using Directives and Pragmas](#).

By default, `-Mconcur` without any sub-options is equivalent to:

```
-Mconcur=dist:block
```

This enables parallelization of loops with blocked iteration allocation across the available threads of execution. These defaults may vary depending on the target system. The following table lists and briefly describes some of the `-Mconcur` suboptions.

Table 10 `-Mconcur` Suboptions

Use this option ...	To instruct the parallelizer to do this...
<code>-Mconcur=allcores</code>	Use all available cores. Specify this option at link time.
<code>-Mconcur=[no]altcode</code>	Generate [do not generate] alternate serial code for parallelized loops. If <code>altcode</code> is specified without arguments, the parallelizer determines an appropriate cutoff length and generates serial code to be executed whenever the loop count is less than or equal to that length.  If <code>altcode:n</code> is specified, the serial <code>altcode</code> is executed whenever the loop count is less than or equal to <code>n</code> . Specifying <code>noaltcode</code> disables this option and no alternate serial code is generated.
<code>-Mconcur=[no]assoc</code>	Enable [disable] parallelization of loops with associative reductions.
<code>-Mconcur=bind</code>	Bind threads to cores. Specify this option at link time.
<code>-Mconcur=cncall</code>	Specifies that it is safe to parallelize loops that contain subroutine or function calls. By default, such loops are excluded from consideration for auto-parallelization. Also, no minimum loop count threshold must be satisfied before parallelization occurs, and last values of scalars are assumed to be safe.
<code>-Mconcur=dist:{block cyclic}</code>	Specifies whether to assign loop iterations to the available threads in blocks or in a cyclic (round-robin) fashion. Block distribution is the default. If <code>cyclic</code> is specified, iterations are allocated to processors cyclically. That is, processor 0 performs iterations 0, 3, 6, etc.; processor 1 performs iterations 1, 4, 7, etc.; and processor 2 performs iterations 2, 5, 8, etc.
<code>-Mconcur=innermost</code>	Enable parallelization of innermost loops.
<code>-Mconcur=levels:&lt;n&gt;</code>	Parallelize loops nested at most <code>n</code> levels deep.
<code>-Mconcur=[no]numa</code>	Use thread/processors affinity when running on a NUMA architecture. Specifying <code>-Mconcur=nonuma</code> disables this option.

The environment variable `NCPUS` is checked at runtime for a parallel program. If `NCPUS` is set to 1, a parallel program runs serially, but will use the parallel routines generated during compilation. If `NCPUS` is set to a value greater than 1, the specified number of processors are used to execute the program. Setting `NCPUS` to a value exceeding the number of physical processors can produce inefficient execution. Executing a program on multiple processors in an environment where some of the

processors are being time-shared with another executing job can also result in inefficient execution.

As with the vectorizer, the `-Mconcur` option can speed up code if it contains well-behaved countable loops and/or computationally intensive nested loops that operate on arrays. However, it is possible that some codes show a decrease in performance on multi-processor systems when compiled with `-Mconcur` due to parallelization overheads, memory bandwidth limitations in the target system, false-sharing of cache lines, or other architectural or code-generation factors. For this reason, it is recommended that you check carefully whether particular program units or loops show improved performance when compiled using this option.

If the compiler is not able to successfully auto-parallelize your application, you should refer to [Using OpenMP](#). It is possible that insertion of explicit parallelization directives or pragmas, and use of the compiler option `-mp` might enable the application to run in parallel.

### 3.7.2. Loops That Fail to Parallelize

In spite of the sophisticated analysis and transformations performed by the compiler, programmers may notice loops that are seemingly parallel, but are not parallelized. In this subsection, we look at some examples of common situations where parallelization does not occur.

#### Innermost Loops

As noted earlier in this section, the PGI compilers will not parallelize innermost loops by default, because it is usually not profitable. However, you can override this default using the `-Mconcur=innermost` command-line option.

#### Timing Loops

Often, loops occur in programs that are similar to timing loops. The outer loop in the following example is one such loop.

```
do j = 1, 2
  do i = 1, n
1    a(i) = b(i) + c(i)
  enddo
enddo
```

The outer loop in the preceding example is not parallelized because the compiler detects a cross-iteration dependence in the assignment to `a(i)`. Suppose the outer loop were parallelized. Then both processors would simultaneously attempt to make assignments into `a(1:n)`. Now in general the values computed by each processor for `a(1:n)` will differ, so that simultaneous assignment into `a(1:n)` will produce values different from sequential execution of the loops.

In this example, values computed for `a(1:n)` don't depend on `j`, so that simultaneous assignment by both processors does not yield incorrect results. However, it is beyond the scope of the compilers' dependence analysis to determine that values computed

in one iteration of a loop don't differ from values computed in another iteration. So the worst case is assumed, and different iterations of the outer loop are assumed to compute different values for  $a(1:n)$ . Is this assumption too pessimistic? If  $j$  doesn't occur anywhere within a loop, the loop exists only to cause some delay, most probably to improve timing resolution. It is not usually valid to parallelize timing loops; to do so would distort the timing information for the inner loops.

## Scalars

Quite often, scalars will inhibit parallelization of non-innermost loops. There are two separate cases that present problems. In the first case, scalars appear to be expandable, but appear in non-innermost loops, as in the following example.

```
do j = 1, n
  x = b(j)
  do i = 1, n
    a(i,j) = x + c(i,j)
  enddo
enddo
```

There are a number of technical problems to be resolved in order to recognize expandable scalars in non-innermost loops. Until this generalization occurs, scalars like  $x$  in the preceding code segment inhibit parallelization of loops in which they are assigned. In the following example, scalar  $k$  is not expandable, and it is not an accumulator for a reduction.

```
      k = 1
      do i = 1, n
        do j = 1, n
1          a(j,i) = b(k) * x
        enddo
        k = i
2        if (i .gt. n/2) k = n - (i - n/2)
      enddo
```

If the outer loop is parallelized, conflicting values are stored into  $k$  by the various processors. The variable  $k$  cannot be made local to each processor because its value must remain coherent among the processors. It is possible the loop could be parallelized if all assignments to  $k$  are placed in critical sections. However, it is not clear where critical sections should be introduced because in general the value for  $k$  could depend on another scalar (or on  $k$  itself), and code to obtain the value of other scalars must reside in the same critical section.

In the previous example, the assignment to  $k$  within a conditional at label 2 prevents  $k$  from being recognized as an induction variable. If the conditional statement at label 2 is removed,  $k$  would be an induction variable whose value varies linearly with  $j$ , and the loop could be parallelized.

## Scalar Last Values

During parallelization, scalars within loops often need to be privatized; that is, each execution thread has its own independent copy of the scalar. Problems can arise if a

privatized scalar is accessed outside the loop. For example, consider the following loops in C/C++ and Fortran:

```
/* C/C++ version */
for (i = 1; i<N; i++){
    if( x[i] > 5.0 )
        t = x[i];
}
v = t;

f(v);
```

```
! Fortran version
do I = 1,N
    if (x(I) > 5.0 ) then
        t = x(I)
    endif
enddo
v = t

call f(v)
```

The value of `t` may not be computed on the last iteration of the loop. Normally, if a scalar is assigned within a loop and used following the loop, the PGI compilers save the last value of the scalar. However, if the loop is parallelized and the scalar is not assigned on every iteration, it may be difficult, without resorting to costly critical sections, to determine on what iteration `t` is last assigned. Analysis allows the compiler to determine that a scalar is assigned on each iteration and hence that the loop is safe to parallelize if the scalar is used later, as illustrated in the following C/C++ and Fortran examples.

```
/* C/C++ version */
for (i=1;i<n;i++) {
    if (x[i]>0.0) {
        t=2.0;
    }
    else {
        t=3.0;
        y[i]=t;
    }
}
v=t;
```

```
! Fortran version
do I = 1,N
    if (x(I)>0.0) then
        t=2.0
    else
        t=3.0
        y(i)=t
    endif
enddo
v=t
```

Notice that `t` is assigned on every iteration of the loop. However, there are cases where a scalar may be privatizable, but if it is used after the loop, it is unsafe to parallelize. Examine the following loops in which each use of `t` within the loop is reached by a definition from the same iteration.

```
/* C/C++ Version */
for (i=1;i<N;i++){
    if(x[i]>0.0){
```

```

    t=x[i];
    y[i]=t;
  }
}
v=t;

f(v);

```

```

! Fortran Version
do I = 1,N
  if (x(I)>0.0) then
    t=x(I)
    y(i)=t
  endif
enddo
v=t

call f(v)

```

Here `t` is privatizable, but the use of `t` outside the loop may yield incorrect results, since the compiler may not be able to detect on which iteration of the parallelized loop `t` is last assigned. The compiler detects the previous cases. When a scalar is used after the loop but is not defined on every iteration of the loop, parallelization does not occur.

When the programmer knows that the scalar is assigned on the last iteration of the loop, the programmer may use a directive or pragma to let the compiler know the loop is safe to parallelize. The directive or pragma `safe_lastval` informs the compiler that, for a given loop, all scalars are assigned in the last iteration of the loop; thus, it is safe to parallelize the loop. We could add the following line to any of our previous examples.

```

!pgi$1 safe_lastval      ! Fortran Version
#pragma loop safe_lastval      /* C/C++ Version */

```

The resulting code looks similar to this:

```

/* C/C++ Version */
#pragma loop safe_lastval
...
for (i=1;i<N;i++){
  if(x[i]>5.0 ) t=x[i];
}
v = t;

```

```

! Fortran Version
!pgi$1 safe_lastv
...
do I = 1,N
  if (x(I) > 5.0 ) then
    t = x(I)
  endif
enddo
v = t

```

In addition, a command-line option `-Msafe_lastval` provides this information for all loops within the routines being compiled, which essentially provides global scope.

## 3.8. Default Optimization Levels

The following table shows the interaction between the `-O<level>`, `-g`, and `-M<opt>` options. In the table, level can be 0, 1, 2, 3 or 4, and `<opt>` can be `vect`, `concur`, `unroll` or `ipa`. The default optimization level is dependent upon these command-line options.

Table 11 Optimization and `-O`, `-g` and `-M<opt>` Options

Optimize Option	Debug Option	-M<opt> Option	Optimization Level
none	none	none	1
none	none	-M<opt>	2
none	-g	none	0
-O	none or -g	none	2
-O<level>	none or -g	none	level
-O<level> <= 2	none or -g	-M<opt>	2

Code that is not optimized yet compiled using the option `-O0` can be significantly slower than code generated at other optimization levels. The `-M<opt>` option, where `<opt>` is `vect`, `concur`, `unroll` or `ipa`, sets the optimization level to 2 if no `-O` options are supplied. The `-fast` option sets the optimization level to a target-dependent optimization level if no `-O` options are supplied.

## 3.9. Local Optimization Using Directives and Pragmas

Command-line options let you specify optimizations for an entire source file. Directives supplied within a Fortran source file and pragmas supplied within a C or C++ source file provide information to the compiler and alter the effects of certain command-line options or the default behavior of the compiler. (Many directives have a corresponding command-line option.)

While a command line option affects the entire source file that is being compiled, directives and pragmas let you do the following:

- ▶ Apply, or disable, the effects of a particular command-line option to selected subprograms or to selected loops in the source file (for example, an optimization).
- ▶ Globally override command-line options.
- ▶ Tune selected routines or loops based on your knowledge or on information obtained through profiling.

[Using Directives and Pragmas](#) provides details on how to add directives and pragmas to your source files.

## 3.10. Execution Timing and Instruction Counting

As this chapter describes, once you have a program that compiles, executes and gives correct results, you may optimize your code for execution efficiency.

Selecting the correct optimization level requires some thought and may require that you compare several optimization levels before arriving at the best solution. To compare optimization levels, you need to measure the execution time for your program. There are several approaches you can take for timing execution.

- ▶ You can use shell commands that provide execution time statistics.
- ▶ You can include function calls in your code that provide timing information.
- ▶ You can profile sections of code.

Timing functions available with the PGI compilers include these:

- ▶ 3F timing routines.
- ▶ The SECNDS pre-declared function in PGFORTRAN.
- ▶ The SYSTEM\_CLOCK or CPU\_CLOCK intrinsics in PGF95 or PGFORTRAN.

In general, when timing a program, you should try to eliminate or reduce the amount of system level activities such as I/O, program loading, and task switching.

The following example shows a *fragment* that indicates how to use SYSTEM\_CLOCK effectively within a Fortran program unit.

### Using SYSTEM\_CLOCK code fragment

```
integer :: nprocs, hz, clock0, clock1
real :: time
call system_clock(count_rate=hz)
call system_clock(count=clock0)
< do work >
call system_clock(count=clock1)
t = (clock1 - clock0)
time = real(t) / real(hz)
```

Or you can use the F90 `cpu_time` subroutine:

```
real :: t1, t2, time
call cpu_time(t1)
< do work >
call cpu_time(t2)
time = t2 - t1
```

## 3.11. Portability of Multi-Threaded Programs on Linux

PGI created the library `libnuma` to handle the variations between various implementations of Linux.

Some older versions of Linux are lacking certain features that support multi-processor and multicore systems; in particular, the system call 'sched\_setaffinity' and the `numa`

library `libnuma`. The PGI runtime library uses these features to implement some `-Mconcur` and `-mp` operations.

These variations led to the creation of the PGI library: `libnuma`, which is used on all 64-bit Linux systems.

When a program is linked with the system `libnuma` library, the program depends on that library to run. On systems without a `libnuma` library, the PGI version of `libnuma` provides the required stubs so that the program links and executes properly. If the program is linked with `libnuma`, the differences between systems is masked by the different versions of `libnuma`.

When a program is deployed to the target system, the proper set of libraries, real or stub, should be deployed with the program.

This facility requires that the program be dynamically linked with `libnuma`.

### 3.11.1. `libnuma`

Not all systems have `libnuma`. Typically, only numa systems have this library. PGI supplies a stub version of `libnuma` which satisfies the calls from the PGI runtime to `libnuma`. `libnuma` is a shared library that is linked dynamically at runtime.

The reason to have a numa library on all systems is to allow multi-threaded programs, such as programs compiled with `-Mconcur` or `-mp`, to be compiled, linked, and executed without regard to whether the host or target systems has a numa library. When the numa library is not available, a multi-threaded program still runs because the calls to the numa library are satisfied by the PGI stub library.

During installation, the installation procedure checks for the existence of a real `libnuma` among the system libraries. If the real library is not found, the PGI stub version is substituted.

# Chapter 4.

## USING FUNCTION INLINING

Function inlining replaces a call to a function or a subroutine with the body of the function or subroutine. This can speed up execution by eliminating parameter passing and function/subroutine call and return overhead. It also allows the compiler to optimize the function with the rest of the code. Note that using function inlining indiscriminately can result in much larger code size and no increase in execution speed.

The PGI compilers provide two categories of inlining:

- ▶ **Automatic function inlining** – In C/C++, you can inline static functions with the `inline` keyword by using the `-Mautoinline` option, which is included with `-fast`.
- ▶ **Function inlining** – You can inline functions which were extracted to the inline libraries in C/Fortran/C++. There are two ways of enabling function inlining: with and without the `lib` suboption. For the latter, you create inline libraries, for example using the `pgfortran` compiler driver and the `-o` and `-Mextract` options.

There are important restrictions on inlining. Inlining only applies to certain types of functions. Refer to [Restrictions on Inlining](#) for more details on function inlining limitations.

This section describes how to use the following options related to function inlining:

```
-Mautoinline  
-Mextract  
-Minline  
-Mnoinline  
-Mrecursive
```

### 4.1. Automatic function inlining in C/C++

To enable automatic function inlining in C/C++ for static functions with the `inline` keyword, use the `-Mautoinline` option (included in `-fast`). Use `-Mnoautoinline` to disable it.

Several `-Mautoinline` suboptions let you determine the selection criteria. These suboptions are:

**maxsize:n**

Automatically inline functions size `n` and less

**totalsize:n**

Limit automatic inlining to total size of `n`

## 4.2. Invoking Function Inlining

To invoke the function inliner, use the `-Minline` option. If you do not specify an inline library, the compiler performs a special prepass on all source files named on the compiler command line before it compiles any of them. This pass extracts functions that meet the requirements for inlining and puts them in a temporary inline library for use by the compilation pass.

Several `-Minline` suboptions let you determine the selection criteria for functions to be inlined. These suboptions include:

**except:func**

Inlines all eligible functions except `func`, a function in the source text. You can use a comma-separated list to specify multiple functions.

**[name:]func**

Inlines all functions in the source text whose name matches `func`. You can use a comma-separated list to specify multiple functions.

**[maxsize:]number**

A numeric option is assumed to be a size. Functions of size `number` or less are inlined. If both `number` and `function` are specified, then functions matching the given name(s) or meeting the size requirements are inlined.

**reshape**

Fortran subprograms with array arguments are not inlined by default if the array shape does not match the shape in the caller. Use this option to override the default.

**smallsize:number**

Always inline functions of size smaller than `number` regardless of other size limits.

**totalsize:number**

Stop inlining in a function when the function's total inlined size reaches the `number` specified.

**[lib:]file.ext**

Instructs the inliner to inline the functions within the library file `file.ext`. If no inline library is specified, functions are extracted from a temporary library created during an extract prepass.



**Tip** Create the library file using the `-Mextract` option.

If you specify both a function name and a `maxsize n`, the compiler inlines functions that match the function name *or* have `n` or fewer statements.

If a name is used without a keyword, then a name with a period is assumed to be an inline library and a name without a period is assumed to be a function name. If a number is used without a keyword, the number is assumed to be a size.

Inlining can be disabled with `-Mnoinline`.

In the following example, the compiler inlines functions with fewer than approximately 100 statements in the source file `myprog.f` and writes the executable code in the default output file `a.out`.

```
$ pgfortran -Minline=maxsize:100 myprog.f
```

For more information on the `-Minline` options, refer to ‘-M Options by Category’ section of the [PGI Compiler Reference Manual](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf), [www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf](http://www.pgroup.com/resources/docs/19.1/pdf/pgi19ref-openpower.pdf).

### 4.3. Using an Inline Library

If you specify one or more inline libraries on the command line with the `-Minline` option, the compiler does not perform an initial extract pass. The compiler selects functions to inline from the specified inline library. If you also specify a size or function name, all functions in the inline library meeting the selection criteria are selected for inline expansion at points in the source text where they are called.

If you do not specify a function name or a size limitation for the `-Minline` option, the compiler inlines every function in the inline library that matches a function in the source text.

In the following example, the compiler inlines the function `proc` from the inline library `lib.il` and writes the executable code in the default output file `a.out`.

```
$ pgfortran -Minline=name:proc,lib:lib.il myprog.f
```

The following command line is equivalent to the preceding line, with the exception that in the following example does not use the keywords `name:` and `lib:`. You typically use keywords to avoid name conflicts when you use an inline library name that does not contain a period. Otherwise, without the keywords, a period informs the compiler that the file on the command line is an inline library.

```
$ pgfortran -Minline=proc,lib.il myprog.f
```

### 4.4. Creating an Inline Library

You can create or update an inline library using the `-Mextract` command-line option. If you do not specify selection criteria with the `-Mextract` option, the compiler attempts to extract all subprograms.

Several `-Mextract` options let you determine the selection criteria for creating or updating an inline library. These selection criteria include:

**func**

Extracts the function `func`. you can use a comma-separated list to specify multiple functions.

**[name:]func**

Extracts the functions whose name matches `func`, a function in the source text.

**[size:]n**

Limits the size of the extracted functions to functions with a statement count less than or equal to `n`, the specified size.



The size `n` may not exactly equal the number of statements in a selected function; the size parameter is merely a rough gauge.

**[lib:]ext.lib**

Stores the extracted information in the library directory `ext.lib`.

If no inline library is specified, functions are extracted to a temporary library created during an extract prepass for use during the compilation stage.

When you use the `-Mextract` option, only the extract phase is performed; the compile and link phases are not performed. The output of an extract pass is a library of functions available for inlining. This output is placed in the inline library file specified on the command line with the `-o filename` specification. If the library file exists, new information is appended to it. If the file does not exist, it is created. You can use a command similar to the following:

```
$ pgfortran -Mextract=lib:lib.il myfunc.f
```

You can use the `-Minline` option with the `-Mextract` option. In this case, the extracted library of functions can have other functions inlined into the library. Using both options enables you to obtain more than one level of inlining. In this situation, if you do not specify a library with the `-Minline` option, the inline process consists of two extract passes. The first pass is a hidden pass implied by the `-Minline` option, during which the compiler extracts functions and places them into a temporary library. The second pass uses the results of the first pass but puts its results into the library that you specify with the `-o` option.

### 4.4.1. Working with Inline Libraries

An inline library is implemented as a directory with each inline function in the library stored as a file using an encoded form of the inlinable function.

A special file named `TOC` in the inline library directory serves as a table of contents for the inline library. This is a printable, ASCII file which you can examine to locate information about the library contents, such as names and sizes of functions, the source file from which they were extracted, the version number of the extractor which created the entry, and so on.

Libraries and their elements can be manipulated using ordinary system commands.

- ▶ Inline libraries can be copied or renamed.
- ▶ Elements of libraries can be deleted or copied from one library to another.
- ▶ The `ls` or `dir` command can be used to determine the last-change date of a library entry.

## 4.4.2. Dependencies

When a library is created or updated using one of the PGI compilers, the last-change date of the library directory is updated. This allows a library to be listed as a dependence in a makefile and ensures that the necessary compilations are performed when a library is changed.

## 4.4.3. Updating Inline Libraries - Makefiles

If you use inline libraries you must be certain that they remain up-to-date with the source files into which they are inlined. One way to assure inline libraries are updated is to include them in a makefile.

The makefile fragment in the following example assumes the file `utils.f` contains a number of small functions used in the files `parser.f` and `alloc.f`.

This portion of the makefile:

- ▶ Maintains the inline library `utils.il`.
- ▶ Updates the library whenever you change `utils.f` or one of the include files it uses.
- ▶ Compiles `parser.f` and `alloc.f` whenever you update the library.

### Sample Makefile

```
SRC = mydir
FC = pgfortran
FFLAGS = -O2
main.o: $(SRC)/main.f $(SRC)/global.h
$(FC) $(FFLAGS) -c $(SRC)/main.f
utils.o: $(SRC)/utils.f $(SRC)/global.h $(SRC)/utils.h
$(FC) $(FFLAGS) -c $(SRC)/utils.f
utils.il: $(SRC)/utils.f $(SRC)/global.h $(SRC)/utils.h
$(FC) $(FFLAGS) -Mextract=15 -o utils.il $(SRC)/utils.f
parser.o: $(SRC)/parser.f $(SRC)/global.h utils.il
$(FC) $(FFLAGS) -Minline=utils.il -c $(SRC)/parser.f
alloc.o: $(SRC)/alloc.f $(SRC)/global.h utils.il
$(FC) $(FFLAGS) -Minline=utils.il -c $(SRC)/alloc.f
myprog: main.o utils.o parser.o alloc.o
$(FC) -o myprog main.o utils.o parser.o alloc.o
```

## 4.5. Error Detection during Inlining

You can specify the `-Minfo=inline` option to request inlining information from the compiler when you invoke the inliner. For example:

```
$ pgfortran -Minline=mylib.il -Minfo=inline myext.f
```

## 4.6. Examples

Assume the program `dhry` consists of a single source file `dhry.f`. The following command line builds an executable file for `dhry` in which `proc7` is inlined wherever it is called:

```
$ pgfortran dhry.f -Minline=proc7
```

The following command lines build an executable file for `dhry` in which `proc7` plus any functions of approximately 10 or fewer statements are inlined (one level only).



The specified functions are inlined only if they are previously placed in the inline library, `temp.il`, during the extract phase.

```
$ pgfortran dhry.f -Mextract=lib:temp.il
$ pgfortran dhry.f -Minline=10,proc7,temp.il
```

Using the same source file `dhry.f`, the following example builds an executable for `dhry` in which all functions of roughly ten or fewer statements are inlined. Two levels of inlining are performed. This means that if function A calls function B, and B calls C, and both B and C are inlinable, then the version of B which is inlined into A will have had C inlined into it.

```
$ pgfortran dhry.f -Minline=maxsize:10
```

## 4.7. Restrictions on Inlining

The following Fortran subprograms cannot be extracted:

- ▶ Main or BLOCK DATA programs.
- ▶ Subprograms containing alternate return, assigned GO TO, DATA, SAVE, or EQUIVALENCE statements.
- ▶ Subprograms containing FORMAT statements.
- ▶ Subprograms containing multiple entries.

A Fortran subprogram is not inlined if any of the following applies:

- ▶ It is referenced in a statement function.
- ▶ A common block mismatch exists; in other words, the caller must contain all common blocks specified in the callee, and elements of the common blocks must agree in name, order, and type (except that the caller's common block can have additional members appended to the end of the common block).
- ▶ An argument mismatch exists; in other words, the number and type (size) of actual and formal parameters must be equal.
- ▶ A name clash exists, such as a call to subroutine `xyz` in the extracted subprogram and a variable named `xyz` in the caller.

The following types of C and C++ functions cannot be inlined:

- ▶ Functions containing switch statements

- ▶ Functions which reference a static variable whose definition is nested within the function
- ▶ Functions which accept a variable number of arguments

Certain C/C++ functions can only be inlined into the file that contains their definition:

- ▶ Static functions
- ▶ Functions which call a static function
- ▶ Functions which reference a static variable

# Chapter 5.

## USING OPENMP

The PGFORTRAN Fortran compiler supports the OpenMP Fortran Application Program Interface. The PGCC and PGC++ compilers support the OpenMP C/C++ Application Program Interface.

OpenMP is a specification for a set of compiler directives, an applications programming interface (API), and a set of environment variables that can be used to specify shared memory parallelism in FORTRAN and C/C++ programs. OpenMP may be used to obtain most of the parallel performance you can expect from your code, or it may serve as a stepping stone to parallelizing an entire application with MPI.

This section provides information on OpenMP as it is supported by PGI compilers. Currently, all PGI compilers support the version 3.1 OpenMP specification.

Use the `-mp` compiler switch to enable processing of the OpenMP pragmas listed in this section. As of the PGI 2011 Release, the OpenMP runtime library is linked by default. Note that GNU pthreads are not completely interoperable with OpenMP threads.



When using `pgc++` on Linux, the GNU STL is thread-safe to the extent listed in the GNU documentation as required by the C++11 standard. If an STL thread-safe issue is suspected, the suspect code can be run sequentially inside of an OpenMP region using `#pragma omp critical` sections.

This section describes how to use the following option supporting OpenMP: `-mp`

## 5.1. OpenMP Overview

### OpenMP 3.1

The PGI Fortran, C, and C++ compilers support OpenMP 3.1 on all platforms.

### OpenMP 4.5

The PGI Fortran, C, and C++ compilers compile most OpenMP 4.5 programs for parallel execution across all the cores of a multicore CPU or server. **target** regions are

implemented with default support for the multicore host as the target, and **parallel** and **distribute** loops are parallelized across all OpenMP threads.

Current limitations include:

- ▶ The **simd** construct can be used to provide tuning hints; the **simd** construct's **private**, **lastprivate**, **reduction**, and **collapse** clauses are processed and supported.
- ▶ The **declare simd** construct is ignored.
- ▶ The **ordered** construct's **simd** clause is ignored.
- ▶ The **task** construct's **depend** and **priority** clauses are not supported.
- ▶ The loop construct's **linear**, **schedule**, and **ordered(n)** clauses are not supported.
- ▶ The **declare reduction** directive is not supported.

### 5.1.1. OpenMP Shared-Memory Parallel Programming Model

The OpenMP shared-memory programming model is a collection of compiler directives or pragmas, library routines, and environment variables that can be used to specify shared-memory parallelism in Fortran and in C/C++ programs.

#### Fortran directives and C/C++ pragmas

Allow users to mark sections of code that can be executed in parallel when the code is compiled using the `-mp` switch.

When this switch is not present, the compiler ignores these directives and pragmas.

Fixed-form Fortran OpenMP directives begin with `!$OMP`, `C$OMP`, or `*$OMP`, beginning in column 1. Free-form Fortran OpenMP pragmas begin with `!$OMP`. OpenMP pragmas for C/C++ begin with `#pragma omp`. This format allows the user to have a single source code file for use with or without the `-mp` switch, as these lines are then merely viewed as comments when `-mp` is not present.

These directives and pragmas allow the user to create task, loop, and parallel section work-sharing constructs and synchronization constructs. They also allow the user to define how data is shared or copied between parallel threads of execution.



The data environment is controlled either by using clauses on the directives or pragmas, or with additional directives or pragmas.

#### Runtime library routines

Are available to query the parallel runtime environment, for example to determine how many threads are participating in execution of a parallel region.

#### Environment variables

Are available to control the execution behavior of parallel programs. For more information, see the [OpenMP website](http://www.openmp.org), <http://www.openmp.org>.

#### Macro substitution

C and C++ pragmas are subject to macro replacement after `#pragma omp`.

## 5.1.2. Terminology

For OpenMP 3.1 there are a number of terms for which it is useful to have common definitions.

### Thread

An execution entity with a stack and associated static memory, called *threadprivate memory*.

- ▶ An OpenMP thread is a thread that is managed by the OpenMP runtime system.
- ▶ A thread-safe routine is a routine that performs the intended function even when executed concurrently, that is, by more than one thread.

### Region

All code encountered during a specific instance of the execution of a given construct or of an OpenMP library routine. A region includes any code in called routines as well as any implicit code introduced by the OpenMP implementation.

Regions are *nested* if one region is (dynamically) enclosed by another region, that is, a region is encountered during the execution of another region. PGI supports both lexically and non-lexically nested parallel regions.

### Parallel region

In OpenMP 3.1 there is a distinction between a parallel region and an active parallel region. A parallel region can be either inactive or active.

- ▶ An inactive parallel region is executed by a single thread.
- ▶ An active parallel region is a parallel region that is executed by a team consisting of more than one thread.



The definition of an active parallel region changed between OpenMP 2.5 and OpenMP 3.1. In OpenMP 2.5, the definition was a parallel region whose IF clause evaluates to true. To examine the significance of this change, look at the following example:

```

program test
  logical omp_in_parallel

!$omp parallel
  print *, omp_in_parallel()
!$omp end parallel

  stop
end

```

Suppose we run this program with OMP\_NUM\_THREADS set to one. In OpenMP 2.5, this program yields T while in OpenMP 3.1, the program yields F. In OpenMP 3.1, execution is not occurring by more than one thread. Therefore, change in this definition may mean previous programs require modification.

### Task

A specific instance of executable code and its data environment, generated when a thread encounters a task construct or a parallel construct.

### 5.1.3. OpenMP Example

Look at the following simple OpenMP example involving loops.

#### OpenMP Loop Example

```

PROGRAM MAIN
  INTEGER I, N, OMP_GET_THREAD_NUM
  REAL*8 V(1000), GSUM, LSUM
  GSUM = 0.0D0
  N = 1000
  DO I = 1, N
    V(I) = DBLE(I)
  ENDDO

!$OMP PARALLEL PRIVATE(I, LSUM) SHARED(V, GSUM, N)
  LSUM = 0.0D0
!$OMP DO
  DO I = 1, N
    LSUM = LSUM + V(I)
  ENDDO
!$OMP END DO
!$OMP CRITICAL
  print *, "Thread ", OMP_GET_THREAD_NUM(), " local sum: ", LSUM
  GSUM = GSUM + LSUM
!$OMP END CRITICAL
!$OMP END PARALLEL

  PRINT *, "Global Sum: ", GSUM
  STOP
END

```

If you execute this example with the environment variable `OMP_NUM_THREADS` set to 4, then the output looks similar to this:

```

Thread          0 local sum:      31375.000000000000
Thread          1 local sum:      93875.000000000000
Thread          2 local sum:     156375.000000000000
Thread          3 local sum:     218875.000000000000
Global Sum:     500500.000000000000
FORTRAN STOP

```

## 5.2. Task Overview

Every part of an OpenMP program is part of a task. A task, whose execution can be performed immediately or delayed, has these characteristics:

- ▶ Code to execute
- ▶ A data environment – that is, it owns its data
- ▶ An assigned thread that executes the code and uses the data.

There are two activities associated with tasks: packaging and execution.

- ▶ Packaging: Each encountering thread packages a new instance of a task – code and data.
- ▶ Execution: Some thread in the team executes the task at some later time.

In the following sections, we use this terminology:

**Task**

The package of code and instructions for allocating data created when a thread encounters a task construct. A task can be implicit or explicit.

- ▶ An explicit task is a task generated when a task construct is encountered during execution.
- ▶ An implicit task is a task generated by the implicit parallel region or generated when a parallel construct is encountered during execution.

**Task construct**

A task directive or pragma plus a structured block.

**Task region**

The dynamic sequence of instructions produced by the execution of a task by a thread.

## 5.3. Fortran Parallelization Directives

Parallelization directives are comments in a program that are interpreted by the PGI Fortran compilers when the option `-mp` is specified on the command line. The form of a parallelization directive is:

```
sentinel directive_name [clauses]
```

With the exception of the SGI-compatible DOACROSS directive, the *sentinel* must comply with these rules:

- ▶ Be one of these: `!$OMP`, `C$OMP`, or `*$OMP`.
- ▶ Must start in column 1 (one) for free-form code.
- ▶ Must appear as a single word without embedded white space.
- ▶ The sentinel marking a DOACROSS directive is `C$`.

The *directive\_name* can be any of the directives listed in [Directive and Pragma Summary Table](#). The valid clauses depend on the directive. [Directive and Pragma Clauses](#) provides a list of clauses, the directives and pragmas to which they apply, and their functionality.

In addition to the sentinel rules, the directive must also comply with these rules:

- ▶ Standard Fortran syntax restrictions, such as line length, case insensitivity, and so on, apply to the directive line.
- ▶ Initial directive lines must have a space or zero in column six.
- ▶ Continuation directive lines must have a character other than a space or a zero in column six. Continuation lines for `C$DOACROSS` directives are specified using the `C$&` sentinel.
- ▶ Directives which are presented in pairs must be used in pairs.

Clauses associated with directives have these characteristics:

- ▶ The order in which clauses appear in the parallelization directives is not significant.
- ▶ Commas separate clauses within the directives, but commas are not allowed between the directive name and the first clause.
- ▶ Clauses on directives may be repeated as needed, subject to the restrictions listed in the description of each clause.

## 5.4. C/C++ Parallelization Pragmas

Parallelization pragmas are `#pragma` statements in a C or C++ program that are interpreted by the PGI C and C++ compilers when the option `-mp` is specified on the command line. The form of a parallelization pragma is:

```
#pragma omp    pragma_name    [clauses]
```

The format for pragmas include these rules:

- ▶ The pragmas follow the conventions of the C and C++ rules.
- ▶ Whitespace can appear before and after the `#`.
- ▶ Preprocessing tokens following the `#pragma omp` are subject to macro replacement.
- ▶ The order in which clauses appear in the parallelization pragmas is not significant.
- ▶ Spaces separate clauses within the pragmas.
- ▶ Clauses on pragmas may be repeated as needed subject to the restrictions listed in the description of each clause.

For the purposes of the OpenMP pragmas, a C/C++ structured block is defined to be a statement or compound statement (a sequence of statements beginning with `{` and ending with `}`) that has a single entry and a single exit. No statement or compound statement is a C/C++ structured block if there is a jump into or out of that statement.

## 5.5. Directive and Pragma Recognition

The compiler option `-mp` enables recognition of the parallelization directives and pragmas.

The use of this option also implies:

### **-Miomutex**

For directives, critical sections are generated around Fortran I/O statements.

For pragmas, calls to I/O library functions are system-dependent and are not necessarily guaranteed to be thread-safe. I/O library calls within parallel regions should be protected by critical regions to ensure they function correctly on all systems.

## 5.6. Directive and Pragma Summary Table

The following table provides a brief summary of the directives and pragmas that PGI supports.



In the table, the values in uppercase letters are Fortran directives while the names in lowercase letters are C/C++ pragmas.

## 5.6.1. Directive and Pragma Summary Table

Table 12 Directive and Pragma Summary Table

Fortran Directive and C++ Pragma	Description
ATOMIC [TYPE] ... END ATOMIC and atomic	<p>Semantically equivalent to enclosing a single statement in the CRITICAL...END CRITICAL directive or critical pragma.</p> <p>TYPE may be empty or one of the following: UPDATE, READ, WRITE, or CAPTURE. The END ATOMIC directive is only allowed when ending ATOMIC CAPTURE regions.</p> <div style="background-color: #e0f0e0; padding: 5px; border: 1px solid #ccc;">  Only certain statements are allowed.         </div>
BARRIER and barrier	Synchronizes all threads at a specific point in a program so that all threads complete work to that point before any thread continues.
CRITICAL ... END CRITICAL and critical	Defines a subsection of code within a parallel region, a critical section, which is executed one thread at a time.
DO...END DO and for	Provides a mechanism for distribution of loop iterations across the available threads in a parallel region.
C\$DOACROSS	Specifies that the compiler should parallelize the loop to which it applies, even though that loop is not contained within a parallel region.
FLUSH and flush	When this appears, all processor-visible data items, or, when a list is present (FLUSH [list]), only those specified in the list, are written to memory, thus ensuring that all the threads in a team have a consistent view of certain objects in memory.
MASTER ... END MASTER and master	Designates code that executes on the master thread and that is skipped by the other threads.
ORDERED and ordered	Defines a code block that is executed by only one thread at a time, and in the order of the loop iterations; this makes the ordered code block sequential, while allowing parallel execution of statements outside the code block.
PARALLEL DO and parallel for	Enables you to specify which loops the compiler should parallelize.
PARALLEL ... END PARALLEL and parallel	Supports a fork/join execution model in which a single thread executes all statements until a parallel region is encountered.
PARALLEL SECTIONS and parallel sections	Defines a non-iterative work-sharing construct without the need to define an enclosing parallel region.
PARALLEL WORKSHARE ... END PARALLEL WORKSHARE	Provides a short form method for including a WORKSHARE directive inside a PARALLEL construct.
SECTIONS ... END SECTIONS and sections	Defines a non-iterative work-sharing construct within a parallel region.
SINGLE ... END SINGLE and single	Designates code that executes on a single thread and that is skipped by the other threads.
TASK and task	Defines an explicit task.

Fortran Directive and C++ Pragma	Description
TASKYIELD and taskyield	Specifies a scheduling point for a task where the currently executing task may be yielded, and a different deferred task may be executed.
TASKWAIT and taskwait	Specifies a wait on the completion of child tasks generated since the beginning of the current task.
THREADPRIVATE and threadprivate	When a common block or variable that is initialized appears in this directive or pragma, each thread's copy is initialized once prior to its first use.
WORKSHARE ... END WORKSHARE	Provides a mechanism to effect parallel execution of non-iterative but implicitly data parallel constructs.

## 5.7. Directive and Pragma Clauses

Some directives and pragmas accept clauses that further allow a user to control the scope attributes of variables for the duration of the directive or pragma. Not all clauses are allowed on all directives, so the clauses that are valid are included with the description of the directive and pragma.

The following table provides a brief summary of the clauses associated with OPENMP directives and pragmas that PGI supports.

For complete information on these clauses, refer to the OpenMP documentation available on the World Wide Web.

Table 13 Directive and Pragma Summary Table

This clause...	Applies to this directive	Applies to this pragma	Has this functionality
CAPTURE	ATOMIC	atomic	Specifies that the atomic action is reading and updating, or writing and updating a value, capturing the intermediate state.
COLLAPSE (n)	DO...END DO PARALLEL DO PARALLEL WORKSHARE	parallel for	Specifies how many loops are associated with the loop construct.
COPYIN (list)	PARALLEL PARALLEL DO PARALLEL SECTIONS PARALLEL WORKSHARE	parallel parallel for	Allows threads to access the master thread's value, for a threadprivate variable. You assign the same value to threadprivate variables for each thread in the team executing the parallel region. Then, for each variable specified, the value of the variable in the master thread of the team is copied to the threadprivate

This clause...	Applies to this directive	Applies to this pragma	Has this functionality
			copies at the beginning of the parallel region.
COPYPRIVATE(list)	SINGLE	single	Specifies that one or more variables should be shared among all threads. This clause provides a mechanism to use a private variable to broadcast a value from one member of a team to the other members.
DEFAULT	PARALLEL PARALLEL DO PARALLEL SECTIONS PARALLEL WORKSHARE	parallel parallel for	Specifies the behavior of unscoped variables in a parallel region, such as the data-sharing attributes of variables.
FINAL	TASK	task	Specifies that all subtasks of this task will be run immediately.
FIRSTPRIVATE(list)	DO PARALLEL PARALLEL DO PARALLEL SECTIONS PARALLEL WORKSHARE SECTIONS SINGLE	for parallel parallel for sections single	Specifies that each thread should have its own instance of a variable, and that each variable in the list should be initialized with the value of the original variable, because it exists before the parallel construct.
IF()	PARALLEL ... END PARALLEL PARALLEL DO ... END PARALLEL DO PARALLEL SECTIONS ... END PARALLEL SECTIONS PARALLEL WORKSHARE	parallel parallel for parallel sections	Specifies whether a loop should be executed in parallel or in serial.
LASTPRIVATE(list)	DO PARALLEL DO ... END PARALLEL DO PARALLEL SECTIONS ... END PARALLEL SECTIONS	parallel parallel for parallel sections sections	Specifies that the enclosing context's version of the variable is set equal to the <i>private</i> version of whichever thread executes the final iteration of a loop construct or last section of an OpenMP <i>section</i> .

This clause...	Applies to this directive	Applies to this pragma	Has this functionality
	SECTIONS		
MERGEABLE	TASK	task	Specifies that this task will run with the same data environment, including OpenMP internal control variables, as when it is encountered.
NOWAIT	DO ... END DO SECTIONS SINGLE WORKSHARE ... END WORKSHARE	for sections single	Eliminates the barrier implicit at the end of a parallel region.
NUM_THREADS	PARALLEL PARALLEL DO ... END PARALLEL DO PARALLEL SECTIONS ... END PARALLEL SECTIONS PARALLEL WORKSHARE	parallel parallel for parallel sections	Sets the number of threads in a thread team.
ORDERED	DO...END DO PARALLEL DO... END PARALLEL DO	parallel for	Specifies that this block within the parallel DO or FOR region needs to be execute serially in the same order indicated by the enclosing loop.
PRIVATE	DO PARALLEL PARALLEL DO ... END PARALLEL DO PARALLEL SECTIONS ... END PARALLEL SECTIONS PARALLEL WORKSHARE SECTIONS SINGLE	for parallel parallel for parallel sections sections single	Specifies that each thread should have its own instance of a variable.
READ	ATOMIC	atomic	Specifies that the atomic action is reading a value.

This clause...	Applies to this directive	Applies to this pragma	Has this functionality
REDUCTION ({operator   intrinsic } : list)	DO PARALLEL PARALLEL DO ... END PARALLEL DO PARALLEL SECTIONS ... END PARALLEL SECTIONS PARALLEL WORKSHARE SECTIONS	for parallel parallel for parallel sections sections	Specifies that one or more variables in <code>list</code> that are private to each thread are the subject of a reduction operation at the end of the parallel region.
SCHEDULE (type[ , chunk])	DO ... END DO PARALLEL DO... END PARALLEL DO	for parallel for	Applies to the looping directive, allowing the user to specify the chunking method for parallelization. Work is assigned to threads in different manners depending on the scheduling type or chunk size used.
SHARED	PARALLEL PARALLEL DO ... END PARALLEL DO PARALLEL SECTIONS ... END PARALLEL SECTIONS PARALLEL WORKSHARE	parallel parallel for parallel sections	Specifies that one or more variables should be shared among all threads. All threads within a team access the same storage area for shared variables.
UNTIED	TASK TASKWAIT	task taskwait	Specifies that any thread in the team can resume the task region after a suspension.
UPDATE	ATOMIC	atomic	Specifies that the atomic action is updating a value.
WRITE	ATOMIC	atomic	Specifies that the atomic action is writing a value.

## 5.8. Runtime Library Routines

User-callable functions are available to the programmer to query and alter the parallel execution environment.

Any C/C++ program unit that invokes these functions should include the statement `#include <omp.h>`. The `omp.h` header file contains definitions for each of the C/

C++ library routines and the required type definitions. For example, to use the `omp_get_num_threads` function, use this syntax:

```
#include <omp.h>
int omp_get_num_threads(void);
```



Unlimited OpenMP thread counts are available in all PGI configurations. The number of threads is unlicensed in the OpenMP runtime libraries - up to the hard limit of 256 threads. The OpenPOWER compiler relies on the LLVM OpenMP runtime, which has a maximum of  $2^{31}$  threads.

The following table summarizes the runtime library calls.



The Fortran call is shown first followed by the equivalent C/C++ call.

Table 14 Runtime Library Routines Summary

Runtime Library Routines with Examples	
<b>omp_get_num_threads</b>	
Returns the number of threads in the team executing the parallel region from which it is called. When called from a serial region, this function returns 1. A nested parallel region is the same as a single parallel region. By default, the value returned by this function is equal to the value of the environment variable <code>OMP_NUM_THREADS</code> or to the value set by the last previous call to <code>omp_set_num_threads()</code> .	
Fortran	<code>integer function omp_get_num_threads()</code>
C/C++	<code>int omp_get_num_threads(void);</code>
<b>omp_set_num_threads</b>	
Sets the number of threads to use for the next parallel region.	
This subroutine or function can only be called from a serial region of code. If it is called from within a parallel region, or from within a subroutine or function that is called from within a parallel region, the results are undefined. Further, this subroutine or function has precedence over the <code>OMP_NUM_THREADS</code> environment variable.	
Fortran	<code>subroutine omp_set_num_threads(scalar_integer_exp)</code>
C/C++	<code>void omp_set_num_threads(int num_threads);</code>
<b>omp_get_thread_num</b>	
Returns the thread number within the team. The thread number lies between 0 and <code>omp_get_num_threads() - 1</code> . When called from a serial region, this function returns 0. A nested parallel region is the same as a single parallel region.	
Fortran	<code>integer function omp_get_thread_num()</code>
C/C++	<code>int omp_get_thread_num(void);</code>
<b>omp_get_ancestor_thread_num</b>	
Returns, for a given nested level of the current thread, the thread number of the ancestor.	
Fortran	<code>integer function omp_get_ancestor_thread_num(level)</code>

Runtime Library Routines with Examples	
	<code>integer level</code>
C/C++	<code>int omp_get_ancestor_thread_num(int level);</code>
<b>omp_get_active_level</b>	
Returns the number of enclosing active parallel regions enclosing the task that contains the call.	
Fortran	<code>integer function omp_get_active_level()</code>
C/C++	<code>int omp_get_active_level(void);</code>
<b>omp_get_level</b>	
Returns the number of parallel regions enclosing the task that contains the call.	
Fortran	<code>integer function omp_get_level()</code>
C/C++	<code>int omp_get_level(void);</code>
<b>omp_get_max_threads</b>	
Returns the maximum value that can be returned by calls to <code>omp_get_num_threads()</code> . If <code>omp_set_num_threads()</code> is used to change the number of processors, subsequent calls to <code>omp_get_max_threads()</code> return the new value. Further, this function returns the maximum value whether executing from a parallel or serial region of code.	
Fortran	<code>integer function omp_get_max_threads()</code>
C/C++	<code>int omp_get_max_threads(void);</code>
<b>omp_get_num_procs</b>	
Returns the number of processors that are available to the program	
Fortran	<code>integer function omp_get_num_procs()</code>
C/C++	<code>int omp_get_num_procs(void);</code>
<b>omp_get_stack_size</b>	
Returns the value of the OpenMP internal control variable that specifies the size that is used to create a stack for a newly created thread. This value may <i>not</i> be the size of the stack of the current thread.	
Fortran	<pre>!omp_get_stack_size interface function omp_get_stack_size () use omp_lib kinds integer ( kind=OMP_STACK_SIZE_KIND ) :: omp_get_stack_size end function omp_get_stack_size end interface</pre>
C/C++	<code>size_t omp_get_stack_size(void);</code>
<b>omp_set_stack_size</b>	
Changes the value of the OpenMP internal control variable that specifies the size to be used to create a stack for a newly created thread. The integer argument specifies the stack size in kilobytes. The size of the stack of the current thread cannot be changed. In the PGI implementation, all OpenMP or auto-parallelization threads are created	

Runtime Library Routines with Examples	
just prior to the first parallel region; therefore, only calls to <code>omp_set_stack_size()</code> that occur prior to the first region have an effect.	
Fortran	<code>subroutine omp_set_stack_size(integer(KIND=OMP_STACK_SIZE_KIND))</code>
C/C++	<code>void omp_set_stack_size(size_t stack_size);</code>
<b>omp_get_team_size</b>	
Returns, for a given nested level of the current thread, the size of the thread team to which the ancestor belongs.	
Fortran	<code>integer function omp_get_team_size (level) integer level</code>
C/C++	<code>int omp_get_team_size(int level);</code>
<b>omp_in_final</b>	
Returns whether or not the call is within a final task. Returns <code>.TRUE.</code> for directives and non-zero for pragmas if called from within a final task region.	
Fortran	<code>integer function omp_in_final()</code>
C/C++	<code>int omp_in_final(void);</code>
<b>omp_in_parallel</b>	
Returns whether or not the call is within a parallel region. Returns <code>.TRUE.</code> for directives and non-zero for pragmas if called from within a parallel region and <code>.FALSE.</code> for directives and zero for pragmas if called outside of a parallel region. When called from within a parallel region that is serialized, for example in the presence of an IF clause evaluating <code>.FALSE.</code> for directives and zero for pragmas, the function returns <code>.FALSE.</code> for directives and zero for pragmas.	
Fortran	<code>logical function omp_in_parallel()</code>
C/C++	<code>int omp_in_parallel(void);</code>
<b>omp_set_dynamic</b>	
Allows automatic dynamic adjustment of the number of threads used for execution of parallel regions.	
Fortran	<code>subroutine omp_set_dynamic(scalar_logical_exp)</code>
C/C++	<code>void omp_set_dynamic(int dynamic_threads);</code>
<b>omp_get_dynamic</b>	
Allows the user to query whether automatic dynamic adjustment of the number of threads used for execution of parallel regions is enabled.	
Fortran	<code>logical function omp_get_dynamic()</code>
C/C++	<code>void omp_get_dynamic(void);</code>
<b>omp_set_nested</b>	
Allows enabling/disabling of nested parallel regions.	
Fortran	<code>subroutine omp_set_nested(nested)</code>

Runtime Library Routines with Examples	
	logical nested
C/C++	<code>void omp_set_nested(int nested);</code>
<b>omp_get_nested</b>	
Allows the user to query whether dynamic adjustment of the number of threads available for execution of parallel regions is enabled.	
Fortran	logical function <code>omp_get_nested()</code>
C/C++	<code>int omp_get_nested(void);</code>
<b>omp_set_schedule</b>	
Set the value of the <code>run_sched_var</code> .	
Fortran	<pre>subroutine omp_set_schedule(kind, modifier)   include 'omp_lib_kinds.h'   integer (kind=omp_sched_kind) kind   integer modifier</pre>
C/C++	<code>void omp_set_schedule(omp_sched_t kind, int chunk_size);</code>
<b>omp_get_schedule</b>	
Retrieve the value of the <code>run_sched_var</code> .	
Fortran	<pre>subroutine omp_get_schedule(kind, modifier)   include 'omp_lib_kinds.h'   integer (kind=omp_sched_kind) kind   integer modifier</pre>
C/C++	<code>void omp_get_schedule(omp_sched_t *kind, int *chunk_size);</code>
<b>omp_get_wtime</b>	
Returns the elapsed wall clock time, in seconds, as a DOUBLE PRECISION value for directives and as a floating-point double value for pragmas.	
Times returned are per-thread times, and are not necessarily globally consistent across all threads.	
Fortran	double precision function <code>omp_get_wtime()</code>
C/C++	<code>double omp_get_wtime(void);</code>
<b>omp_get_wtick</b>	
Returns the resolution of <code>omp_get_wtime()</code> , in seconds, as a DOUBLE PRECISION value for Fortran directives and as a floating-point double value for C/C++ pragmas.	
Fortran	double precision function <code>omp_get_wtick()</code>
C/C++	<code>double omp_get_wtick();</code>
<b>omp_init_lock</b>	
Initializes a lock associated with the variable <code>lock</code> for use in subsequent calls to lock routines.	
The initial state of the lock is unlocked. If the variable is already associated with a lock, it is illegal to make a call to this routine.	
Fortran	<pre>subroutine omp_init_lock(lock)   include 'omp_lib_kinds.h'</pre>

Runtime Library Routines with Examples	
	<code>integer(kind=omp_lock_kind) lock</code>
C/C++	<code>void omp_init_lock(omp_lock_t *lock); void omp_init_nest_lock(omp_nest_lock_t *lock);</code>
<b>omp_destroy_lock</b>	
Disassociates a lock associated with the variable.	
Fortran	<code>subroutine omp_destroy_lock(lock) include 'omp_lib_kinds.h' integer(kind=omp_lock_kind) lock</code>
C/C++	<code>void omp_destroy_lock(omp_lock_t *lock); void omp_destroy_nest_lock(omp_nest_lock_t *lock);</code>
<b>omp_set_lock</b>	
Causes the calling thread to wait until the specified lock is available. The thread gains ownership of the lock when it is available. If the variable is not already associated with a lock, it is illegal to make a call to this routine.	
Fortran	<code>subroutine omp_set_lock(lock) include 'omp_lib_kinds.h' integer(kind=omp_lock_kind) lock</code>
C/C++	<code>void omp_set_lock(omp_lock_t *lock); void omp_set_nest_lock(omp_nest_lock_t *lock);</code>
<b>omp_unset_lock</b>	
Causes the calling thread to release ownership of the lock associated with <i>integer_var</i> . If the variable is not already associated with a lock, it is illegal to make a call to this routine.	
Fortran	<code>subroutine omp_unset_lock(lock) include 'omp_lib_kinds.h' integer(kind=omp_lock_kind) lock</code>
C/C++	<code>void omp_unset_lock(omp_lock_t *lock); void omp_unset_nest_lock(omp_nest_lock_t *lock);</code>
<b>omp_test_lock</b>	
Causes the calling thread to try to gain ownership of the lock associated with the variable. The function returns <code>.TRUE.</code> for directives and non-zero for pragmas if the thread gains ownership of the lock; otherwise, it returns <code>.FALSE.</code> for directives and zero for pragmas. If the variable is not already associated with a lock, it is illegal to make a call to this routine.	
Fortran	<code>logical function omp_test_lock(lock) include 'omp_lib_kinds.h' integer(kind=omp_lock_kind) lock</code>
C/C++	<code>int omp_test_lock(omp_lock_t *lock); int omp_test_nest_lock(omp_nest_lock_t *lock);</code>

## 5.9. Environment Variables

You can use OpenMP environment variables to control the behavior of OpenMP programs. These environment variables allow you to set and pass information that can alter the behavior of directives and pragmas. The OpenPOWER compiler relies on the llvm OpenMP runtime, which has different default values.

The following summary table is a quick reference for the OpenMP environment variables that PGI uses.

Table 15 OpenMP-related Environment Variable Summary Table

Environment Variable	Default	Description
OMP_DYNAMIC	FALSE	Typically enables (TRUE) or disables (FALSE) the dynamic adjustment of the number of threads.
OMP_MAX_ACTIVE_LEVELS	2 <sup>31</sup>	Specifies the maximum number of nested parallel regions.
OMP_NESTED	FALSE	Enables (TRUE) or disables (FALSE) nested parallelism.
OMP_NUM_THREADS	# of logical CPUs	Specifies the number of threads to use during execution of parallel regions at the corresponding nested level. For example, OMP_NUM_THREADS=4,2 uses 4 threads at the first nested parallel level, and 2 at the next nested parallel level.
OMP_SCHEDULE	STATIC with chunk size of 0	Specifies the type of iteration scheduling and optionally the chunk size to use for <i>omp for</i> and <i>omp parallel for</i> loops that include the runtime schedule clause. The supported schedule types, which can be specified in upper- or lower-case are static, dynamic, guided, and auto.
OMP_PROC_BIND	FALSE	Specifies whether executing threads should be bound to a core during execution. Allowable values are "true" and "false".
OMP_STACKSIZE		Overrides the default stack size for a newly created thread.
OMP_THREAD_LIMIT	2 <sup>31</sup>	Specifies the absolute maximum number of threads that can be used in a program.
OMP_WAIT_POLICY	ACTIVE	Sets the behavior of idle threads, defining whether they spin or sleep when idle. The values are ACTIVE and PASSIVE.

# Chapter 6.

## USING MPI

Message Passing Interface (MPI) is an industry-standard application programming interface designed for rapid data exchange between processors in a cluster application. MPI is computer software used in computer clusters that allows the processes of a parallel application to communicate with one another.

PGI provides MPI support with PGI compilers and tools on Linux using Open MPI. Of course, you may always build using an arbitrary version of MPI; to do this, use the `-I`, `-L`, and `-l` option.

PGI products for Linux include Open MPI, PGI products for macOS includes MPICH, and PGI products for Windows includes MS-MPI. This section describes how to use the MPI capabilities of PGI compilers and how to configure PGI compilers so these capabilities can be used with custom MPI installations.

### 6.1. MPI Overview

This section contains general information applicable to various MPI distributions. For distribution-specific information, refer to the sections later in this section.

MPI is a set of function calls and libraries that are used to send messages between multiple processes. These processes can be located on the same system or on a collection of distributed servers. Unlike OpenMP, the distributed nature of MPI allows it to work in almost any parallel environment.

### 6.2. Using Open MPI on Linux

PGI products for Linux ship with a PGI-built version of Open MPI that includes everything required to compile, execute and debug MPI programs using Open MPI.

To build an application using Open MPI, use the Open MPI compiler wrappers: `mpicc`, `mpic++`, `mpif77`, and `mpif90`. These wrappers automatically set up the compiler commands with the correct include file search paths, library directories, and link libraries.

To build an application using Open MPI for debugging, add `-g` to the compiler wrapper command line arguments.

## 6.3. Using MPI Compiler Wrappers

When you use MPI compiler wrappers to build with the `-fpic` or `-mmodel=medium` options, then you must specify `-pgf90libs` to link with the correct libraries. Here are a few examples:

For a static link to the MPI libraries, use this command:

```
% mpifort hello.f
```

For a dynamic link to the MPI libraries, use this command:

```
% mpifort hello.f -pgf90libs
```

To compile with `-fpic`, which, by default, invokes dynamic linking, use this command:

```
% mpifort -fpic -pgf90libs hello.f
```

To compile with `-mmodel=medium`, use this command:

```
% mpifort -mmodel=medium -pgf90libs hello.f
```

## 6.4. Limitations

The Open Source Cluster utilities, in particular the MPICH and ScaLAPACK libraries, are provided with support necessary to build and define their proper use. However, use of these libraries on linuxpower systems is subject to the following limitations:

- ▶ MPI libraries are limited to Messages of length < 2GB, and integer arguments are *INTEGER\*4* in FORTRAN, and *int* in C.
- ▶ Integer arguments for ScaLAPACK libraries are *INTEGER\*4* in FORTRAN, and *int* in C.
- ▶ Arrays passed must be < 2GB in size.

## 6.5. Testing and Benchmarking

The `Examples` directory contains various benchmarks and tests. Copy this directory into a local working directory by issuing the following command:

```
% cp -r $PGI/linuxpower/19.1/EXAMPLES/MPI .
```

### NAS Parallel Benchmarks

The `NPB2.3` subdirectory contains version 2.3 of the NAS Parallel Benchmarks in MPI. Issue the following commands to run the BT benchmark on four nodes of your cluster:

```
% cd MPI/NPB2.3/BT
% make BT NPROCS=4 CLASS=W
% cd ../bin
% mpirun -np 4 bt.W.4
```

There are several other NAS parallel benchmarks available in this directory. Similar commands are used to build and run each of them. If you want to run a larger problem, try building the Class A version of BT by substituting "A" for "W" in the previous commands.

### ScaLAPACK

The ScaLAPACK test times execution of the 3D PBLAS (parallel BLAS) on your cluster. To run this test, execute the following commands:

```
% cd scalapack
% make
% mpirun -np 4 pdbla3tim
```

# Chapter 7.

## USING AN ACCELERATOR

An accelerator is a special-purpose co-processor attached to a CPU and to which the CPU can offload data and executable kernels to perform compute-intensive calculations. This section describes a collection of compiler directives used to specify regions of code in Fortran and C programs that can be offloaded from a *host* CPU to an attached *accelerator*.

### 7.1. Overview

The programming model and directives described in this section allow programmers to create high-level *host+accelerator* programs without the need to explicitly initialize the accelerator, manage data or program transfers between the host and accelerator, or initiate accelerator startup and shutdown. Rather, all of these details are implicit in the programming model and are managed by the PGI Fortran, C, and C++ accelerator compilers.

The method described provides a model for accelerator programming that is portable across operating systems and various host CPUs and accelerators. The directives allow a programmer to migrate applications incrementally to accelerator targets using standards-compliant Fortran, C, or C++.

This programming model allows the programmer to augment information available to the compilers, including specification of data local to an accelerator region, guidance on mapping of loops onto an accelerator, and similar performance-related details.

#### 7.1.1. User-directed Accelerator Programming

In user-directed accelerator programming the user specifies the regions of a host program to be targeted for offloading to an accelerator device. The bulk of a user's program, as well as regions containing constructs that are not supported on the targeted accelerator, are executed on the host. This section concentrates on specification of loops and regions of code to be offloaded to an accelerator.

## 7.1.2. Features Not Covered or Implemented

This section does not describe features or limitations of the host programming environment as a whole. Further, it does not cover automatic detection and offloading of regions of code to an accelerator by a compiler or other tool. While future versions of the PGI compilers may allow for automatic offloading, this feature is not currently supported.

## 7.2. Terminology

Clear and consistent terminology is important in describing any programming model. This section provides definitions of the terms required for you to effectively use this section and the associated programming model.

### **Accelerator**

a special-purpose co-processor attached to a CPU and to which the CPU can offload data and executable kernels to perform compute-intensive calculations.

### **Compute intensity**

for a given loop, region, or program unit, the ratio of the number of arithmetic operations performed on computed data divided by the number of memory transfers required to move that data between two levels of a memory hierarchy.

### **Compute region**

a structured block defined by an OpenACC compute construct. A *compute construct* is a structured block containing loops which are compiled for the accelerator. A compute region may require device memory to be allocated and data to be copied from host to device upon region entry, and data to be copied from device to host memory and device memory deallocated upon exit. The dynamic range of a compute construct, including any code in procedures called from within the construct, is the compute region. In this release, compute regions may not contain other compute regions or data regions.

### **Construct**

a structured block identified by the programmer or implicitly defined by the language. Certain actions may occur when program execution reaches the start and end of a construct, such as device memory allocation or data movement between the host and device memory. Loops in a compute construct are targeted for execution on the accelerator. The dynamic range of a construct including any code in procedures called from within the construct, is called a *region*.

### **CUDA**

stands for Compute Unified Device Architecture; NVIDIA's CUDA environment is a C-like programming environment used to explicitly control and program an NVIDIA GPU.

### **Data region**

a region defined by an OpenACC data construct, or an implicit data region for a function or subroutine containing OpenACC directives. Data regions typically require device memory to be allocated and data to be copied from host to device memory upon entry, and data to be copied from device to host memory and device

memory deallocated upon exit. Data regions may contain other data regions and compute regions.

**Device**

a general reference to any type of accelerator.

**Device memory**

memory attached to an accelerator which is physically separate from the host memory.

**Directive**

in C, a #pragma, or in Fortran, a specially formatted comment statement that is interpreted by a compiler to augment information about or specify the behavior of the program.

**DMA**

Direct Memory Access, a method to move data between physically separate memories; this is typically performed by a DMA engine, separate from the host CPU, that can access the host physical memory as well as an IO device or GPU physical memory.

**GPU**

a Graphics Processing Unit; one type of accelerator device.

**GPGPU**

General Purpose computation on Graphics Processing Units.

**Host**

the main CPU that in this context has an attached accelerator device. The host CPU controls the program regions and data loaded into and executed on the device.

**Loop trip count**

the number of times a particular loop executes.

**OpenACC**

a parallel programming standard describing a set of compiler directives which can be applied to standard C, C++, and Fortran to specify regions of code for offloading from a host CPU to an attached accelerator.

**Private data**

with respect to an iterative loop, data which is used only during a particular loop iteration. With respect to a more general region of code, data which is used within the region but is not initialized prior to the region and is re-initialized prior to any use after the region.

**Region**

the dynamic range of a construct, including any procedures invoked from within the construct.

**Structured block**

in C, an executable statement, possibly compound, with a single entry at the top and a single exit at the bottom. In Fortran, a block of executable statements with a single entry at the top and a single exit at the bottom.

**Vector operation**

a single operation or sequence of operations applied uniformly to each element of an array.

**Visible device copy**

a copy of a variable, array, or subarray allocated in device memory, that is visible to the program unit being compiled.

## 7.3. Execution Model

The execution model targeted by the PGI compilers is host-directed execution with an attached accelerator device, such as a GPU. The bulk of a user application executes on the host. Compute intensive regions are offloaded to the accelerator device under control of the host. The accelerator device executes kernels, which may be as simple as a tightly-nested loop, or as complex as a subroutine, depending on the accelerator hardware.

### 7.3.1. Host Functions

Even in accelerator-targeted regions, the host must orchestrate the execution; it

- ▶ allocates memory on the accelerator device
- ▶ initiates data transfer
- ▶ sends the kernel code to the accelerator
- ▶ passes kernel arguments
- ▶ queues the kernel
- ▶ waits for completion
- ▶ transfers results back to the host
- ▶ deallocates memory



In most cases, the host can queue a sequence of kernels to be executed on the device, one after the other.

### 7.3.2. Levels of Parallelism

Most current GPUs support two levels of parallelism:

- ▶ an outer *doall* (fully parallel) loop level
- ▶ an inner *synchronous* (SIMD or vector) loop level

Each level can be multidimensional with 2 or 3 dimensions, but the domain must be strictly rectangular. The *synchronous* level may not be fully implemented with SIMD or vector operations, so explicit synchronization is supported and required across this level. No synchronization is supported between parallel threads across the *doall* level.

The execution model on the device side exposes these two levels of parallelism and the programmer is required to understand the difference between, for example, a fully parallel loop and a loop that is vectorizable but requires synchronization across iterations. All fully parallel loops can be scheduled for either *doall* or *synchronous* parallel execution, but by definition SIMD vector loops that require synchronization can only be scheduled for synchronous parallel execution.

## 7.4. Memory Model

The most significant difference between a *host-only* program and a *host+accelerator* program is that the memory on the accelerator can be completely separate from host memory, which is the case on most current GPUs. For example:

- ▶ The host cannot read or write accelerator memory by reference because it is not mapped into the virtual memory space of the host.
- ▶ All data movement between host memory and accelerator memory must be performed by the host through runtime library calls that explicitly move data between the separate memories.
- ▶ It is not valid to assume the accelerator can read or write host memory, though this may be supported by accelerators in the future.

### 7.4.1. Separate Host and Accelerator Memory Considerations

The programmer must be aware of the potentially separate memories for many reasons, including but not limited to:

- ▶ Memory bandwidth between host memory and accelerator memory determines the compute intensity required to effectively accelerate a given region of code.
- ▶ Limited size of accelerator memory may prohibit offloading of regions of code that operate on very large amounts of data.

### 7.4.2. Accelerator Memory

On the accelerator side, current GPUs implement a weak memory model. In particular, they do not support memory coherence between threads unless those threads are parallel only at the synchronous level and the memory operations are separated by an explicit barrier. Otherwise, if one thread updates a memory location and another reads the same location, or two threads store a value to the same location, the hardware does not guarantee the results. While the results of running such a program might be inconsistent, it is not accurate to say that the results are incorrect. By definition, such programs are defined as being in error. While a compiler can detect some potential errors of this nature, it is nonetheless possible to write an accelerator region that produces inconsistent numerical results.

### 7.4.3. Cache Management

Some current GPUs have a software-managed cache, some have hardware-managed caches, and most have hardware caches that can be used only in certain situations and are limited to read-only data. In low-level programming models such as CUDA, it is up to the programmer to manage these caches. However, in the OpenACC programming model, the compiler manages these caches using hints from the programmer in the form of directives.

## 7.4.4. CUDA Unified Memory

PGI compilers have supported the use of CUDA Unified Memory since PGI 17.7. This feature, described in detail in the [OpenACC and CUDA Unified Memory](https://www.pgroup.com/blogs/posts/openacc-unified-memory.htm), [PGInsider](https://www.pgroup.com/blogs/posts/openacc-unified-memory.htm) blog, is available with the Linux/x86-64 and Linux/OpenPOWER compilers. It is supported on Linux/x86-64 using both the default PGI code generator and the LLVM-based code generator. To enable this feature, add the option `-ta=tesla:managed` to the compiler and linker command lines.

In the presence of `-ta=tesla:managed`, all C/C++/Fortran explicit allocation statements in a program unit are replaced by equivalent "managed" data allocation calls that place the data in CUDA Unified Memory. Managed data share a single address for CPU/GPU and data movement between CPU and GPU memories is implicitly handled by the CUDA driver. Therefore, OpenACC data clauses and directives are not needed for "managed" data. They are essentially ignored, and in fact can be omitted.

When a program allocates managed memory, it allocates host pinned memory as well as device memory thus making allocate and free operations somewhat more expensive and data transfers somewhat faster. A memory pool allocator is used to mitigate the overhead of the allocate and free operations. The pool allocator is enabled by default for `-ta=tesla:managed` or `-ta=tesla:pinned`.

Data movement of managed data is controlled by the NVIDIA CUDA GPU driver; whenever data is accessed on the CPU or the GPU, it could trigger a data transfer if the last time it was accessed was not on the same device. In some cases, page thrashing may occur and impact performance. An introduction to CUDA Unified Memory is available on [Parallel Forall](#).

This feature has the following limitations:

- ▶ Use of managed memory applies only to dynamically-allocated data. Static data (C static and extern variables, Fortran module, common block and save variables) and function local data is still handled by the OpenACC runtime. Dynamically allocated Fortran local variables and Fortran allocatable arrays are implicitly managed but Fortran array pointers are not.
- ▶ Given an allocatable aggregate with a member that points to local, global or static data, compiling with `-ta=tesla:managed` and attempting to access memory through that pointer from the compute kernel will cause a failure at runtime.
- ▶ C++ virtual functions are not supported.
- ▶ The `-ta=tesla:managed` compiler option must be used to compile the files in which variables are allocated, even if there is no OpenACC code in the file.

This feature has the following additional limitations when used with NVIDIA Kepler GPUs:

- ▶ Data motion on Kepler GPUs is achieved through fast pinned asynchronous data transfers; from the program's perspective, however, the transfers are synchronous.
- ▶ The PGI runtime enforces synchronous execution of kernels when `-ta=tesla:managed` is used on a system with a Kepler GPU. This situation may result in slower performance because of the extra synchronizations and decreased overlap between CPU and GPU.
- ▶ The total amount of managed memory is limited to the amount of available device memory on Kepler GPUs.

### CUDA Unified Memory Pool Allocator

Dynamic memory allocations are made using `cudaMallocManaged()`, a routine which has higher overhead than allocating non-unified memory using `cudaMalloc()`. The more calls to `cudaMallocManaged()`, the more significant the impact on performance.

To mitigate the overhead of `cudaMallocManaged()` calls, both `-ta=tesla:managed` and `-ta=tesla:pinned` use a CUDA Unified Memory pool allocator to minimize the number of calls to `cudaMallocManaged()`. The pool allocator is enabled by default. It can be disabled, or its behavior modified, using these environment variables:

**Table 16 Pool Allocator Environment Variables**

Environment Variable	Use
<code>PGI_ACC_POOL_ALLOC</code>	Disable the pool allocator. The pool allocator is enabled by default; to disable it, set <code>PGI_ACC_POOL_ALLOC</code> to 0.
<code>PGI_ACC_POOL_SIZE</code>	Set the size of the pool. The default size is 1GB but other sizes (i.e., 2GB, 100MB, 500KB, etc.) can be used. The actual pool size is set such that the size is the nearest, smaller number in the Fibonacci series compared to the provided or default size. If necessary, the pool allocator will add more pools but only up to the <code>PGI_ACC_POOL_THRESHOLD</code> value.
<code>PGI_ACC_POOL_ALLOC_MAXSIZE</code>	Set the maximum size for allocations. The default maximum size for allocations is 500MB but another size (i.e., 100KB, 10MB, 250MB, etc.) can be used as long as it is greater than or equal to 16B.
<code>PGI_ACC_POOL_ALLOC_MINSIZE</code>	Set the minimum size for allocation blocks. The default size is 128B but other sizes can be used. The size must be greater than or equal to 16B.
<code>PGI_ACC_POOL_THRESHOLD</code>	Set the percentage of total device memory that the pool allocator can occupy. Values from 0 to 100 are accepted. The default value is 50, corresponding to 50% of device memory.

## 7.5. OpenACC Programming Model

With the emergence of GPU and many-core architectures in high performance computing, programmers want the ability to program using a familiar, high level programming model that provides both high performance and portability to a wide range of computing architectures. OpenACC emerged in 2011 as a programming model that uses high-level compiler directives to expose parallelism in the code and parallelizing compilers to build the code for a variety of parallel accelerators.

This chapter will not attempt to describe OpenACC itself. For that, please refer to the OpenACC specification on the OpenACC [www.openacc.org](http://www.openacc.org) website. Here, we will discuss differences between the OpenACC specification and its implementation by the PGI compilers.

Other resources to help you with your parallel programming including video tutorials, course materials, code samples, a best practices guide and more are available on the OpenACC website.

### 7.5.1. Enable Accelerator Directives

PGI compilers enable accelerator directives with the `-acc` and `-ta` command line options. For more information on this option as it relates to the Accelerator, refer to [Compiling an Accelerator Program](#).

#### **`_OPENACC` macro**

The `_OPENACC` macro name is defined to have a value `yyyymm` where `yyyy` is the year and `mm` is the month designation of the version of the OpenACC directives supported by the implementation. For example, the version for November, 2017 is 201711. All OpenACC compilers define this macro when OpenACC directives are enabled.

### 7.5.2. Support

The PGI compilers implement OpenACC 2.6 as defined in *The OpenACC Application Programming Interface*, Version 2.6, November 2017, <http://www.openacc.org>, with the exception that the following features are not yet supported:

- ▶ nested parallelism
- ▶ declare link
- ▶ enforcement of the **cache** clause restriction that all references to listed variables must lie within the region being cached

### 7.5.3. Extensions

The PGI Fortran compiler supports an extension to the `collapse` clause on the `loop` construct. The OpenACC specification defines `collapse`:

```
collapse (n)
```

For Fortran, PGI supports the use of the identifier `force` within `collapse`:

```
collapse(force:n)
```

Using `collapse(force:n)` instructs the compiler to enforce collapsing parallel loops that are not perfectly nested.

## 7.6. Supported Processors and GPUs

This PGI release supports OpenPOWER host processors.

Use the `-acc` flag to enable OpenACC directives and the `-ta=tesla` flag to target NVIDIA GPUs. You can then use the generated code on any supported system with CUDA installed that has a CUDA-enabled GeForce, Quadro, or Tesla card.

For more information on these flags as they relate to accelerator technology, refer to [Compiling an Accelerator Program](#).

For a complete list of supported CUDA GPUs, refer to the NVIDIA website at: [http://www.nvidia.com/object/cuda\\_learn\\_products.html](http://www.nvidia.com/object/cuda_learn_products.html)

## 7.7. CUDA Toolkit Versions

The PGI compilers use NVIDIA's CUDA Toolkit when building programs for execution on an NVIDIA GPU. Every PGI installation package puts the required CUDA Toolkit components into a PGI installation directory called `2019/cuda`.

An NVIDIA CUDA driver must be installed on a system with a GPU before you can run a program compiled for the GPU on that system. PGI products do not contain CUDA Drivers. You must download and install the appropriate [CUDA Driver from NVIDIA](#). The CUDA Driver version must be at least as new as the version of the CUDA Toolkit with which you compiled your code.

The PGI tool `pgaccelinfo` prints the driver version as its first line of output. You can use it to find out which version of the CUDA Driver is installed on your system.

PGI 19.1 includes the following versions of the CUDA Toolkit:

- ▶ CUDA 9.2
- ▶ CUDA 10.0

You can let the compiler pick which version of the CUDA Toolkit to use or you can instruct it to use a particular version. The rest of this section describes all of your options.

If you do not specify a version of the CUDA Toolkit, the compiler uses the version of the CUDA Driver installed on the system on which you are compiling to determine which CUDA Toolkit to use. This auto-detect feature was introduced in the PGI 18.7 release; auto-detect is especially convenient when you are compiling and running your application on the same system. In the absence of any other information, the compiler will look for a CUDA Toolkit version in the PGI `2019/cuda` directory that matches the version of the CUDA Driver installed on the system. If a match is not found, the

compiler searches for the newest CUDA Toolkit version that is not newer than the CUDA Driver version. If there is no CUDA Driver installed, the PGI 19.1 compilers fall back to the default of CUDA 9.2.

If the only PGI compiler you have installed is PGI 19.1, then:

- ▶ If your CUDA Driver is 10.0, the compilers use CUDA Toolkit 10.0.
- ▶ If your CUDA Driver is 9.2, the compilers use CUDA Toolkit 9.2.
- ▶ If your CUDA Driver is 9.1, the compilers will issue an error that CUDA Toolkit 9.1 was not found; CUDA Toolkit 9.1 is not bundled with PGI 19.1
- ▶ If you do not have a CUDA driver installed on the compilation system, the compilers use the default CUDA Toolkit version 9.2.
- ▶ If your CUDA Driver is newer than CUDA 10.0, the compilers will still use the CUDA Toolkit 10.0. The compiler selects the newest CUDA Toolkit it finds that is not newer than the CUDA Driver.

You can change the compiler's default selection for CUDA Toolkit version using one of the following methods:

- ▶ Use a compiler option. Add the `cudaX.Y` sub-option to `-Mcuda` or `-ta=tesla` where `X.Y` denotes the CUDA version. For example, to compile a C file with the CUDA 9.2 Toolkit you would use:

```
pgcc -ta=tesla:cuda9.2
```

Using a compiler option changes the CUDA Toolkit version for one invocation of the compiler.

- ▶ Use an rcfile variable. Add a line defining `DEFCUDAVERSION` to the `siterc` file in the installation `bin/` directory or to a file named `.mypgirc` in your home directory. For example, to specify the CUDA 9.2 Toolkit as the default, add the following line to one of these files:

```
set DEFCUDAVERSION=9.2;
```

Using an rcfile variable changes the CUDA Toolkit version for all invocations of the compilers reading the rcfile.

When you specify a CUDA Toolkit version, you can additionally instruct the compiler to use a CUDA Toolkit installation different from the defaults bundled with the current PGI compilers. While most users do not need to use any other CUDA Toolkit installation than those provided with PGI, situations do arise where this capability is needed. Developers working with pre-release CUDA software may occasionally need to test with a CUDA Toolkit version not included in a PGI release. Conversely, some developers might find a need to compile with a CUDA Toolkit older than the oldest CUDA Toolkit installed with a PGI release. For these users, PGI compilers can interoperate with components from a CUDA Toolkit installed outside of the PGI installation directories.

PGI tests extensively using the co-installed versions of the CUDA Toolkits and fully supports their use. Use of CUDA Toolkit components not included with a PGI install is done with your understanding that functionality differences may exist.

To use a CUDA toolkit that is not installed with a PGI release, such as CUDA 8.0 with PGI 19.1, there are three options:

- ▶ Use the rcfile variable `DEFAULT_CUDA_HOME` to override the base default

```
set DEFAULT_CUDA_HOME = /opt/cuda-8.0;
```

- ▶ Set the environment variable `CUDA_HOME`

```
export CUDA_HOME=/opt/cuda-8.0
```

- ▶ Use the compiler compilation line assignment `CUDA_HOME=`

```
pgfortran CUDA_HOME=/opt/cuda-8.0
```

The PGI compilers use the following order of precedence when determining which version of the CUDA Toolkit to use.

1. If you do not tell the compiler which CUDA Toolkit version to use, the compiler picks the CUDA Toolkit from the PGI installation directory `2019/cuda` that matches the version of the CUDA Driver installed on your system. If the PGI installation directory does not contain a direct match, the newest version in that directory which is not newer than the CUDA driver version is used. If there is no CUDA driver installed on your system, the compiler falls back on an internal default; in PGI 18.10, this default is CUDA 9.1.
2. The rcfile variable `DEFAULT_CUDA_HOME` will override the base default.
3. The environment variable `CUDA_HOME` will override all of the above defaults.
4. The environment variable `PGI_CUDA_HOME` overrides all of the above; it is available for advanced users in case they need to override an already-defined `CUDA_HOME`.
5. A user-specified `cudaX.Y` sub-option to `-Mcuda` and `-ta=tesla` will override all of the above defaults and the CUDA Toolkit located in the PGI installation directory `2019/cuda` will be used.
6. The compiler compilation line assignment `CUDA_HOME=` will override all of the above defaults (including the `cudaX.Y` sub-option).

## 7.8. Compute Capability

The compilers can generate code for NVIDIA GPU compute capabilities 3.0 through 7.5. The compilers construct a default list of compute capabilities that matches the compute capabilities supported by the GPUs found on the system used in compilation. If there are no GPUs detected, the compilers select `cc35`, `cc60`, and `cc70`.

You can override the default by specifying one or more compute capabilities using either command-line options or an rcfile.

To change the default with a command-line option, provide a comma-separated list of compute capabilities to `-ta=tesla:` for OpenACC or `-Mcuda=` for CUDA Fortran.

To change the default with an rcfile, set the **DEF COMPUTE CAP** value to a blank-separated list of compute capabilities in the `siterc` file located in your installation's `bin` directory:

```
set DEF COMPUTE CAP=60 70;
```

Alternatively, if you don't have permissions to change the `siterc` file, you can add the **DEF COMPUTE CAP** definition to a separate `.myppgirc` file in your home directory.

The generation of device code can be time consuming, so you may notice an increase in compile time as the number of compute capabilities increases.

## 7.9. Compiling an Accelerator Program

Several compiler options are applicable specifically when working with accelerators. These options include `-ta`, `-acc`, and `-Minfo`.

### 7.9.1. `-ta`

Enable OpenACC and specify the type of accelerator to which to target accelerator regions.

#### **`-ta` suboptions**

There are three primary suboptions:

##### **host**

Compile OpenACC for serial execution on the host CPU; `host` has no suboptions.

##### **multicore**

Compile OpenACC for parallel execution on the host CPU; `multicore` has no suboptions.

##### **tesla**

Compile OpenACC for parallel execution on a Tesla GPU; `tesla` supports suboptions.

Multiple target accelerators can be specified. By default, the compiler generates code for `-ta=tesla,host`.

#### **`-ta=tesla` suboptions**

The `tesla` sub-option to `-ta` can itself be given suboptions. The following secondary suboptions are supported:

##### **ccXY**

Generate code for a device with compute capability X.Y. Multiple compute capabilities can be specified, and one version will be generated for each. By default, the compiler will detect the compute capability for each installed GPU. Use `-help -ta` to see the valid compute capabilities for your installation.

##### **ccall**

Generate code for all compute capabilities supported by this platform and by the selected or default CUDA Toolkit.

##### **cudaX.Y**

Use CUDA X.Y Toolkit compatibility, where installed

##### **7.5, 8.0, 9.0, 9.1**

Support for the X.Y suboption has been removed. Use the `cudaX.Y` suboption instead.

##### **[no]debug**

Enable [disable] debug information generation in device code

**deepcopy**

Enable full deep copy of aggregate data structures in OpenACC; Fortran only

**fastmath**

Use routines from the fast math library

**[no]flushz**

Enable [disable] flush-to-zero mode for floating point computations on the GPU

**[no]fma**

Generate [do not generate] fused multiply-add instructions; default at `-O3`

**keep**

Keep the kernel files (.bin, .ptx, source)

**[no]lineinfo**

Enable [disable] GPU line information generation

**[no]nvvm**

Generate [do not generate] code using the NVVM-based back-end

**loadcache:{L1|L2}**

Choose what hardware level cache to use for global memory loads; options include the default, L1, or L2

**managed**

Use CUDA Managed Memory

**maxregcount:n**

Specify the maximum number of registers to use on the GPU; leaving this blank indicates no limit

**pinned**

Use CUDA Pinned Memory

**[no]rdc**

Generate [do not generate] relocatable device code.

**safecache**

Allow variable-sized array sections in cache directives; compiler assumes they fit into CUDA shared memory

**[no]unroll**

Enable [disable] automatic inner loop unrolling; default at `-O3`

**zeroinit**

Initialize allocated device memory with zero

**autocompare**

Automatically compare CPU/GPU results: implies redundant

**redundant**

Redundant CPU/GPU execution

**Usage**

In the following example, `tesla` is the accelerator target architecture and the accelerator generates code for compute capabilities 6.0 and 7.0.

```
$ pgfortran -ta=tesla:cc60,cc70
```

The compiler automatically invokes the necessary software tools to create the kernel code and embeds the kernels in the object file.

To access accelerator libraries, you must link an accelerator program with the `-ta` flag.

### DWARF Debugging Formats

PGI's debugging capability for Tesla uses the LLVM back-end. Use the compiler's `-g` option to enable the generation of full dwarf information on both the host and device; in the absence of other optimization flags, `-g` sets the optimization level to zero. If a `-O` option raises the optimization level to one or higher, only GPU line information is generated on the device even when `-g` is specified. To enforce full dwarf generation for device code at optimization levels above zero, use the `debug` sub-option to `-ta=tesla`. Conversely, to prevent the generation of dwarf information for device code, use the `nodebug` sub-option to `-ta=tesla`. Both `debug` and `nodebug` can be used independently of `-g`.

## 7.9.2. -acc

Enable OpenACC directives.

### -acc suboptions

The following suboptions may be used:

#### **[no]autopar**

Enable [disable] loop autoparallelization within `acc parallel`. The default is to autoparallelize, that is, to enable loop autoparallelization.

#### **legacy**

Suppress warnings about deprecated PGI accelerator directives.

#### **[no]routineseq**

Compile every routine for the device. The default behavior is to not treat every routine as a `seq` directive.

#### **strict**

Instructs the compiler to issue warnings for non-OpenACC accelerator directives.

#### **sync**

Ignore `async` clauses

#### **verystRICT**

Instructs the compiler to fail with an error for any non-OpenACC accelerator directive.

#### **[no]wait**

Wait for each device kernel to finish. Kernel launching is blocked by default unless the `async` clause is used.

## Usage

The following command-line requests that OpenACC directives be enabled and that an error be issued for any non-OpenACC accelerator directive.

```
$ pgfortran -acc=verystrict -g prog.f
```

## 7.10. Multicore Support

PGI Accelerator OpenACC compilers support the option `-ta=multicore`, to set the target accelerator for OpenACC programs to the host multicore CPU. This will compile OpenACC compute regions for parallel execution across the cores of the host processor or processors. The host multicore will be treated as a shared-memory accelerator, so the data clauses (`copy`, `copyin`, `copyout`, `create`) will be ignored and no data copies will be executed.

By default, `-ta=multicore` will generate code that will use all the available cores of the processor. If the compute region specifies a value in the `num_gangs` clause, the minimum of the `num_gangs` value and the number of available cores will be used. At runtime, the number of cores can be limited by setting the environment variable `ACC_NUM_CORES` to a constant integer value. The number of cores can also be set with the `void acc_set_num_cores(int numcores)` runtime call. If an OpenACC compute construct appears lexically within an OpenMP parallel construct, the OpenACC compute region will generate sequential code. If an OpenACC compute region appears dynamically within an OpenMP region or another OpenACC compute region, the program may generate many more threads than there are cores, and may produce poor performance.

The `ACC_BIND` environment variable is set by default with `-ta=multicore`; `ACC_BIND` has similar behavior to `MP_BIND` for OpenMP.

The `-ta=multicore` option differs from the `-ta=host` option in that `-ta=host` generates sequential code for the OpenACC compute regions.

## 7.11. Running an Accelerator Program

Running a program that has accelerator directives and was compiled and linked with the `-ta` flag is the same as running the program compiled without the `-ta` flag.

- ▶ When running programs on NVIDIA GPUs, the program looks for and dynamically loads the CUDA libraries.
- ▶ On Linux, if you have no server running on your NVIDIA GPU, when your program reaches its first accelerator region, there may be a 0.5 to 1.5 second pause to warm up the GPU from a power-off audience. You can avoid this delay by running the `pgcudainit` program in the background, which keeps the GPU powered on.
- ▶ If you compile a program for a particular accelerator type, then run the program on a system without that accelerator, or on a system where the target libraries are

not in a directory where the runtime library can find them, the program may fail at runtime with an error message.

- ▶ If you set the environment variable `PGI_ACC_NOTIFY` to a nonzero integer value, the runtime library prints a line to standard error every time it launches a kernel on the accelerator.

## 7.12. OpenACC Error Handling

The OpenACC specification provides a mechanism to allow you to intercept errors triggered during execution on a GPU and execute a specific routine in response before the program exits. For example, if an MPI process fails while allocating memory on the GPU, the application may want to call `MPI_Abort` to shut down all the other processes before the program exits. This section explains how to take advantage of this feature.

To intercept errors the application must give a callback routine to the OpenACC runtime. To provide the callback, the application calls `acc_set_error_routine` with a pointer to the callback routine.

The interface is the following, where `err_msg` contains a description of the error:

```
typedef void (*exitroutinetype) (char *err_msg);
extern void acc_set_error_routine(exitroutinetype callback_routine);
```

When the OpenACC runtime detects a runtime error, it will invoke the `callback_routine`.



This feature is not the same as error recovery. If the callback routine returns to the application, the behavior is decidedly undefined.

Let's look at this feature in more depth using an example.

Take the MPI program below and run it with two processes. Process 0 tries to allocate a large array on the GPU, then sends a message to the second process to acknowledge the success of the operation. Process 1 waits for the acknowledgment and terminates upon receiving it.

```
#include <stdio.h>
#include <stdlib.h>
#include "mpi.h"

#define N 2147483648

int main(int argc, char **argv)
{
    int rank, size;

    MPI_Init(&argc, &argv);

    MPI_Comm_rank(MPI_COMM_WORLD, &rank);
    MPI_Comm_size(MPI_COMM_WORLD, &size);

    int ack;
    if(rank == 0) {
        float *a = (float*) malloc(sizeof(float) * N);
    }

    #pragma acc enter data create(a[0:N])
```











































































































