



# **NVIDIA Quantum-2 Firmware Release Notes**

## **v31.2012.1068**

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## Release Notes Update History

Revision	Date	Description
1.0	September 7, 2023	Initial release of this release notes version.

## Overview

Firmware, which is added at the time of manufacturing, is used to run user programs on the device and can be thought of as the software that allows hardware to run. Embedded firmware is used to control the functions of various hardware devices and systems, much like a computer's operating system (OS) controls the function of software applications. Firmware may be written into read-only memory (ROM), erasable programmable read-only memory (EPROM), or flash memory.

## Firmware Download

Please visit <https://www.nvidia.com/en-us/networking/> → Support → Support → Firmware Download

## Document Revision History

A list of the changes made to this document are provided in [Changes and New Features](#) and [Changes and New Features History](#).

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# Firmware Compatible Products

These are the release notes for the NVIDIA Quantum™-2 firmware. This firmware complements the NVIDIA Quantum switch with a set of advanced features, allowing easy and remote management of the switch.

This firmware supports the following protocols:

- InfiniBand—SDR, EDR, HDR, NDR

## Supported Switch Systems

This firmware supports the devices listed in the table below:

Model Number	NVIDIA SKU	Description
QM9790	920-9B210-00FN-0D2 920-9B210-00FN-0D0	NVIDIA Quantum 2 based NDR InfiniBand Switch, 64 NDR ports, 32 OSFP ports, 2 Power Supplies (AC), Standard depth

## Firmware Interoperability

This firmware version has been validated to work against platforms with the following firmware and software versions.

HCA/Switch	Firmware Version
NVIDIA Quantum-2	31.2012.1068
NVIDIA Quantum	27.2012.1010

ConnectX-7	28.37.1014
ConnectX-6	20.37.1014
Minimal MFT version	4.23.0-102

## Supported Cables

### Warning

NVIDIA does not support InfiniBand cables or modules not qualified or approved by NVIDIA.

## Switch and HCAs InfiniBand Cable Connectivity Matrix

NVIDIA Quantum™ based switches and NVIDIA® ConnectX® HCAs support HDR (PAM4, 50Gb/s per lane) and EDR (NRZ, 25Gb/s per lane) technologies. As the ConnectX adapter cards are identified by their maximum supported throughput (e.g., ConnectX-6 VPI 100Gb/s card can support either 2-lanes of 50Gb/s or 4-lanes of 25Gb/s), the exact connectivity will be determined by the cable that is being used.

As a reference:

Speed Mode	Speed Supported	Number of Lanes Used
NDR	400Gb/s InfiniBand	4 lanes of 100Gb/s
NDR200	200Gb/s InfiniBand	2 lanes of 100Gb/s
HDR	200Gb/s InfiniBand	4 lanes of 50Gb/s
HDR100	100Gb/s InfiniBand	2 lanes of 50Gb/s
EDR	100Gb/s InfiniBand	4 lanes of 25Gb/s

The following tables present the connectivity matrix, between NVIDIA Quantum based switches, ConnectX HCA, and the cables.

## Switch-to-Switch Connectivity

NVIDIA Quantum-2 switches come with OSFP cages. NVIDIA Quantum and Switch-IB 2 switches come with QSFP cages. The connectivity matrix below are separated into multiple tables due to the above physical differences between the switches.

Switch	Switch	Cable			
		NDR Transceiver	NDR DAC/ACC	HDR DAC/AOC	EDR DAC/AOC
NVIDIA Quantum-2	NVIDIA Quantum-2	2 × NDR	2 × NDR	N/A	N/A
NVIDIA Quantum-2	NVIDIA Quantum	N/A	N/A	2 × HDR	2 × EDR
NVIDIA Quantum-2	Switch-IB 2	N/A	N/A	N/A	2 × EDR

## HCA-to-Switch Connectivity

Switch		Adapter	Cable		
			HDR AOC	HDR DAC	HDR100 DAC/AOC (Copper Cables Only)
ConnectX-6 200Gb/s	NDR Switch	NVIDIA Quantum-2	2 × HDR	2 × HDR	4 × HDR100
ConnectX-6 100Gb/s		NVIDIA Quantum-2	N/A	2 × EDR	4 × HDR100
ConnectX-4/ ConnectX-5		NVIDIA Quantum-2	N/A	2 × EDR	N/A

## Supported Link Speed

The table below lists the current supported link speed.

Speed	Cable	Cable Length [meters]	Limitations
NDR	Optical	Up to 30	NDR optical cables support only NDR speed.
	Copper	Up to 2	
HDR	Optical	Up to 30	HDR optical cables support only HDR speed.
	Copper	Up to 2	

## Validated and Supported NDR Cables

Speed	OPN	Description
NDR	MCP7Y00-N001	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 1m
NDR	MCP7Y00-N002	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 2m
NDR	MCP7Y00-N01A	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 1.5m
NDR	MCP7Y00-N02A	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 2.5m
NDR	MCP7Y50-N001	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 1m
NDR	MCP7Y50-N002	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 2m
NDR	MCP7Y50-N01A	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 1.5m
NDR	MMA4Z00-NS	NVIDIA twin port transceiver, 800Gbps, 2xNDR, OSFP, 2xMPO12 APC, 850nm MMF, up to 50m, finned

<b>Speed</b>	<b>OPN</b>	<b>Description</b>
NDR	MMS4X00-NL	NVIDIA twin port transceiver, 800Gbps,2xNDR, OSFP, 2xMPO12 APC, 1310nm SMF, up to 30m, finned
NDR	MMS4X00-NS	NVIDIA twin port transceiver, 800Gbps,2xNDR, OSFP, 2xMPO12 APC, 1310nm SMF, up to 100m, finned
NDR	MMA1Z00-NS400	NVIDIA single port transceiver, 400Gbps,NDR, QSFP112, MPO12 APC, 850nm MMF, up to 50m, flat top
NDR	MCA4J80-N003-FTF	NVIDIA Active copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 3m, flat to finned
NDR	MCA7J70-N004	NVIDIA active copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 4m
NDR	MCA7J70-N005	NVIDIA active copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 5m
NDR	MCA7J60-N004	NVIDIA active copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 4m
NDR	MCA7J60-N005	NVIDIA active copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 5m
NDR	MCA7J65-N004	NVIDIA Active copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112, 4m
NDR	MCA7J65-N005	NVIDIA Active copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112, 5m
NDR	MCA7J75-N004	NVIDIA Active copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 4m
NDR	MCA7J75-N005	NVIDIA Active copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 5m
NDR	MCP7Y10-N001	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,1m
NDR	MCP7Y10-N002	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,2m
NDR	MCP7Y10-N003	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,3m
NDR	MCP7Y10-N01A	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,1.5m
NDR	MCP7Y10-N02A	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,2.5m

<b>Speed</b>	<b>OPN</b>	<b>Description</b>
NDR	MCP7Y40-N001	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 1m
NDR	MCP7Y40-N002	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 2m
NDR	MCP7Y40-N003	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 3m
NDR	MCP7Y40-N01A	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 1.5m
NDR	MCP7Y40-N02A	NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 2.5m
NDR	MMS4X00-NL400 *	NVIDIA single port transceiver, 400Gbps,NDR, OSFP, MPO12 APC, 1310nm SMF, up to 30m, flat top
NDR	MMA4Z00-NS400 *	NVIDIA single port transceiver, 400Gbps,NDR, OSFP, MPO12 APC, 850nm MMF, up to 50m, flat top
NDR	MMS4X00-NS400 *	NVIDIA single port transceiver, 400Gbps,NDR, OSFP, MPO12 APC, 1310nm SMF, up to 100m, flat top
NDR	MCP4Y10-N001-FLT *	NVIDIA Passive Copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 1m, flat top
NDR	MCP4Y10-N00A-FLT *	NVIDIA Passive Copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 0.5m, flat top
NDR	MCA4J80-N003-FLT *	Active copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 3m, flat top
NDR	MCP4Y10-N00A **	NVIDIA passive copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 0.5m
NDR	MCP4Y10-N00B **	NVIDIA passive copper cable, IB NDR, up to 800Gb/s, OSFP, 0.75m
NDR	MCP4Y10-N001 **	NVIDIA passive copper cable, IB NDR, up to 800Gb/s, OSFP, 1m
NDR	MCP4Y10-N01A **	NVIDIA passive Copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 1.5m
NDR	MCP4Y10-N002 **	NVIDIA passive copper cable, IB NDR, up to 800Gb/s, OSFP, 2m

## **Warning**

\* Cables supported in adapter cards that are not supported in switches. \*\* Cables supported in switches that are not supported in adapter cards.

## Validated and Supported HDR Cables

Speed	OPN	Description
HDR	MCP7Y60-H001	NVIDIA passive copper splitter cable, IB twin port HDR 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 1m
HDR	MCP7Y60-H002	NVIDIA passive copper splitter cable, IB twin port HDR 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 2m
HDR	MCP7Y60-H01A	NVIDIA passive copper splitter cable, IB twin port HDR 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 1.5m
HDR	MCP7Y70-H001	NVIDIA passive copper splitter cable, IB twin port HDR 400Gb/s to 4x100Gb/s, OSFP to 4xQSFP56, 1m
HDR	MCP7Y70-H002	NVIDIA passive copper splitter cable, IB twin port HDR 400Gb/s to 4x100Gb/s, OSFP to 4xQSFP56, 2m
HDR	MCP7Y70-H01A	NVIDIA passive copper splitter cable, IB twin port HDR 400Gb/s to 4x100Gb/s, OSFP to 4xQSFP56, 1.5m
HDR	MFA7U10-H003	NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 3m
HDR	MFA7U10-H005	NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 5m
HDR	MFA7U10-H010	NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 10m
HDR	MFA7U10-H015	NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 15m
HDR	MFA7U10-H020	NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 20m

<b>Speed</b>	<b>OPN</b>	<b>Description</b>
HDR	MFA7U10-H030	NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 30m
HDR	MCP1650-H001E30	NVIDIA Passive Copper cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, 1m, black pulltab, 30AWG
HDR	MCP1650-H002E26	NVIDIA Passive Copper cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, 2m, black pulltab, 26AWG
HDR	MCP1650-H00AE30	NVIDIA Passive Copper cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, 0.5m, black pulltab, 30AWG
HDR	MCP1650-H01AE30	NVIDIA Passive Copper cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, 1.5m, black pulltab, 30AWG
HDR	MFS1S00-H003E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 3m
HDR	MFS1S00-H005E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 5m
HDR	MFS1S00-H010E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 10m
HDR	MFS1S00-H015E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 15m
HDR	MFS1S00-H020E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 20m
HDR	MFS1S00-H030E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 30m
HDR	MFS1S00-H050E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 50m
HDR	MFS1S00-H100E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 100m
HDR	MFS1S00-H003V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 3m
HDR	MFS1S00-H005V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 5m
HDR	MFS1S00-H010V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 10m
HDR	MFS1S00-H015V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 15m

Speed	OPN	Description
HDR	MFS1S00-H020V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 20m
HDR	MFS1S00-H030V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 30m
HDR	MFS1S00-H050V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 50m
HDR	MFS1S00-H100V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 100m
HDR	MFS1S00-H130V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 130m
HDR	MFS1S00-H150V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, 150m
HDR	MFS1S50-H0xxE	NVIDIA Active Fiber Splitter cable, IB HDR, 200Gb/s to 2x100Gb/s, QSFP56 to 2xQSFP56, LSZH, up to 30m

## Firmware Upgrade

Firmware upgrade may be performed directly from any previous version to this version. To upgrade firmware, please refer to the NVIDIA Firmware Tools (MFT) package at [network.nvidia.com/products/adapter-software/firmware-tools/](https://network.nvidia.com/products/adapter-software/firmware-tools/)

## PRM Revision Compatibility

This firmware version complies with the NVIDIA Switches Programmer's Reference Manual (PRM), Rev 1.40 or later.

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# Changes and New Features

**NOTE:** This firmware (with a new GPIO configuration) improves long-term Quantum-2 operation, and it is strongly desired that all customers upgrade to this version (or later).

Keyword	Description
Device HW	<p>Added limited support for MMS4X50-NM module.</p> <div style="background-color: #f0e6e6; padding: 10px;"><p> <b>Warning</b> When working with two 4xports, toggling one port following the other port might lead to link flap issue. To recover, toggle the port again.</p></div>
General	See <a href="#">Bug fixes</a> .

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# Bug Fixes in this Firmware Version

The following table provides a list of **firmware** bugs fixed in this version.

Internal Ref.	Issues
35926593585886	<b>Description:</b> Quantum-2 unmanaged switch may freeze while sending MVCR.
	<b>Keywords:</b> MVCR, Switch
	<b>Discovered in Version:</b> 31.2012.1024
	<b>Fixed in Version:</b> 31.2012.1068
358904435877033573164	<b>Description:</b> Rare issue that triggers the i2c to module connection to lock and causes the Quantum-2 switch to freeze.
	<b>Keywords:</b> i2c, Switch
	<b>Discovered in Version:</b> 31.2012.1024
	<b>Fixed in Version:</b> 31.2012.1068
3548254	<b>Description:</b> FR4 MMS4X50-NM cable link-up failure after a disconnect or AC cycle.
	<b>Keywords:</b> Cables, link up
	<b>Discovered in Version:</b> 31.2012.1024
	<b>Fixed in Version:</b> 31.2012.1068
3570478	<b>Description:</b> Fixed SNR value calculation for correct readings from the MMA4Z00 optical cable module.
	<b>Keywords:</b> SNR
	<b>Discovered in Version:</b> 31.2012.1024
	<b>Fixed in Version:</b> 31.2012.1068
3311198	<b>Description:</b> Disabled "low priority credits" feature on the switch side that caused the credits mechanism to overload

Internal Ref.	Issues
	the links with credit packets, reducing the available bandwidth for transmitting data packets on the link.
	<b>Keywords:</b> Bandwidth
	<b>Discovered in Version:</b> 31.2012.1024
	<b>Fixed in Version:</b> 31.2012.1068

For a list of bug fixed from previous versions, see [Bug Fixes History](#).

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# Known Issues

The following sections describe known issues in firmware releases and possible workarounds.

Internal Ref	Issue
3436244	<b>Description:</b> On rare occasions, a 'group join' request may reach a timeout.
	<b>Workaround:</b> N/A
	<b>Keywords:</b> SHARP
	<b>Fixed in Version:</b> 31.2012.1024
3198634	<b>Description:</b> When using HDR speeds on NDR systems with NDR optic cables, the link does not raise.
	<b>Workaround:</b> N/A
	<b>Keywords:</b> HDR Speed, Optic Cables
	<b>Discovered in Version:</b> 31.2012.1024
3323781	<b>Description:</b> GMP MADs with GRH header towards the Router are not supported.
	<b>Workaround:</b> N/A
	<b>Keywords:</b> Routers
	<b>Discovered in Version:</b> 31.2010.5042
2922333	<b>Description:</b> In some cases, MMS4X00-NL1.2 may have low BER.
	<b>Workaround:</b> N/A
	<b>Keywords:</b> Signal Integrity
	<b>Discovered in Version:</b> 31.2010.2110
2838195	<b>Description:</b> Using NDR speed with Optical Transceivers causes bandwidth to be 350Gb/s instead of 400Gb/s in small packets.
	<b>Workaround:</b> N/A

Internal Ref	Issue
	<b>Keywords:</b> Optical Transceivers
	<b>Discovered in Version:</b> 31.2010.1310
2834238	<b>Description:</b> When using Optical Transceiver, toggling a port in a cage may toggle the adjacent port in the cage.
	<b>Workaround:</b> N/A
	<b>Keywords:</b> Optical Transceivers, Port Toggle
	<b>Discovered in Version:</b> 31.2010.1310
955641	<b>Description:</b> VL_HIGH_LIMIT is not affecting the VL arbiter as expected.
	<b>Workaround:</b> Arbitration table should be set using only the low priority VL arbitration table.
	<b>Keywords:</b> VL Arbitration
	<b>Discovered in Version:</b> 31.2010.1310
1249608	<b>Description:</b> Configuring weight "0" for VL, results in unexpected behavior.
	<b>Workaround:</b> Arbitration table should be configured with weights other than "0".
	<b>Keywords:</b> VL Arbitration
	<b>Discovered in Version:</b> 31.2010.1310
2057793	<b>Description:</b> Congestion profiles in VS-MAD PortProfileSetting support only fixed mode. Percentage mode is not supported.
	<b>Workaround:</b> N/A
	<b>Keywords:</b> InfiniBand Congestion Control
	<b>Discovered in Version:</b> 31.2010.1310

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# Changes and New Features History

This section includes history of changes and new feature of three major releases back. For older versions' history, please refer to their dedicated release notes.

Keyword	Description
31.2012.1024	
Fast Recovery from Unhealthy Links	Added support of the fast recovery from unhealthy links including BER monitor and credit watchdog.
Fast Recovery Notifications Towards UFM	Added support for notifications of Fast Recovery towards UFM entity (collector) in the network.
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.6102	
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.6064	
Counters: Unhealthy Link	Added BER Monitor counters for unhealthy link.
SHARP-Based Reliable Multicast Packets	Added support for SHARP-based reliable multicast packets.
Additional Status in Each MAD	Added support of <i>AdditionalStatus</i> in MAD packets.

Keyword	Description
Mirroring Congested Packets Towards UFM	Added support for mirroring of congested packets towards UFM entity (collector) in the network.
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.5108	
IB Router	Added support for NDR InfiniBand Router which enables isolation and connectivity between up to eight different InfiniBand subnets. The IB Router enables features such as Adaptive Routing (AR), Hash Based Forwarding (HBF), and Self-Healing Interconnect Enhancement for IntelIgent Datacenters (SHIELD).
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.5002	
SHARP SAT Reliable Multicast	Added engineering-sample-level support for RMC request (SHARP SAT opcode 0xA) and RMC response (SHARP SAT opcode 0xB).
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.4102	
General	Stability improvements.
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.4010	
pFRN Collector	Added support for mirroring of PFRN packets towards UFM entity (collector) in the subnet.
PKEY Filter for Multicast	Added support for MulticastPKeyTrapSuppression (PKEY mismatch filtering).
Congestion Control Updates	Added support for 1kb granularity for the port congestion profiles.
SL-to-VL Mapping	Added switch support for port mask optimization of SL-to-VL Mapping Table configuration.
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.3118	
Hash-Based Routing	Enabled the reordering of sensitive traffic to load balance on multiple ports by using Hash-Based Routing.
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.3004	
Counters	Added support for PortXmitWaitVLExtended counters.
pFRN	Added support for pFRN (Proactive Fault Routing Notification) which allows for dynamic link failure detection and route correction for topologies based on Adaptive Routing
General	See <a href="#">Bug fixes</a> .

Keyword	Description
31.2010.2300	

Keyword	Description
SHARPV3	Added GA-level support for aggregation jobs to run over parallel links.
General	<a href="#">Bug fixes.</a>

Keyword	Description
31.2010.2246	
SHARPV3	Added beta-level support for aggregation jobs to run over parallel links.
General	<a href="#">Bug fixes.</a>

Keyword	Description
31.2010.2110	
SHARPV3	Added GA-level support for SHARPV3 on Quantum-2 systems.
General	<a href="#">Bug fixes.</a>

Keyword	Description
31.2010.2036	
Systems	Added power and system monitoring optimizations.
SHARP V3	Added beta-level support for SHARP V3 on Quantum-2 systems.
Hash Based Forwarding	Added alpha-level support for Hash Based Forwarding Routing Capability.
Security	Added security enhancements to QM9790 system.

<b>Keyword</b>	<b>Description</b>
31.2010.1310	
Systems	Added GA-level support for NVIDIA Quantum-2-based switch QM9790.
Congestion Control	Added ES-level support for congestion control class key.
Vendor Key	Added ES-level support for vendor class key.
Hierarchy Information	Added support for Hierarchy Information mad.
Remote Debug Token	Added support for Remote Debug Token.
NVIDIA® Scalable Hierarchical Aggregation and Reduction Protocol (SHARP) <sup>™</sup>	Added GA-level support for SHARPV2 for NVIDIA Quantum-2 systems.
Counters	Added support for PortVLXmitFlowCtlUpdateErrors counters.
Security	Added support for Secure Firmware and Secure Firmware Boot on NVIDIA Quantum-2 systems.

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# Bug Fixes History

The following table provides a list of bugs fixed in version 31.2012.1024.

Internal Ref.	Issues
3554182	<b>Description:</b> Link does not raise with 2 <sup>nd</sup> source MMS4X00-NS transceivers.
	<b>Keywords:</b> Cables, link up
	<b>Discovered in Version:</b> 31.2010.6064
	<b>Fixed in Version:</b> 31.2012.1024
3538638	<b>Description:</b> The message of code 57 in the PDDR Troubleshooting information page was incorrect.
	<b>Keywords:</b> Link Diagnostics
	<b>Discovered in Version:</b> 31.2010.6064
	<b>Fixed in Version:</b> 31.2012.1024
3407038	<b>Description:</b> An unresponsive PSU client can cause the SDA I <sup>2</sup> C line to hang.
	<b>Keywords:</b> I <sup>2</sup> C
	<b>Discovered in Version:</b> 31.2010.6064
	<b>Fixed in Version:</b> 31.2012.1024
3477039	<b>Description:</b> Wrong RTT value is exposed under PRTL PRM.
	<b>Keywords:</b> Registers, RTT Value
	<b>Discovered in Version:</b> 31.2010.6064
	<b>Fixed in Version:</b> 31.2012.1024
3481394	<b>Description:</b> When trying to choose the threshold for the Fast Recovery feature (BER Config), it is possible that threshold 0 will be loaded.
	<b>Keywords:</b> Fast Recovery, BER Configuration
	<b>Discovered in Version:</b> 31.2010.6064

Internal Ref.	Issues
	<b>Fixed in Version:</b> 31.2012.1024
3499997	<b>Description:</b> In some cases, the combination of SHARP SAT traffic and SHARP MADs can cause the switch to get stuck.
	<b>Keywords:</b> SHARP
	<b>Discovered in Version:</b> 31.2010.4210
	<b>Fixed in Version:</b> 31.2012.1024
3451519	<b>Description:</b> When using ibdiagnet, an incorrect module alarm type was reported.
	<b>Keywords:</b> ibdiagnet, Module Temperature Alarm Type
	<b>Discovered in Version:</b> 31.2010.5108
	<b>Fixed in Version:</b> 31.2012.1024

The following table provides a list of bugs fixed in previous versions. For a list of bug fixed from the current version, see [Bug Fixes](#).

Internal Ref.	Issues
3326692	<b>Description:</b> Wrap-around of the time_since_last_clear counter caused incorrect reporting of counters on the port.
	<b>Keywords:</b> Counters
	<b>Discovered in Version:</b> 31.2010.3118
	<b>Fixed in Version:</b> 31.2010.6102
3389432	<b>Description:</b> The flint burning firmware process might take longer than expected, possibly leading to timeouts in SM and logical links drops by the SM, which, in turn, may lead to failure of the flint burn command.
	<b>Keywords:</b> SM, Timeout, Flint, Failure
	<b>Discovered in Version:</b> 31.2010.6064
	<b>Fixed in Version:</b> 31.2010.6102
3339363	<b>Description:</b> pFRN notification state machine got halted in busy-wait on all riscs due to inability to free TX credits.

Internal Ref.	Issues
	<b>Keywords:</b> pFRN
	<b>Discovered in Version:</b> 31.2010.3118
	<b>Fixed in Version:</b> 31.2010.6064
3393378	<b>Description:</b> In some cases, pFRN configuration over multi-SWID caused out-of-bound access to an array and overran FLID configuration.
	<b>Keywords:</b> pFRN
	<b>Fixed in Version:</b> 31.2010.6064
3342918	<b>Description:</b> On rare occasions, the port might get stuck (in all speeds) during the link up flow when using optical modules.
	<b>Keywords:</b> Port Link Up, Port Toggling, Optical Modules
	<b>Fixed in Version:</b> 31.2010.6064
3395821	<b>Description:</b> Bandwidth is lower than expected on MMS4X00-NL-QP1 cable.
	<b>Keywords:</b> MMS4X00-NL-QP1, Bandwidth
	<b>Fixed in Version:</b> 31.2010.6064
2824249	<b>Description:</b> After a firmware update failure, the bad image was not erased.
	<b>Keywords:</b> Installation, Firmware
	<b>Discovered in Version:</b> 31.2010.2036
	<b>Fixed in Version:</b> 31.2010.6064
3362685	<b>Description:</b> In QM9700 systems, when a transceiver module is plugged in when only one of the optic cables is connected (while the second cable is disconnected), the port LED may be incorrectly displayed on the disconnected side.
	<b>Keywords:</b> Port LED, Optic Cables
	<b>Discovered in Version:</b> 31.2010.4102
	<b>Fixed in Version:</b> 31.2010.5108
3377608	<b>Description:</b> When operating in dynamic trees allocation mode, MAD error responses might be received in libsharp.
	<b>Keywords:</b> sharp_am, libsharp

Internal Ref.	Issues
	<b>Fixed in Version:</b> 31.2010.5108
3362200	<b>Description:</b> In rare cases that involve stress of traffic, unexpected hardware fast path behavior may occur, possibly leading to the switch firmware hanging when toggling the ports.
	<b>Keywords:</b> Turbo Path
	<b>Discovered in Version:</b> 31.2010.5002
	<b>Fixed in Version:</b> 31.2010.5108
3301825	<b>Description:</b> The firmware does not return values for the counters "PortSwLifetimeLimitDiscards" and "PortSwHOQLifetimeLimitDiscards". Support has now been added for the counters.
	<b>Keywords:</b> Counters
	<b>Discovered in Version:</b> 31.2010.3118
	<b>Fixed in Version:</b> 31.2010.5042
3335002	<b>Description:</b> pFRN mirror v1 header pad count showed an invalid padding size.
	<b>Keywords:</b> PFRN
	<b>Discovered in Version:</b> 31.2010.4010
	<b>Fixed in Version:</b> 31.2010.5042
3269531	<b>Description:</b> After multiple MSPS (Management System Power Supply register) calls, the switch gets stuck.
	<b>Keywords:</b> MSPS
	<b>Discovered in Version:</b> 27.2010.3118
	<b>Fixed in Version:</b> 27.2010.5002
3267152	<b>Description:</b> On NDR devices, when collecting BER data, the peer falls, causing the switch to hang.
	<b>Keywords:</b> BER COLLECT
	<b>Discovered in Version:</b> 31.2010.4102
	<b>Fixed in Version:</b> 31.2010.5002
3261861	<b>Description:</b> Connecting an HDR device to an NDR device with Optical cables longer than 30m causes degradation in the

Internal Ref.	Issues
	bandwidth.
	<b>Keywords:</b> HDR-to-NDR
	<b>Discovered in Version:</b> 31.2010.4102
	<b>Fixed in Version:</b> 31.2010.5002
2974424	<b>Description:</b> Currently, on cables that perform polarity inversion there is no link up.
	<b>Keywords:</b> Cables, Polarity Inversion
	<b>Discovered in Version:</b> 31.2010.3118
	<b>Fixed in Version:</b> 31.2010.5002
3199650	<b>Description:</b> A physical link failure between switches while a SHARP job is running and utilizing the link can cause one of the switches to become invalid for further SHARP jobs. This can result in either a "No resource" response for new SHARP job requests or in jobs getting stuck. The bug fix requires SHARP version 3.2.
	<b>Keywords:</b> SHARP
	<b>Discovered in Version:</b> 31.2010.4010
	<b>Fixed in Version:</b> 31.2010.4102
3245821	<b>Description:</b> In case of an AR group table set request, the ARN mask is flushed for group that has an active pFRN timer.
	<b>Keywords:</b> PFRN
	<b>Discovered in Version:</b> 31.2010.4010
	<b>Fixed in Version:</b> 31.2010.4102
3253717	<b>Description:</b> mask_force_clear_timeout timer in pFRN feature was not functional (the mask was not cleared when the timer expired).
	<b>Keywords:</b> PFRN
	<b>Discovered in Version:</b> 31.2010.4010
	<b>Fixed in Version:</b> 31.2010.4102
3242209	<b>Description:</b> Set PFRN mad did not return error on wrong inputs in mask_clear_timer and mask_force_clear_timer fields.
	<b>Keywords:</b> PFRN

Internal Ref.	Issues
	<b>Discovered in Version:</b> 31.2010.4010
	<b>Fixed in Version:</b> 31.2010.4102
3143685	<b>Description:</b> The switch does not return SN or PN when trying to call via mlxlink or ibdiagnet.
	<b>Keywords:</b> SN, PN, mlxlink, ibdiagnet
	<b>Discovered in Version:</b> 31.2010.2300
	<b>Fixed in Version:</b> 31.2010.4010
3174239	<b>Description:</b> On rare occasions, traps were not properly repressed, which caused redundant traps to be sent multiple times.
	<b>Keywords:</b> Traps
	<b>Discovered in Version:</b> 31.2010.3118
	<b>Fixed in Version:</b> 31.2010.4010
3002314	<b>Description:</b> On rare occasion, when port is configured to mloop toggle may cause link to not rise.
	<b>Keywords:</b> Optic in Mloop
	<b>Discovered in Version:</b> 31.2010.2110
	<b>Fixed in Version:</b> 31.2010.3118
3127727	<b>Description:</b> On rare occasion, when egress port is split to two, the egress port may get stuck due to wrong Fast Path configuration.
	<b>Keywords:</b> Switch Hang, Fast Path, Split
	<b>Discovered in Version:</b> 31.2010.3004
	<b>Fixed in Version:</b> 31.2010.3118
3082569	<b>Description:</b> In some traffic patterns involving small packets, the PortRcvErrors counter may mistakenly count events of local physical errors due to an internal flow in the hardware that involves link packets.
	<b>Keywords:</b> Counters
	<b>Discovered in Version:</b> 31.2010.2246
	<b>Fixed in Version:</b> 31.2010.3004
3085427	<b>Description:</b> On rare occasions, SHARP semaphore may remain locked on a port following an event of a port link down or an

Internal Ref.	Issues
	<p>application crash.</p> <p><b>Keywords:</b> SHARPV3</p> <p><b>Discovered in Version:</b> 31.2010.2036</p> <p><b>Fixed in Version:</b> 31.2010.3004</p>
3011581	<p><b>Description:</b> On rare occasions, job failures with SharpError trap may be experienced as a result of previous jobs that have failed.</p> <p><b>Keywords:</b> SHARPV3</p> <p><b>Discovered in Version:</b> 31.2010.2036</p> <p><b>Fixed in Version:</b> 31.2010.3004</p>
3000602	<p><b>Description:</b> After disconnecting MMS4X00-NL* cable and connecting Ultron cable to the same port, ports fails to link up.</p> <p><b>Keywords:</b> Cables</p> <p><b>Discovered in Version:</b> 31.2010.2110</p> <p><b>Fixed in Version:</b> 31.2010.2300</p>
3060122	<p><b>Description:</b> In the event of link fault of a link between root switch and non-root switch during the run of a job, the next job run on the non-root switch may fail.</p> <p><b>Keywords:</b> SHARPV3</p> <p><b>Discovered in Version:</b> 31.2010.2036</p> <p><b>Fixed in Version:</b> 31.2010.2300</p>
2923464	<p><b>Description:</b> When using MMS4X00-NL Optical module, on rare occasions port that is in NDR speed may get stuck and stay in Polling state.</p> <p><b>Keywords:</b> NDR, Optical Module</p> <p><b>Discovered in Version:</b> 31.2010.1404</p> <p><b>Fixed in Version:</b> 31.2010.2246</p>
2859363	<p><b>Description:</b> When using NVIDIA Quantum-2 systems in Auto-Neg mode, NDR speed in one lane (1x) is not supported.</p> <p><b>Keywords:</b> Auto-Negotiation</p> <p><b>Discovered in Version:</b> 31.2010.1310</p>

Internal Ref.	Issues
	<b>Fixed in Version:</b> 31.2010.2246
3033131	<b>Description:</b> The number of flows changed from 2 to 1, as intended.
	<b>Keywords:</b> SHARPV3
	<b>Discovered in Version:</b> 31.2010.2110
	<b>Fixed in Version:</b> 31.2010.2246
2972388	<b>Description:</b> Running of concurrent jobs may lead to states where jobs unexpectedly terminate or get stuck.
	<b>Keywords:</b> SHARPV3
	<b>Discovered in Version:</b> 31.2010.2036
	<b>Fixed in Version:</b> 31.2010.2110
2982113	<b>Description:</b> On rare occasions, job resource cleanup may fail.
	<b>Keywords:</b> SHARPV3
	<b>Discovered in Version:</b> 31.2010.2036
	<b>Fixed in Version:</b> 31.2010.2110
2971339	<b>Description:</b> During high load scenarios, performance degradation may be experienced.
	<b>Keywords:</b> SHARPV3
	<b>Discovered in Version:</b> 31.2010.2036
	<b>Fixed in Version:</b> 31.2010.2110
2849215	<b>Description:</b> On NVIDIA Quantum-2 switches, when working with MFA7U10-H0xx cables, if one of the ports in a cage is disabled at the time of initialization by user configuration, reenabling the port will require toggling the link (i.e. enable → disable → enable).
	<b>Keywords:</b> NVIDIA Quantum-2, Cables
	<b>Discovered in Version:</b> 31.2010.1310
	<b>Fixed in Version:</b> 31.2010.2036
2890632	<b>Description:</b> On NVIDIA Quantum-2 systems, changing the Optical module rate was not allowed.
	<b>Keywords:</b> Optical Modules
	<b>Discovered in Version:</b> 31.2010.1310

Internal Ref.	Issues
	<b>Fixed in Version:</b> 31.2010.2036
2885798	<b>Description:</b> In NVIDIA Quantum-2 systems, effective errors may occur with short Copper cable MCP4Y10-N00B.
	<b>Workaround:</b> N/A
	<b>Discovered in Version:</b> 31.2010.1310
	<b>Fixed in Version:</b> 31.2010.2036
2910161	<b>Description:</b> In auto-negotiation flow, using copper cables when toggling both port's sides may cause the port to get stuck on rare occasions.
	<b>Keywords:</b> Auto-Negotiation, Copper Cables
	<b>Discovered in Version:</b> 31.2010.1310
	<b>Fixed in Version:</b> 31.2010.2036

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