

**NVIDIA Quantum-2 Firmware Release Notes** v31.2012.4036

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#### **Release Notes Update History**

| Version      | Date        | Description                                    |
|--------------|-------------|--|
| 31.2012.4036 | May 2, 2024 | Initial release of this release notes version. |

#### **Overview**

Firmware, which is added at the time of manufacturing, is used to run user programs on the device and can be thought of as the software that allows hardware to run. Embedded firmware is used to control the functions of various hardware devices and systems, much like a computer's operating system (OS) controls the function of software applications. Firmware may be written into read-only memory (ROM), erasable programmable read-only memory (EPROM), or flash memory.

#### **Firmware Download**

Please visit <a href="https://www.nvidia.com/en-us/networking/">https://www.nvidia.com/en-us/networking/</a> Support Support Firmware Download

#### **Document Revision History**

A list of the changes made to this document are provided in <u>Changes and New Features</u> and <u>Changes and New Features History</u>.

# Firmware Compatible Products

These are the release notes for the NVIDIA Quantum<sup>™</sup>-2 firmware. This firmware complements the NVIDIA Quantum switch with a set of advanced features, allowing easy and remote management of the switch.

This firmware supports the following protocols:

InfiniBand—SDR, EDR, HDR, NDR

#### **Supported Switch Systems**

This firmware supports the devices listed in the table below:

| Model<br>Number | NVIDIA SKU                                       | Description  |
|-----------------|--|--|
| QM9790          | 920-9B210-<br>00FN-0D2<br>920-9B210-<br>00FN-0D0 | NVIDIA Quantum 2 based NDR InfiniBand Switch, 64<br>NDR ports, 32 OSFP ports, 2 Power Supplies (AC),<br>Standard depth |

### **Firmware Interoperability**

This firmware version has been validated to work against platforms with the following firmware and software versions.

| HCA/Switch       | Firmware Version |
|------------------|------------------|
| NVIDIA Quantum-2 | 31.2012.4036     |
| NVIDIA Quantum   | 27.2012.4036     |
| ConnectX-7       | 28.37.1014       |

| HCA/Switch          | Firmware Version |
|---------------------|------------------|
| ConnectX-6          | 20.37.1014       |
| Minimal MFT version | 4.27.0           |

#### **Supported Cables**



#### Note

NVIDIA does not support InfiniBand cables or modules not qualified or approved by NVIDIA.

#### **Switch and HCAs InfiniBand Cable Connectivity Matrix**

NVIDIA Quantum™ based switches and NVIDIA® ConnectX® HCAs support HDR (PAM4, 50Gb/s per lane) and EDR (NRZ, 25Gb/s per lane) technologies. As the ConnectX adapter cards are identified by their maximum supported throughput (e.g., ConnectX-6 VPI 100Gb/s card can support either 2-lanes of 50Gb/s or 4-lanes of 25Gb/s), the exact connectivity will be determined by the cable that is being used.

#### As a reference:

| Speed Mode | Speed Supported    | Number of Lanes Used |
|------------|--------------------|----------------------|
| NDR        | 400Gb/s InfiniBand | 4 lanes of 100Gb/s   |
| NDR200     | 200Gb/s InfiniBand | 2 lanes of 100Gb/s   |
| HDR        | 200Gb/s InfiniBand | 4 lanes of 50Gb/s    |
| HDR100     | 100Gb/s InfiniBand | 2 lanes of 50Gb/s    |
| EDR        | 100Gb/s InfiniBand | 4 lanes of 25Gb/s    |

The following tables present the connectivity matrix, between NVIDIA Quantum based switches, ConnectX HCA, and the cables.

#### **Switch-to-Switch Connectivity**

NVIDIA Quantum-2 switches come with OSFP cages. NVIDIA Quantum and Switch-IB 2 switches come with QSFP cages. The connectivity matrix below are separated into multiple tables due to the above physical differences between the switches.

| Switch               | Switch              | Cable              |                |                 |                 |  |
|----------------------|---------------------|--------------------|----------------|-----------------|-----------------|--|
|                      |                     | NDR<br>Transceiver | NDR<br>DAC/ACC | HDR DAC/<br>AOC | EDR DAC/<br>AOC |  |
| NVIDIA Quantum-<br>2 | NVIDIA<br>Quantum-2 | 2 × NDR            | 2 × NDR        | N/A             | N/A             |  |
| NVIDIA Quantum-<br>2 | NVIDIA<br>Quantum   | N/A                | N/A            | 2 × HDR         | 2 × EDR         |  |
| NVIDIA Quantum-<br>2 | Switch-IB 2         | N/A                | N/A            | N/A             | 2 × EDR         |  |

### **HCA-to-Switch Connectivity**

| Switch                    |               | Adapter          | Cable   |            |   |
|---------------------------|---------------|------------------|---------|------------|---|
|                           |               |                  | HDR AOC | HDR<br>DAC | HDR100 DAC/AOC<br>(Copper Cables<br>Only) |
| ConnectX-6<br>200Gb/s     |               | NVIDIA Quantum-2 | 2 × HDR | 2 × HDR    | 4 × HDR100                                |
| ConnectX-6<br>100Gb/s     | NDR<br>Switch | NVIDIA Quantum-2 | N/A     | 2 × EDR    | 4 × HDR100                                |
| ConnectX-4/<br>ConnectX-5 |               | NVIDIA Quantum-2 | N/A     | 2 × EDR    | N/A                                       |

### **Supported Link Speed**

The table below lists the current supported link speed.

| Speed   | Cable   | Cable Length<br>[meters] | Limitations                                |
|---------|---------|--------------------------|--|
| NIDD    | Optical | Up to 30                 | NDD optical cables support only NDD speed  |
| NDR Cop | Copper  | Up to 2                  | NDR optical cables support only NDR speed. |
| Optical |         | Up to 30                 | LIDD optical cables support appuliDD speed |
| HDR     | Copper  | Up to 2                  | HDR optical cables support only HDR speed. |

## **Validated and Supported NDR Cables**

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN            | Description  | Lifecycl<br>e Phase |
|--------------|--------------------------|--------------------------|--|---------------------|
| NDR          | 980-<br>9I601-<br>00N003 | MCA4J8<br>0-N003-<br>FTF | NVIDIA active copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 3m, flat to finned          | MP                  |
| NDR          | 980-<br>91949-<br>00N005 | MCA7J6<br>0-N005         | NVIDIA active copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 5m | P-Rel               |
| NDR          | 980-<br>9I50E-<br>00N005 | MCA7J7<br>0-N005         | NVIDIA active copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 5m | P-Rel               |
| NDR          | 980-<br>9IA0F-<br>00N001 | MCP4Y1<br>0-N001         | NVIDIA passive Copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 1m                         | MP                  |
| NDR          | 980-<br>9IA0K-<br>00N00A | MCP4Y1<br>0-N00A         | NVIDIA passive Copper cable, IB twin port NDR, up to 800Gb/s, OSFP, 0.5m                       | MP                  |
| NDR          | 980-<br>9I432-<br>00N001 | MCP7Y0<br>0-N001         | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP,1m | P-Rel               |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN    | Description   | Lifecycl<br>e Phase |
|--------------|--------------------------|------------------|---|---------------------|
| NDR          | 980-<br>91924-<br>00N002 | MCP7Y0<br>0-N002 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 2m     | P-Rel               |
| NDR          | 980-<br>9192N-<br>00N003 | MCP7Y0<br>0-N003 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 3m     | P-Rel               |
| NDR          | 980-<br>9I926-<br>00N01A | MCP7Y0<br>0-N01A | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP,1.5m    | P-Rel               |
| NDR          | 980-<br>9192O-<br>00N02A | MCP7Y0<br>0-N02A | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xOSFP, 2.5m   | P-Rel               |
| NDR          | 980-<br>9I928-<br>00N001 | MCP7Y1<br>0-N001 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,1m   | P-Rel               |
| NDR          | 980-<br>91929-<br>00N002 | MCP7Y1<br>0-N002 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,2m   | P-Rel               |
| NDR          | 980-<br>9180P-<br>00N003 | MCP7Y1<br>0-N003 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,3m   | P-Rel               |
| NDR          | 980-<br>9I80A-<br>00N01A | MCP7Y1<br>0-N01A | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,1.5m | P-Rel               |
| NDR          | 980-<br>9180Q-<br>00N02A | MCP7Y1<br>0-N02A | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 2x400Gb/s, OSFP to 2xQSFP112,2.5m | P-Rel               |
| NDR          | 980-<br>9180B-<br>00N001 | MCP7Y4<br>0-N001 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 1m  | P-Rel               |
| NDR          | 980-<br>9180C-<br>00N002 | MCP7Y4<br>0-N002 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 2m  | P-Rel               |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN    | Description  | Lifecycl<br>e Phase |
|--------------|--------------------------|------------------|--|---------------------|
| NDR          | 980-<br>9I75R-<br>00N003 | MCP7Y4<br>0-N003 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 3m   | P-Rel               |
| NDR          | 980-<br>9I75D-<br>00N01A | MCP7Y4<br>0-N01A | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 1.5m | P-Rel               |
| NDR          | 980-<br>9I75S-<br>00N02A | MCP7Y4<br>0-N02A | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xQSFP112, 2.5m | P-Rel               |
| NDR          | 980-<br>9I75E-<br>00N001 | MCP7Y5<br>0-N001 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 1m      | P-Rel               |
| NDR          | 980-<br>9I46G-<br>00N002 | MCP7Y5<br>0-N002 | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 2m      | P-Rel               |
| NDR          | 980-<br>9I46I-<br>00N01A | MCP7Y5<br>0-N01A | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 1.5m    | P-Rel               |
| NDR          | 980-<br>9I73V-<br>000005 | MFP7E1<br>0-N005 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 5m  | MP                  |
| NDR          | 980-<br>9I57W-<br>000007 | MFP7E1<br>0-N007 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 7m  | MP                  |
| NDR          | 980-<br>9I57X-<br>00N010 | MFP7E1<br>0-N010 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 10m   | MP                  |
| NDR          | 980-<br>9I57Y-<br>000015 | MFP7E1<br>0-N015 | NVIDIA passive fiber cable, MMF , MPO12 APC to MPO12 APC, 15m  | MP                  |
| NDR          | 980-<br>9157Z-<br>000020 | MFP7E1<br>0-N020 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 20m   | MP                  |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN    | Description  | Lifecycl<br>e Phase |
|--------------|--------------------------|------------------|--|---------------------|
| NDR          | 980-<br>9I573-<br>00N025 | MFP7E1<br>0-N025 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 25m   | MP                  |
| NDR          | 980-<br>91570-<br>00N030 | MFP7E1<br>0-N030 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 30m   | MP                  |
| NDR          | 980-<br>91570-<br>00N035 | MFP7E1<br>0-N035 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 35m   | MP                  |
| NDR          | 980-<br>9I570-<br>00N040 | MFP7E1<br>0-N040 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 40m   | MP                  |
| NDR          | 980-<br>9I57Y-<br>00N050 | MFP7E1<br>0-N050 | NVIDIA passive fiber cable, MMF, MPO12 APC to MPO12 APC, 50m   | MP                  |
| NDR          | 980-<br>9I571-<br>00N003 | MFP7E2<br>0-N003 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 3m  | MP                  |
| NDR          | 980-<br>9I572-<br>00N005 | MFP7E2<br>0-N005 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 5m  | MP                  |
| NDR          | 980-<br>9I573-<br>00N007 | MFP7E2<br>0-N007 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 7m  | MP                  |
| NDR          | 980-<br>9I554-<br>00N010 | MFP7E2<br>0-N010 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 10m | MP                  |
| NDR          | 980-<br>9I555-<br>00N015 | MFP7E2<br>0-N015 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 15m | MP                  |
| NDR          | 980-<br>9I556-<br>00N020 | MFP7E2<br>0-N020 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 20m | MP                  |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN    | Description  | Lifecycl<br>e Phase |
|--------------|--------------------------|------------------|--|---------------------|
| NDR          | 980-<br>9I557-<br>00N030 | MFP7E2<br>0-N030 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 30m | MP                  |
| NDR          | 980-<br>9I55Z-<br>00N050 | MFP7E2<br>0-N050 | NVIDIA passive fiber cable, MMF, MPO12 APC to 2xMPO12 APC, 50m | MP                  |
| NDR          | 980-<br>9I58C-<br>00N007 | MFP7E3<br>0-N007 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 7m    | MP                  |
| NDR          | 980-<br>9I58D-<br>00N010 | MFP7E3<br>0-N010 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 10m   | MP                  |
| NDR          | 980-<br>9I58E-<br>00N015 | MFP7E3<br>0-N015 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 15m   | MP                  |
| NDR          | 980-<br>9I58F-<br>00N020 | MFP7E3<br>0-N020 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 20m   | MP                  |
| NDR          | 980-<br>9I58G-<br>00N030 | MFP7E3<br>0-N030 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 30m   | MP                  |
| NDR          | 980-<br>9I580-<br>00N030 | MFP7E3<br>0-N040 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 40m   | MP                  |
| NDR          | 980-<br>9I58H-<br>00N050 | MFP7E3<br>0-N050 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 50m   | MP                  |
| NDR          | 980-<br>9I581-<br>00N050 | MFP7E3<br>0-N060 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 60m   | MP                  |
| NDR          | 980-<br>91582-<br>00N050 | MFP7E3<br>0-N070 | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 70m   | MP                  |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN            | Description   | Lifecycl<br>e Phase |
|--------------|--------------------------|--------------------------|---|---------------------|
| NDR          | 980-<br>9I58I-<br>00N100 | MFP7E3<br>0-N100         | NVIDIA passive fiber cable, SMF, MPO12 APC to MPO12 APC, 100m                                     | MP                  |
| NDR          | 980-<br>9I58L-<br>00N005 | MFP7E4<br>0-N005         | NVIDIA passive fiber cable, SMF, MPO12 APC to 2xMPO12 APC, 5m                                     | MP                  |
| NDR          | 980-<br>91693-<br>00NS00 | MMA1Z<br>00-<br>NS400    | NVIDIA single port transceiver, 400Gbps,NDR, QSFP112, MPO12 APC, 850nm MMF, up to 50m, flat top   | P-Rel               |
| NDR          | 980-<br>9I510-<br>00NS00 | MMA4Z<br>00-NS           | NVIDIA twin port transceiver, 800Gbps,2xNDR, OSFP, 2xMPO12 APC, 850nm MMF, up to 50m, finned      | MP                  |
| NDR          | 980-<br>9160Z-<br>00N003 | MCA4J8<br>0-N003         | NVIDIA ACTIVE COPPER CABLE, IB TWIN PORT<br>NDR UP TO 800GBS, OSFP, 3M                            | MP                  |
| NDR          | 980-<br>91600-<br>00N003 | MCA4J8<br>0-N003-<br>FLT | NVIDIA ACTIVE COPPER CABLE, IB TWIN PORT<br>NDR UP TO 800GBS, OSFP, 3M, FLAT TOP                  | MP                  |
| NDR          | 980-<br>9I601-<br>00N004 | MCA4J8<br>0-N004         | NVIDIA ACTIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800 GB/S, OSFP, 4M                         | MP                  |
| NDR          | 980-<br>91602-<br>00N005 | MCA4J8<br>0-N005         | NVIDIA ACTIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800 GB/S, OSFP, 5M                         | MP                  |
| NDR          | 980-<br>91948-<br>00N004 | MCA7J6<br>0-N004         | NVIDIA ACTIVE COPPER SPLITTER CABLE, IB NDR<br>800GBS TO 2X400GBS, OSFP TO 2XOSFP, 4M             | P-Rel               |
| NDR          | 980-<br>9I81B-<br>00N004 | MCA7J6<br>5-N004         | NVIDIA ACTIVE COPPER SPLITTER CABLE, IB TWIN PORT NDR 800GB/S TO 2X400GB/S, OSFP TO 2XQSFP112, 4M | Prototyp<br>e       |
| NDR          | 980-<br>9181C-<br>00N005 | MCA7J6<br>5-N005         | NVIDIA ACTIVE COPPER SPLITTER CABLE, IB TWIN PORT NDR 800GB/S TO 2X400GB/S, OSFP TO 2XQSFP112, 5M | Prototyp<br>e       |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN            | Description   | Lifecycl<br>e Phase |
|--------------|--------------------------|--------------------------|---|---------------------|
| NDR          | 980-<br>9I50D-<br>00N004 | MCA7J7<br>0-N004         | NVIDIA ACTIVE COPPER SPLITTER CABLE, IB TWIN PORT NDR 800GBS TO 4X200GBS, OSFP TO 4XOSFP, 4M      | P-Rel               |
| NDR          | 980-<br>9I76G-<br>00N004 | MCA7J7<br>5-N004         | NVIDIA AVTIVE COPPER SPLITTER CABLE, IB TWIN PORT NDR 800GB/S TO 4X200GB/S, OSFP TO 4XQSFP112, 4M | Prototyp<br>e       |
| NDR          | 980-<br>9I76H-<br>00N005 | MCA7J7<br>5-N005         | NVIDIA AVTIVE COPPER SPLITTER CABLE, IB TWIN PORT NDR 800GB/S TO 4X200GB/S, OSFP TO 4XQSFP112, 5M | Prototyp<br>e       |
| NDR          | 980-<br>9IA0G-<br>00N001 | MCP4Y1<br>0-N001-<br>FLT | NVIDIA PASSIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800GB/S, OSFP, 1M, FLAT TOP               | MP                  |
| NDR          | 980-<br>9IA0H-<br>00N001 | MCP4Y1<br>0-N001-<br>FTF | NVIDIA PASSIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800GB/S, OSFP, 1M, FLAT TO FINNED         | MP                  |
| NDR          | 980-<br>9IA0I-<br>00N002 | MCP4Y1<br>0-N002         | NVIDIA PASSIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800GB/S, OSFP, 2M                         | MP                  |
| NDR          | 980-<br>9IA0J-<br>00N002 | MCP4Y1<br>0-N002-<br>FLT | NVIDIA PASSIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800GB/S, OSFP, 2M, FLAT TOP               | MP                  |
| NDR          | 980-<br>9IA0L-<br>00N00A | MCP4Y1<br>0-N00A-<br>FLT | NVIDIA PASSIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800GB/S, OSFP, 0.5M, FLAT TOP             | MP                  |
| NDR          | 980-<br>9IA0Q-<br>00N01A | MCP4Y1<br>0-N01A         | NVIDIA PASSIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800GB/S, OSFP, 1.5M                       | MP                  |
| NDR          | 980-<br>9IA0R-<br>00N01A | MCP4Y1<br>0-N01A-<br>FLT | NVIDIA PASSIVE COPPER CABLE, IB TWIN PORT<br>NDR, UP TO 800GB/S, OSFP, 1.5M, FLAT TOP             | MP                  |
| NDR          | 980-<br>9I46T-<br>00N003 | MCP7Y5<br>0-N003         | NVIDIA PASSIVE COPPER SPLITTER CABLE, IB NDR 800GB/S TO 4X200GB/S, OSFP TO 4XOSFP, 3M             | P-Rel               |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN            | Description   | Lifecycl<br>e Phase |
|--------------|--------------------------|--------------------------|---|---------------------|
| HDR          | 980-<br>9I46K-<br>00H001 | MCP7Y6<br>0-H001         | MELLANOX PASSIVE COPPER SPLITTER CABLE,<br>400(2X200)GBPS TO 2X200GBPS, OSFP TO<br>2XQSFP56, 1M, FIN TO FLAT    | MP                  |
| HDR          | 980-<br>9I46L-<br>00H002 | MCP7Y6<br>0-H002         | MELLANOX PASSIVE COPPER SPLITTER CABLE,<br>400(2X200)GBPS TO 2X200GBPS, OSFP TO<br>2XQSFP56, 2M, FIN TO FLAT    | MP                  |
| HDR          | 980-<br>9I93M-<br>00H01A | MCP7Y6<br>0-H01A         | MELLANOX PASSIVE COPPER SPLITTER CABLE,<br>400(2X200)GBPS TO 2X200GBPS, OSFP TO<br>2XQSFP56, 1.5M, FIN TO FLAT  | MP                  |
| HDR          | 980-<br>9I93N-<br>00H001 | MCP7Y7<br>0-H001         | MELLANOX PASSIVE COPPER SPLITTER CABLE,<br>400(2X200)GBPS TO 4X100GBPS, OSFP TO<br>4XQSFP56, 1M, FIN TO FLAT    | MP                  |
| HDR          | 980-<br>9193O-<br>00H002 | MCP7Y7<br>0-H002         | MELLANOX PASSIVE COPPER SPLITTER CABLE,<br>400(2X200)GBPS TO 4X100GBPS, OSFP TO<br>4XQSFP56, 2M, FIN TO FLAT    | MP                  |
| HDR          | 980-<br>9I47P-<br>00H01A | MCP7Y7<br>0-H01A         | MELLANOX PASSIVE COPPER SPLITTER CABLE,<br>400(2X200) GBPS TO 4X100GBPS, OSFP TO<br>4XQSFP56, 1.5M, FIN TO FLAT | MP                  |
| HDR          | 980-<br>9I41X-<br>00H003 | MFA7U1<br>0-H003         | MELLANOX ACTIVE FIBER SPLITTER CABLE, IB<br>HDR, 400GB/S TO 2X200GB/S, OSFP TO<br>2XQSFP56, 3M                  | P-Rel               |
| HDR          | 980-<br>9I41Y-<br>00H003 | MFA7U1<br>0-H003-<br>FLT | MELLANOX AOC SPLITTER,IB TWIN PORT HDR,<br>400GB/S TO 2X200GB/S, OSFP TO 2XQSFP56,<br>3MM FLAT TOP              | P-Rel               |
| HDR          | 980-<br>9I11Z-<br>00H005 | MFA7U1<br>0-H005         | MELLANOX ACTIVE FIBER SPLITTER CABLE, IB<br>HDR, 400GB/S TO 2X200GB/S, OSFP TO<br>2XQSFP56, 5M                  | P-Rel               |
| HDR          | 980-<br>9I110-<br>00H005 | MFA7U1<br>0-H005-<br>FLT | MELLANOX AOC SPLITTER, IB TWIN PORT HDR,<br>400GB/S TO 2X200GB/S, OSFP TO 2XQSFP56, 5M,<br>FLAT TOP             | P-Rel               |
| HDR          | 980-<br>9I111-<br>00H010 | MFA7U1<br>0-H010         | MELLANOX ACTIVE FIBER SPLITTER CABLE, IB<br>HDR, 400GB/S TO 2X200GB/S, OSFP TO<br>2XQSFP56, 10M                 | P-Rel               |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN            | Description  | Lifecycl<br>e Phase |
|--------------|--------------------------|--------------------------|--|---------------------|
| HDR          | 980-<br>9I112-<br>00H010 | MFA7U1<br>0-H010-<br>FLT | MELLANOX AOC SPLITTER, IB TWIN PORT HDR,<br>400GB/S TO 2X200GB/S, OSFP TO 2XQSFP56,<br>10M, FLAT TOP | P-Rel               |
| HDR          | 980-<br>9I113-<br>00H015 | MFA7U1<br>0-H015         | MELLANOX ACTIVE FIBER SPLITTER CABLE, IB<br>HDR, 400GB/S TO 2X200GB/S, OSFP TO<br>2XQSFP56, 15M      | P-Rel               |
| HDR          | 980-<br>9I114-<br>00H015 | MFA7U1<br>0-H015-<br>FLT | MELLANOX AOC SPLITTER, IB TWIN PORT HDR,<br>400GB/S TO 2X200GB/S, OSFP TO 2XQSFP56,<br>15M, FLAT TOP | P-Rel               |
| HDR          | 980-<br>9I115-<br>00H020 | MFA7U1<br>0-H020         | MELLANOX ACTIVE FIBER SPLITTER CABLE, IB<br>HDR, 400GB/S TO 2X200GB/S, OSFP TO<br>2XQSFP56, 20M      | P-Rel               |
| HDR          | 980-<br>9I116-<br>00H020 | MFA7U1<br>0-H020-<br>FLT | MELLANOX AOC SPLITTER, IB TWIN PORT HDR,<br>400GB/S TO 2X200GB/S, OSFP TO 2XQSFP56,<br>20M, FLAT TOP | P-Rel               |
| HDR          | 980-<br>9I117-<br>00H030 | MFA7U1<br>0-H030         | MELLANOX ACTIVE FIBER SPLITTER CABLE, IB<br>HDR, 400GB/S TO 2X200GB/S, OSFP TO<br>2XQSFP56, 30M      | P-Rel               |
| HDR          | 980-<br>9I118-<br>00H030 | MFA7U1<br>0-H030-<br>FLT | MELLANOX AOC SPLITTER, IB TWIN PORT HDR,<br>400GB/S TO 2X200GB/S, OSFP TO 2XQSFP56,<br>30M, FLAT TOP | P-Rel               |
| NDR          | 980-<br>9173U-<br>000003 | MFP7E1<br>0-N003         | NVIDIA PASSIVE FIBER CABLE, MMF, MPO12 APC<br>TO MPO12 APC, 3M                                       | MP                  |
| NDR          | 980-<br>9I559-<br>00N002 | MFP7E3<br>0-N002         | NVIDIA PASSIVE FIBER CABLE, SMF MPO12 APC<br>TO MPO12 APC, 2M  | MP                  |
| NDR          | 980-<br>9I55A-<br>00N003 | MFP7E3<br>0-N003         | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO MPO12 APC, 3M                                       | MP                  |
| NDR          | 980-<br>9I55B-<br>00N005 | MFP7E3<br>0-N005         | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO MPO12 APC, 5M                                       | MP                  |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN          | Description  | Lifecycl<br>e Phase |
|--------------|--------------------------|------------------------|--|---------------------|
| NDR          | 980-<br>9I58J-<br>00N150 | MFP7E3<br>0-N150       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO MPO12 APC, 150M                                     | MP                  |
| NDR          | 980-<br>9I58K-<br>00N003 | MFP7E4<br>0-N003       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO 2XMPO12 APC, 3M                                     | MP                  |
| NDR          | 980-<br>9I58M-<br>00N007 | MFP7E4<br>0-N007       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO 2XMPO12 APC, 7M                                     | MP                  |
| NDR          | 980-<br>9I58N-<br>00N010 | MFP7E4<br>0-N010       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO 2XMPO12 APC, 10M                                    | MP                  |
| NDR          | 980-<br>9I56O-<br>00N015 | MFP7E4<br>0-N015       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO 2XMPO12 APC, 15M                                    | MP                  |
| NDR          | 980-<br>9I56P-<br>00N020 | MFP7E4<br>0-N020       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO 2XMPO12 APC, 20M                                    | MP                  |
| NDR          | 980-<br>9I56Q-<br>00N030 | MFP7E4<br>0-N030       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO 2XMPO12 APC, 30M                                    | MP                  |
| NDR          | 980-<br>9I56R-<br>000050 | MFP7E4<br>0-N050       | NVIDIA PASSIVE FIBER CABLE, SMF, MPO12 APC<br>TO 2XMPO12 APC, 50M                                    | MP                  |
| NDR          | 980-<br>9I51A-<br>00NS00 | MMA4Z<br>00-NS-<br>FLT | NVIDIA TWIN PORT TRANSCEIVER,<br>800GBPS,2XNDR, OSFP, 2XMPO12 APC, 850NM<br>MMF, UP TO 50M, FLAT TOP | MP                  |
| NDR          | 980-<br>9I51S-<br>00NS00 | MMA4Z<br>00-<br>NS400  | NVIDIA SINGLE PORT TRANSCEIVER,<br>400GBPS,NDR, OSFP, MPO12 APC, 850NM MMF,<br>UP TO 50M, FLAT TOP   | MP                  |
| NDR          | 980-<br>9I30G-<br>00NM00 | MMS4X0<br>0-NM         | NVIDIA TWIN PORT TRANSCEIVER,<br>800GBPS,2XNDR, OSFP, 2XMPO, 1310NM SMF, UP<br>TO 500M, FINNED       | MP                  |

| Data<br>Rate | NVIDIA<br>P/N            | Legacy<br>OPN          | Description   | Lifecycl<br>e Phase |
|--------------|--------------------------|------------------------|---|---------------------|
| NDR          | 980-<br>9I301-<br>00NM00 | MMS4X0<br>0-NM-<br>FLT | NVIDIA TWIN PORT TRANSCEIVER,<br>800GBPS,2XNDR, OSFP, 2XMPO 12 APC, 1310NM<br>SMF, UP TO 500M, FLAT TOP       | P-Rel               |
| NDR          | 980-<br>9I30H-<br>00NM00 | MMS4X0<br>0-NS         | NVIDIA TWIN PORT TRANSCEIVER,<br>800GBPS,2XNDR, OSFP, 2XMPO12 APC, 1310NM<br>SMF, UP TO 100M, FINNED          | MP                  |
| NDR          | 980-<br>9I30I-<br>00NM00 | MMS4X0<br>0-NS-FLT     | NVIDIA TWIN PORT TRANSCEIVER,<br>800GBPS,2XNDR, OSFP, 2XMPO, 1310NM SMF, UP<br>TO 100M, FLAT TOP              | MP                  |
| NDR          | 980-<br>9I31N-<br>00NM00 | MMS4X0<br>0-NS400      | NVIDIA SINGLE PORT TRANSCEIVER,<br>400GBPS,NDR, OSFP, MPO12 APC, 1310NM SMF,<br>UP TO 100M, FLAT TOP          | MP                  |
| NDR          | 980-<br>9I30L-<br>00N000 | MMS4X5<br>0-NM         | NVIDIA HYDRA TWIN PORT TRANSCEIVER,<br>800GBPS,2XFR4, 2XNDR, OSFP, 2XMINI-LC,<br>1310NM SMF, UP TO 2K, FINNED | P-Rel               |
| NDR          | 980-<br>9I46U-<br>00N02A | MCP7Y5<br>0-N02A       | NVIDIA passive copper splitter cable, IB twin port NDR 800Gb/s to 4x200Gb/s, OSFP to 4xOSFP, 2.5m             | P-Rel               |

## **Validated and Supported HDR Cables**

| Data<br>Rate | NVIDIA P/N            | Legacy<br>OPN      | Description   | Lifecycle<br>Phase |
|--------------|-----------------------|--------------------|---|--------------------|
| HDR          | 980-9186N-<br>00H003* | MCA1J00-<br>H003E* | NVIDIA Active Copper cable, IB HDR, up to 200Gb/s, QSFP56, 3m, yellow pulltab | EOL [MP]           |
| HDR          | 980-9186O-<br>00H004* | MCA1J00-<br>H004E* | NVIDIA Active Copper cable, IB HDR, up to 200Gb/s, QSFP56, 4m, yellow pulltab | EOL [MP]           |
| HDR          | 980-91977-<br>00H003* | MCA7J50-<br>H003R* | NVIDIA Active copper hybrid cable, IB<br>HDR 200Gb/s to 2xHDR100 100Gb/s,     | EOL [MP]           |

| Data<br>Rate | NVIDIA P/N            | Legacy<br>OPN       | Description  | Lifecycle<br>Phase     |
|--------------|-----------------------|---------------------|--|------------------------|
|              |                       |                     | QSFP56 to 2xQSFP56, 3m, colored  |                        |
| HDR          | 980-91978-<br>00H004* | MCA7J50-<br>H004R*  | NVIDIA Active copper hybrid cable, IB<br>HDR 200Gb/s to 2xHDR100 100Gb/s,<br>QSFP56 to 2xQSFP56, 4m, colored | EOL [MP]               |
| HDR          | 980-91979-<br>00H005  | MCA7J50-<br>H005R   | NVIDIA Active copper hybrid cable, IB<br>HDR 200Gb/s to 2xHDR100 100Gb/s,<br>QSFP56 to 2xQSFP56, 5m, colored | EOL<br>[Prototyp<br>e] |
| HDR          | 980-9I548-<br>00H001  | MCP1650-<br>H001E30 | Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 1m   | HVM                    |
| HDR          | 980-9I549-<br>00H002  | MCP1650-<br>H002E26 | Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 2m   | HVM                    |
| HDR          | 980-9I54A-<br>00H00A  | MCP1650-<br>H00AE30 | Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 0.5m   | HVM                    |
| HDR          | 980-9I54B-<br>00H01A  | MCP1650-<br>H01AE30 | Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 1.5 m  | HVM                    |
| HDR          | 980-9I39E-<br>00H001  | MCP7H50<br>-H001R30 | Nvidia Passive copper splitter cable,<br>200Gbps to 2x100Gbps, QSFP56 to<br>2xQSFP56, 1m                     | HVM                    |
| HDR          | 980-9199F-<br>00H002  | MCP7H50<br>-H002R26 | Nvidia Passive copper splitter cable,<br>200Gbps to 2x100Gbps, QSFP56 to<br>2xQSFP56, 2m                     | HVM                    |
| HDR          | 980-9I98G-<br>00H01A  | MCP7H50<br>-H01AR30 | Nvidia Passive copper splitter cable,<br>200Gbps to 2x100Gbps, QSFP56 to<br>2xQSFP56, 1.5m                   | HVM                    |
| HDR          | 980-9I46K-<br>00H001  | MCP7Y60-<br>H001    | NVIDIA passive copper splitter cable,<br>400(2x200)Gbps to 2x200Gbps, OSFP to<br>2xQSFP56, 1m, fin to flat   | MP                     |
| HDR          | 980-9I46L-<br>00H002  | MCP7Y60-<br>H002    | NVIDIA passive copper splitter cable,<br>400(2x200)Gbps to 2x200Gbps, OSFP to<br>2xQSFP56, 2m, fin to flat   | MP                     |
| HDR          | 980-9I93M-<br>00H01A  | MCP7Y60-<br>H01A    | NVIDIA passive copper splitter cable, 400(2x200)Gbps to 2x200Gbps, OSFP to                                   | MP                     |

| Data<br>Rate | NVIDIA P/N           | Legacy<br>OPN    | Description  | Lifecycle<br>Phase |
|--------------|----------------------|------------------|--|--------------------|
|              |                      |                  | 2xQSFP56, 1.5m, fin to flat  |                    |
| HDR          | 980-9193N-<br>00H001 | MCP7Y70-<br>H001 | NVIDIA passive copper splitter cable,<br>400(2x200)Gbps to 4x100Gbps, OSFP to<br>4xQSFP56, 1m, fin to flat   | MP                 |
| HDR          | 980-9193O-<br>00H002 | MCP7Y70-<br>H002 | NVIDIA passive copper splitter cable,<br>400(2x200)Gbps to 4x100Gbps, OSFP to<br>4xQSFP56, 2m, fin to flat   | MP                 |
| HDR          | 980-9I47P-<br>00H01A | MCP7Y70-<br>H01A | NVIDIA passive copper splitter cable,<br>400(2x200)Gbps to 4x100Gbps, OSFP to<br>4xQSFP56, 1.5m, fin to flat | MP                 |
| HDR          | 980-9I41X-<br>00H003 | MFA7U10<br>-H003 | NVIDIA AOC splitter, IB twin port HDR,<br>400Gb/s to 2x200Gb/s, OSFP to<br>2xQSFP56, 3m                      | P-Rel              |
| HDR          | 980-9I11Z-<br>00H005 | MFA7U10<br>-H005 | NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 5m                            | P-Rel              |
| HDR          | 980-9I111-<br>00H010 | MFA7U10<br>-H010 | NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 10m                           | P-Rel              |
| HDR          | 980-9I113-<br>00H015 | MFA7U10<br>-H015 | NVIDIA AOC splitter, IB twin port HDR, 400Gb/s to 2x200Gb/s, OSFP to 2xQSFP56, 15m                           | P-Rel              |
| HDR          | 980-9I115-<br>00H020 | MFA7U10<br>-H020 | NVIDIA AOC splitter, IB twin port HDR,<br>400Gb/s to 2x200Gb/s, OSFP to<br>2xQSFP56, 20m                     | P-Rel              |
| HDR          | 980-9I117-<br>00H030 | MFA7U10<br>-H030 | NVIDIA AOC splitter, IB twin port HDR,<br>400Gb/s to 2x200Gb/s, OSFP to<br>2xQSFP56, 30m                     | P-Rel              |
| HDR          | 980-9I11V-<br>00H050 | MFA7U10<br>-H050 | NVIDIA AOC splitter, IB twin port HDR,<br>400Gb/s to 2x200Gb/s, OSFP to<br>2xQSFP56, 50m                     | Prototype          |

| Data<br>Rate | NVIDIA P/N           | Legacy<br>OPN        | Description   | Lifecycle<br>Phase |
|--------------|----------------------|----------------------|---|--------------------|
| HDR          | 980-9I124-<br>00H003 | MFS1S00-<br>H003E    | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 3m               | EOL<br>[HVM]       |
| HDR          | 980-9I123-<br>00H003 | MFS1S00-<br>H003-LL  | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 3m  | EOL [P-<br>Rel]    |
| HDR          | 980-9I457-<br>00H003 | MFS1S00-<br>H003V    | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 3m                               | MP                 |
| HDR          | 980-9I45A-<br>00H005 | MFS1S00-<br>H005E    | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 5m               | EOL<br>[HVM]       |
| HDR          | 980-91449-<br>00H005 | MFS1S00-<br>H005-LL  | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 5m  | EOL [P-<br>Rel]    |
| HDR          | 980-9I45D-<br>00H005 | MFS1S00-<br>H005V    | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 5m                               | MP                 |
| HDR          | 980-9I45G-<br>00H010 | MFS1S00-<br>H010E    | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 10m              | EOL<br>[HVM]       |
| HDR          | 980-9I45H-<br>00H010 | MFS1S00-<br>H010E_FF | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 10m              | EOL<br>[HVM]       |
| HDR          | 980-9I44F-<br>00H010 | MFS1S00-<br>H010-LL  | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 10m | EOL [P-<br>Rel]    |
| HDR          | 980-9I45J-<br>00H010 | MFS1S00-<br>H010V    | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 10m                              | MP                 |
| HDR          | 980-9I45M-<br>00H015 | MFS1S00-<br>H015E    | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 15m              | EOL<br>[HVM]       |
| HDR          | 980-9I44L-<br>00H015 | MFS1S00-<br>H015-LL  | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab,                  | EOL [P-<br>Rel]    |

| Data<br>Rate | NVIDIA P/N           | Legacy<br>OPN       | Description   | Lifecycle<br>Phase |
|--------------|----------------------|---------------------|---|--------------------|
|              |                      |                     | low latency, 15m  |                    |
| HDR          | 980-9I45O-<br>00H015 | MFS1S00-<br>H015V   | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 15m                              | MP                 |
| HDR          | 980-9I45R-<br>00H020 | MFS1S00-<br>H020E   | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 20m              | EOL<br>[HVM]       |
| HDR          | 980-9I44Q-<br>00H020 | MFS1S00-<br>H020-LL | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 20m | EOL [P-<br>Rel]    |
| HDR          | 980-9I45T-<br>00H020 | MFS1S00-<br>H020V   | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 20m                              | MP                 |
| HDR          | 980-9I45Y-<br>00H030 | MFS1S00-<br>H030E   | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 30m              | EOL<br>[HVM]       |
| HDR          | 980-9I45X-<br>00H030 | MFS1S00-<br>H030-LL | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 30m | EOL [P-<br>Rel]    |
| HDR          | 980-9I440-<br>00H030 | MFS1S00-<br>H030V   | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 30m                              | MP                 |
| HDR          | 980-9I455-<br>00H050 | MFS1S00-<br>H050E   | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 50m              | EOL<br>[HVM]       |
| HDR          | 980-9I447-<br>00H050 | MFS1S00-<br>H050V   | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 50m                              | MP                 |
| HDR          | 980-9I44G-<br>00H100 | MFS1S00-<br>H100E   | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 100m             | EOL<br>[HVM]       |
| HDR          | 980-9I44H-<br>00H100 | MFS1S00-<br>H100V   | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 100m                             | MP                 |
| HDR          | 980-9I44I-<br>00H130 | MFS1S00-<br>H130E   | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 130m             | EOL<br>[HVM]       |

| Data<br>Rate | NVIDIA P/N           | Legacy<br>OPN     | Description  | Lifecycle<br>Phase |
|--------------|----------------------|-------------------|--|--------------------|
| HDR          | 980-9I44K-<br>00H130 | MFS1S00-<br>H130V | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 130m                                    | MP                 |
| HDR          | 980-9I45L-<br>00H150 | MFS1S00-<br>H150E | NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 150m                    | EOL<br>[HVM]       |
| HDR          | 980-9I44N-<br>00H150 | MFS1S00-<br>H150V | Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 150m                                    | MP                 |
| HDR          | 980-9I452-<br>00H003 | MFS1S50-<br>H003E | NVIDIA active fiber splitter cable, IB<br>HDR, 200Gb/s to 2x100Gb/s, QSFP56 to<br>2xQSFP56 , LSZH, 3m  | EOL<br>[HVM]       |
| HDR          | 980-91445-<br>00H003 | MFS1S50-<br>H003V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 3m             | HVM                |
| HDR          | 980-91956-<br>00H005 | MFS1S50-<br>H005E | NVIDIA active fiber splitter cable, IB<br>HDR, 200Gb/s to 2x100Gb/s, QSFP56 to<br>2xQSFP56 , LSZH, 5m  | EOL<br>[HVM]       |
| HDR          | 980-91969-<br>00H005 | MFS1S50-<br>H005V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 5m             | HVM                |
| HDR          | 980-9I95A-<br>00H010 | MFS1S50-<br>H010E | NVIDIA active fiber splitter cable, IB<br>HDR, 200Gb/s to 2x100Gb/s, QSFP56 to<br>2xQSFP56 , LSZH, 10m | EOL<br>[HVM]       |
| HDR          | 980-9I96D-<br>00H010 | MFS1S50-<br>H010V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 10m            | HVM                |
| HDR          | 980-9I95E-<br>00H015 | MFS1S50-<br>H015E | NVIDIA active fiber splitter cable, IB<br>HDR, 200Gb/s to 2x100Gb/s, QSFP56 to<br>2xQSFP56 , LSZH, 15m | EOL<br>[HVM]       |
| HDR          | 980-9I96H-<br>00H015 | MFS1S50-<br>H015V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 15m            | HVM                |

| Data<br>Rate | NVIDIA P/N           | Legacy<br>OPN     | Description  | Lifecycle<br>Phase |
|--------------|----------------------|-------------------|--|--------------------|
| HDR          | 980-91951-<br>00H020 | MFS1S50-<br>H020E | NVIDIA active fiber splitter cable, IB<br>HDR, 200Gb/s to 2x100Gb/s, QSFP56 to<br>2xQSFP56 , LSZH, 20m     | EOL<br>[HVM]       |
| HDR          | 980-9196L-<br>00H020 | MFS1S50-<br>H020V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 20m                | HVM                |
| HDR          | 980-9195M-<br>00H030 | MFS1S50-<br>H030E | NVIDIA active fiber splitter cable, IB<br>HDR, 200Gb/s to 2x100Gb/s, QSFP56 to<br>2xQSFP56 , LSZH, 30m     | EOL<br>[HVM]       |
| HDR          | 980-9196P-<br>00H030 | MFS1S50-<br>H030V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 30m                | HVM                |
| HDR          | 980-9195S-<br>00H040 | MFS1S50-<br>H040V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 40m                | Prototype          |
| HDR          | 980-9195T-<br>00H050 | MFS1S50-<br>H050V | Nvidia active optical splitter cable,<br>200Gbps to 2x100Gbps , QSFP56 to 2x<br>QSFP56, 50m                | Prototype          |
| HDR          | 980-9195Z-<br>00H003 | MFS1S90-<br>H003E | NVIDIA active fiber splitter cable, IB<br>HDR, 2x200Gb/s to 2x200Gb/s,<br>2xQSFP56 to 2xQSFP56 , LSZH, 3m  | EOL<br>[HVM]       |
| HDR          | 980-91960-<br>00H005 | MFS1S90-<br>H005E | NVIDIA active fiber splitter cable, IB<br>HDR, 2x200Gb/s to 2x200Gb/s,<br>2xQSFP56 to 2xQSFP56 , LSZH, 5m  | EOL<br>[HVM]       |
| HDR          | 980-9I961-<br>00H010 | MFS1S90-<br>H010E | NVIDIA active fiber splitter cable, IB<br>HDR, 2x200Gb/s to 2x200Gb/s,<br>2xQSFP56 to 2xQSFP56 , LSZH, 10m | LTB<br>[HVM]       |
| HDR          | 980-9I962-<br>00H015 | MFS1S90-<br>H015E | NVIDIA active fiber splitter cable, IB<br>HDR, 2x200Gb/s to 2x200Gb/s,<br>2xQSFP56 to 2xQSFP56 , LSZH, 15m | EOL<br>[HVM]       |
| HDR          | 980-91423-<br>00H020 | MFS1S90-<br>H020E | NVIDIA active fiber splitter cable, IB<br>HDR, 2x200Gb/s to 2x200Gb/s,<br>2xQSFP56 to 2xQSFP56 , LSZH, 20m | LTB<br>[HVM]       |

| Data<br>Rate | NVIDIA P/N            | Legacy<br>OPN     | Description  | Lifecycle<br>Phase |
|--------------|-----------------------|-------------------|--|--------------------|
| HDR          | 980-91424-<br>00H030  | MFS1S90-<br>H030E | NVIDIA active fiber splitter cable, IB<br>HDR, 2x200Gb/s to 2x200Gb/s,<br>2xQSFP56 to 2xQSFP56 , LSZH, 30m | EOL<br>[HVM]       |
| HDR          | 980-9I17S-<br>00HS00  | MMA1T00<br>-HS    | NVIDIA transceiver, HDR, QSFP56, MPO, 850nm, SR4, up to 100m   | HVM                |
| HDR          | 980-91055-<br>00H000* | MMS1W5<br>0-HM*   | NVIDIA transceiver, IB HDR, up to 200Gb/s, QSFP56, LC-LC, 1310nm, FR4                                      | MP                 |

### Firmware Upgrade

Firmware upgrade may be performed directly from any previous version to this version. To upgrade firmware, please refer to the NVIDIA Firmware Tools (MFT) package at <a href="mailto:network.nvidia.com/products/adapter-software/firmware-tools/">network.nvidia.com/products/adapter-software/firmware-tools/</a>

#### **PRM Revision Compatibility**

This firmware version complies with the NVIDIA Switches Programmer's Reference Manual (PRM), Rev 1.40 or later.

# **Changes and New Features**

| Keyword   | Description            |  |
|---|------------------------|--|
|   | 31.2012.4036           |  |
| Counters  Added support for Vendor Specific General Service CI Extended PortVLXmitTimeCong counters (64 bit counters) |                        |  |
| Bug Fixes   | See <u>Bug Fixes</u> . |  |

# **Bug Fixes in this Firmware Version**

For a list of bug fixed from previous versions, see **Bug Fixes History**.

| Internal Ref. | Issues  |
|---------------|---|
|               | <b>Description:</b> Sending pFRN packets to ports that were connected to themselves (loop), caused the switch hanged due to semaphore lock mismatch.  |
| 3864399       | Keywords: pFRN  |
|               | Discovered in Version: 31.2012.2108   |
|               | <b>Fixed in Version:</b> 31.2012.4036   |
|               | <b>Description:</b> Incorrect buffer configuration for trap and mirror packets may cause the switch data path to become stuck, potentially resulting in buffer overrun and internal credit leakage. |
| 3860421       | Keywords: Data Path, Buffer   |
|               | Discovered in Version: 31.2012.2108   |
|               | <b>Fixed in Version:</b> 31.2012.4036   |
|               | <b>Description:</b> Updated the ratio between the amount of data sent (TX) to FCCL packets to be determined by the credit size; credit packet will be sent after roughly 4096B of sent data.        |
| 3700332       | Keywords: FCCL, Credit Packet   |
|               | Discovered in Version: 31.2012.2014   |
|               | Fixed in Version: 31.2012.4006  |
| 3657798       | <b>Description:</b> During the transition from 2x split to 4x split port configuration, the procedure for clearing routing logic for management ports was incorrect.                                |

| Internal Ref. | Issues  |  |  |
|---------------|---|--|--|
|               | Keywords: Management Ports, Split   |  |  |
|               | Discovered in Version: 31.2012.3008   |  |  |
|               | <b>Fixed in Version:</b> 31.2012.4006   |  |  |
| 3753126       | <b>Description:</b> Interference on the AC line may cause a wrong reset sequence, which leads to a system error and causes all ports to be in link-down mode. Only a power cycle restores the switch to a normal operation mode. For CPLD Upgrade please contact NVIDIA technical support team. Fixes available in Main CPLD revision 0300 and Port CPLD revision 0700 or higher. |  |  |
|               | Keywords: CPLD  |  |  |
|               | Discovered in Version: 31.2012.2108   |  |  |
|               | Fixed in Version: 31.2012.4006  |  |  |
|               | <b>Description:</b> Because of a race condition in cable information interfaces, the lock bit remains stuck in the high position. Consequently, a timeout flow has been implemented to address this scenario.   |  |  |
| 3715407       | Keywords: Cables, Timeout   |  |  |
|               | Discovered in Version: 31.2012.2014   |  |  |
|               | <b>Fixed in Version:</b> 31.2012.4006   |  |  |
|               | <b>Description:</b> PSU fan error is experienced. To address this issue, the PSU fans high RPM warning threshold has been increased to 32500.   |  |  |
| 3740969       | Keywords: PSU Fan   |  |  |
|               | Discovered in Version: 31.2012.2108   |  |  |
|               | <b>Fixed in Version:</b> 31.2012.4006   |  |  |
|               | <b>Description:</b> HBF (Hash Based Forwarding) configurations were applied on the incorrect port.  |  |  |
| 3773771       | <b>Keywords:</b> Hash Based Forwarding  |  |  |
|               | Discovered in Version: 31.2012.3008   |  |  |
|               | <b>Fixed in Version:</b> 31.2012.4006   |  |  |

| Internal Ref. | Issues  |
|---------------|---|
|               | <b>Description:</b> In cases where buffer is overutilized, failure to allocate a buffer for the SHARP TX retry buffer may lead to buffer oversubscription.  |
| 3707400       | Keywords: SHARP, Buffer   |
|               | Discovered in Version: 31.2012.2108   |
|               | <b>Fixed in Version:</b> 31.2012.4006   |
|               | <b>Description:</b> The PRBS register could not be properly disabled. To solve this, the Ethernet FSM closes upon receiving an Enable event without a lock only when the FSM is already in the Close state.                         |
| 3724050       | Keywords: Ethernet FSM, PRBS  |
|               | Discovered in Version: 31.2012.2014   |
|               | <b>Fixed in Version:</b> 31.2012.4006   |
| 3824931       | <b>Description:</b> Illegal packets of a permissive LID (0xFFFF) and VL other than 15 were incorrectly configured to destined for port 0 and be proceeded by the switch firmware, which lead to overloading of the switch firmware. |
| 3843040       | Keywords: Checks  |
|               | Discovered in Version: 31.2012.2108   |
|               | <b>Fixed in Version:</b> 31.2012.4006   |

# **Known Issues**

The following sections describe known issues in firmware releases and possible workarounds.

| Internal Ref | Issue  |  |
|--------------|--|--|
|              | <b>Description:</b> Following a module reset, link up may take up to 70 seconds.   |  |
| 3813780      | Workaround: N/A  |  |
|              | Keywords: Module Rest, Link Up   |  |
|              | Discovered in Version: 31.2012.4006  |  |
|              | <b>Description:</b> When performing a partial split on an optical module, port may not link up.  |  |
| 3778631      | <b>Workaround:</b> Conduct PAOS down and then PAOS up on the port.   |  |
|              | Keywords: Optical Module, Partial Split  |  |
|              | Discovered in Version: 31.2012.4006  |  |
|              | <b>Description:</b> When using HDR speeds on NDR systems with NDR optic cables, the link does not raise.   |  |
| 3198634      | Workaround: N/A  |  |
|              | Keywords: HDR Speed, Optic Cables  |  |
|              | Discovered in Version: 31.2012.1024  |  |
| 3605188      | <b>Description:</b> Occasionally, on managed switch systems with FR4 coherent optical module, after running the "split- unsplit" flow or enabling L3 routing, the port may not raise a link. |  |
|              | <b>Workaround:</b> To get the link up on the FR4, perform SW reset to the switch   |  |
|              | <b>Keywords:</b> FR4 coherent optical module   |  |

| Internal Ref | Issue   |  |
|--------------|---|--|
|              | Discovered in Version: 31.2012.1024   |  |
|              | <b>Description:</b> GMP MADs with GRH header towards the Router are not supported.  |  |
| 3323781      | Workaround: N/A   |  |
|              | Keywords: Routers   |  |
|              | Discovered in Version: 31.2010.5042   |  |
|              | <b>Description:</b> In some cases, MMS4X00-NL1.2 may have low BER.  |  |
| 2922333      | Workaround: N/A   |  |
| 2922333      | <b>Keywords:</b> Signal Integrity   |  |
|              | Discovered in Version: 31.2010.2110   |  |
|              | <b>Description:</b> Using NDR speed with Optical Transceivers causes bandwidth to be 350Gb/s instead of 400Gb/s in small packets. |  |
| 2838195      | Workaround: N/A   |  |
|              | <b>Keywords:</b> Optical Transceivers   |  |
|              | Discovered in Version: 31.2010.1310   |  |
|              | <b>Description:</b> When using Optical Transceiver, toggling a port in a cage may toggle the adjacent port in the cage.           |  |
| 2834238      | Workaround: N/A   |  |
|              | <b>Keywords:</b> Optical Transceivers, Port Toggle  |  |
|              | Discovered in Version: 31.2010.1310   |  |
|              | <b>Description:</b> VL_HIGH_LIMIT is not affecting the VL arbiter as expected.  |  |
| 955641       | <b>Workaround:</b> Arbitration table should be set using only the low priority VL arbitration table.                              |  |
|              | <b>Keywords:</b> VL Arbitration   |  |
|              | Discovered in Version: 31.2010.1310   |  |
| 1249608      | <b>Description:</b> Configuring weight "0" for VL, results in unexpected behavior.  |  |

| Internal Ref | Issue   |  |
|--------------|---|--|
|              | <b>Workaround:</b> Arbitration table should be configured with weights other than "0".  |  |
|              | Keywords: VL Arbitration  |  |
|              | Discovered in Version: 31.2010.1310   |  |
|              | <b>Description:</b> Congestion profiles in VS-MAD PortProfileSetting support only fixed mode. Percentage mode is not supported. |  |
| 2057793      | Workaround: N/A   |  |
|              | Keywords: InfiniBand Congestion Control   |  |
|              | Discovered in Version: 31.2010.1310   |  |

# **Changes and New Features History**

This section includes history of changes and new feature of three major releases back. For older versions' history, please refer to their dedicated release notes.

| Keyword    | Description  |  |
|------------|--|--|
|            | 31.2012.3040   |  |
| General    | Bug Fixes.   |  |
|            | 31.2012.3008   |  |
| General    | Bug Fixes.   |  |
|            | 31.2012.2108   |  |
| General    | Bug Fixes.   |  |
|            | 31.2012.2014   |  |
| Device HW  | Added Beta-level support for MMS4X50-NM module.  |  |
| SHARP      | Added support for gateway Dump. The new Dump file contains SHARP gateway content.  |  |
| Counters   | Added PPCNT new group 0x26 and a new page 241 for Diagnostic Data VS MAD.  |  |
| Counters   | Added support for two performance class MADs : 1. PortXmitConCtrl 2. PortVLXmitTimeCong  |  |
| Resiliency | Added a mechanism to release the packet interface when a long command packet handling timeout is detected to reduce firmware freeze. |  |
| Telemetry  | Added support for Fast Recovery notifications of the OS entity in the network.   |  |

| Keyword | Description            |
|---------|------------------------|
| General | See <u>Bug fixes</u> . |

| Keyword   | Description   |
|-----------|---|
|           | 31.2012.1068  |
|           | Added limited support for MMS4X50-NM module.  |
| Device HW | (i) <b>Note</b> When working with two 4xports, toggling one port following the other port might lead to link flap issue. To recover, toggle the port again. |
| General   | See <u>Bug fixes</u> .  |

| Keyword                                       | Description  |  |
|---|--|--|
|   | 31.2012.1024   |  |
| Fast Recovery from<br>Unhealthy Links         | Added support of the fast recovery from unhealthy links including BER monitor and credit watchdog. |  |
| Fast Recovery<br>Notifications Towards<br>UFM | Added support for notifications of Fast Recovery towards UFM entity (collector) in the network.    |  |
| General                                       | See <u>Bug fixes</u> .   |  |

| Keyword      | Description            |  |
|--------------|------------------------|--|
| 31.2010.6102 |                        |  |
| General      | See <u>Bug fixes</u> . |  |

| Keyword                                    | Description   |  |  |
|--|---|--|--|
|  | 31.2010.6064  |  |  |
| Counters: Unhealthy<br>Link                | Added BER Monitor counters for unhealthy link.  |  |  |
| SHARP-Based Reliable<br>Multicast Packets  | Added support for SHARP-based reliable multicast packets.                                       |  |  |
| Additional Status in Each MAD              | Added support of <i>AdditionalStatus</i> in MAD packets.  |  |  |
| Mirroring Congested<br>Packets Towards UFM | Added support for mirroring of congested packets towards UFM entity (collector) in the network. |  |  |
| General                                    | See <u>Bug fixes</u> .  |  |  |

| Keyword      | Description  |  |
|--------------|--|--|
| 31.2010.5108 |  |  |
| IB Router    | Added support for NDR InfiniBand Router which enables isolation and connectivity between up to eight different InfiniBand subnets. The IB Router enables features such as Adaptive Routing (AR), Hash Based Forwarding (HBF), and Self-Healing Interconnect Enhancement for InteLigent Datacenters (SHIELD). |  |
| General      | See <u>Bug fixes</u> .   |  |

| Keyword                         | Description  |  |
|---------------------------------|--|--|
| 31.2010.5002                    |  |  |
| SHARP SAT Reliable<br>Multicast | Added e ngineering-sample- level support for RMC request (SHARP SAT opcode 0xA) and RMC response (SHARP SAT opcode 0xB). |  |
| General                         | See <u>Bug fixes</u> .   |  |

| Keyword      | Description             |  |
|--------------|-------------------------|--|
| 31.2010.4102 |                         |  |
| General      | Stability improvements. |  |
| General      | See <u>Bug fixes</u> .  |  |

| Keyword                       | Description   |  |  |
|-------------------------------|---|--|--|
|                               | 31.2010.4010  |  |  |
| pFRN Collector                | Added support for mirroring of PFRN packets towards UFM entity (collector) in the subnet. |  |  |
| PKEY Filter for<br>Multicast  | Added support for MulticastPKeyTrapSuppression (PKEY mismatch filtering).                 |  |  |
| Congestion Control<br>Updates | Added support for 1kb granularity for the port congestion profiles.                       |  |  |
| SL-to-VL Mapping              | Added switch support for port mask optimization of SL-to-VL Mapping Table configuration.  |  |  |
| General                       | See <u>Bug fixes</u> .  |  |  |

| Keyword            | Description  |  |
|--------------------|--|--|
| 31.2010.3118       |  |  |
| Hash-Based Routing | Enabled the reordering of sensitive traffic to load balance on multiple ports by using Hash-Based Routing. |  |
| General            | See <u>Bug fixes</u> .   |  |

| Keyword      | Description   |
|--------------|---|
| 31.2010.3004 |   |
| Counters     | Added support for PortXmitWaitVLExtended counters.  |
| pFRN         | Added support for pFRN (Proactive Fault Routing Notification) which allows for dynamic link failure detection and route correction for topologies based on Adaptive Routing |
| General      | See <u>Bug fixes</u> .  |

| Keyword      | Description   |
|--------------|---|
| 31.2010.2300 |   |
| SHARPv3      | Added GA-level support for aggregation jobs to run over parallel links. |
| General      | Bug fixes.  |

| Keyword      | Description   |  |
|--------------|---|--|
| 31.2010.2246 |   |  |
| SHARPv3      | Added beta-level support for aggregation jobs to run over parallel links. |  |
| General      | Bug fixes.  |  |

| Keyword      | Description  |  |
|--------------|--|--|
| 31.2010.2110 |  |  |
| SHARPv3      | Added GA-level support for SHARPv3 on Quantum-2 systems. |  |
| General      | Bug fixes.   |  |

| Keyword                  | Description   |  |
|--------------------------|---|--|
| 31.2010.2036             |   |  |
| Systems                  | Added power and system monitoring optimizations.                        |  |
| SHARP V3                 | Added beta-level support for SHARP V3 on Quantum-2 systems.             |  |
| Hash Based<br>Forwarding | Added alpha-level support for Hash Based Forwarding Routing Capability. |  |
| Security                 | Added security enhancements to QM9790 system.                           |  |

| Keyword      | Description  |  |
|--------------|--|--|
| 31.2010.1310 |  |  |
| Systems      | Added GA-level support for NVIDIA Quantum-2-based switch QM9790. |  |

| Keyword   | Description   |
|---|---|
| Congestion Control  | Added ES-level support for congestion control class key.                                |
| Vendor Key  | Added ES-level support for vendor class key.  |
| Hierarchy Information   | Added support for Hierarchy Information mad.  |
| Remote Debug Token  | Added support for Remote Debug Token.   |
| NVIDIA® Scalable<br>Hierarchical<br>Aggregation and<br>Reduction Protocol<br>(SHARP)™ | Added GA-level support for SHARPv2 for NVIDIA Quantum-2 systems.                        |
| Counters  | Added support for PortVLXmitFlowCtlUpdateErrors counters.                               |
| Security  | Added support for Secure Firmware and Secure Firmware Boot on NVIDIA Quantum-2 systems. |

## **Bug Fixes History**

| Internal Ref. | Issues  |
|---------------|---|
| 3742609       | <b>Description:</b> Fix split mode LEDs wrong endian mapping issue. |
|               | Keywords: LED   |
|               | Discovered in Version: 31.2012.1068                                 |
|               | <b>Fixed in Version:</b> 31.2012.3040                               |
| 3725809       | <b>Description:</b> Increased PSU fans high RPM warning threshold.  |
|               | Keywords: Fan, Warning  |
|               | Discovered in Version: 31.2012.2108                                 |
|               | <b>Fixed in Version:</b> 31.2012.3040                               |

| Internal Ref. | Issues  |
|---------------|---|
| 3677817       | <b>Description:</b> Fixed a race condition in the cable info interfaces that caused the switch be non-responsive.                     |
|               | Keywords: Cable info interfaces   |
|               | Discovered in Version: 31.2012.2108   |
|               | Fixed in Version: 31.2012.3008  |
| 3705783       | <b>Description:</b> Fixed the timer for sending Trap-135 to 1 second (in case a host stopped sending packets in the middle of a SAT). |
|               | Keywords: Timer   |
|               | Discovered in Version: 31.2012.2108   |
|               | Fixed in Version: 31.2012.3008  |
| 3669982       | <b>Description:</b> Fixed an issue that caused the SHARP job to fail due to an error event that triggered the firmware dumps.         |
|               | <b>Keywords:</b> SHARP, firmware dumps  |

| Internal Ref.      | Issues   |
|--------------------|--|
|                    | Discovered in Version: 31.2012.2108  |
|                    | <b>Fixed in Version:</b> 31.2012.3008  |
|                    | <b>Description:</b> Fixed credit management of shared buffer scheme that effected the overall bandwidth performance of the switch.                       |
| 3626729<br>3677386 | Keywords: Buffer Scheme  |
| 3077380            | Discovered in Version: 31.2012.2108  |
|                    | Fixed in Version: 31.2012.3008   |
|                    | <b>Description:</b> Fixed the statics issue where the I <sup>2</sup> C to modules is locked and might cause switch to get stuck by resetting the switch. |
| 3600467            | <b>Keywords:</b> I <sup>2</sup> C  |
|                    | Discovered in Version: 31.2012.2108  |
|                    | Fixed in Version: 31.2012.3008   |
| 3719983            | <b>Description:</b> Fixed the packets sent to port0 which might drop due to heavy load, causing MADs timeouts in SM.                                     |
|                    | Keywords: Packets  |
|                    | Discovered in Version: 31.2012.2108  |
|                    | Fixed in Version: 31.2012.3008   |

| Internal Ref. | Issues   |
|---------------|--|
| 3536538       | <b>Description:</b> For mirror agent configured with dynamic port analyzer, configuring linear forwarding table may cause mirror agent enablement and unexpected mirrored packets. |
|               | Keywords: Recovery   |
|               | Discovered in Version: 31.2012.1068  |
|               | <b>Fixed in Version:</b> 31.2012.2014  |

| Internal Ref.      | Issues  |
|--------------------|---|
| 3592659<br>3585886 | <b>Description:</b> Switch may freeze while sending MVCR.   |
|                    | Keywords: MVCR  |
|                    | Discovered in Version: 31.2012.1024   |
|                    | Fixed in Version: 31.2012.1068  |
|                    | <b>Description:</b> Rare issue that triggers the I <sup>2</sup> C to module connection to lock and causes the switch to freeze.   |
| 3589044<br>3587703 | <b>Keywords:</b> I <sup>2</sup> C   |
| 3573164            | Discovered in Version: 31.2012.1024   |
|                    | Fixed in Version: 31.2012.1068  |
|                    | <b>Description:</b> FR4 MMS4X50-NM cable link-up failure after a disconnect or AC cycle.  |
| 3548254            | Keywords: Cables, Link Up   |
|                    | Discovered in Version: 31.2012.1024   |
|                    | Fixed in Version: 31.2012.1068  |
|                    | <b>Description:</b> Fixed SNR value calculation for correct readings from the MMA4Z00 optical cable module.   |
| 3570478            | Keywords: SNR   |
|                    | Discovered in Version: 31.2012.1024   |
|                    | Fixed in Version: 31.2012.1068  |
| 3311198            | <b>Description:</b> Disabled "low priority credits" feature on the switch side that caused the credits mechanism to overload the links with credit packets, reducing the available bandwidth for transmitting data packets on the link. |
| 3586423            | Keywords: Bandwidth   |
|                    | Discovered in Version: 31.2012.1024   |
|                    | Fixed in Version: 31.2012.1068  |

| Internal Ref. | Issues  |
|---------------|---|
| 3554182       | <b>Description:</b> Link does not raise with 2 <sup>nd</sup> source MMS4X00-NS transceivers.  |
|               | Keywords: Cables, Link Up   |
|               | Discovered in Version: 31.2010.6064   |
|               | <b>Fixed in Version:</b> 31.2012.1024   |
|               | <b>Description:</b> The message of code 57 in the PDDR Troubleshooting information page was incorrect.  |
| 3538638       | Keywords: Link Diagnostics  |
|               | Discovered in Version: 31.2010.6064   |
|               | <b>Fixed in Version:</b> 31.2012.1024   |
|               | <b>Description:</b> An unresponsive PSU client can cause the SDA I <sup>2</sup> C line to hang.   |
| 3407038       | <b>Keywords:</b> I <sup>2</sup> C   |
|               | Discovered in Version: 31.2010.6064   |
|               | <b>Fixed in Version:</b> 31.2012.1024   |
|               | <b>Description:</b> Wrong RTT value is exposed under PRTL PRM.  |
| 3477039       | Keywords: Registers, RTT Value  |
| 3477039       | Discovered in Version: 31.2010.6064   |
|               | <b>Fixed in Version:</b> 31.2012.1024   |
|               | <b>Description:</b> When trying to choose the threshold for the Fast Recovery feature (BER Config), it is possible that threshold 0 will be loaded. |
| 3481394       | Keywords: Fast Recovery, BER Configuration  |
|               | Discovered in Version: 31.2010.6064   |
|               | <b>Fixed in Version:</b> 31.2012.1024   |
| 3499997       | <b>Description:</b> In some cases, the combination of SHARP SAT traffic and SHARP MADs can cause the switch to get stuck.                           |
|               | Keywords: SHARP   |

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|               | Discovered in Version: 31.2010.4210  |
|               | <b>Fixed in Version:</b> 31.2012.1024  |
|               | <b>Description:</b> When using ibdiagnet, an incorrect module alarm type was reported. |
| 3451519       | <b>Keywords:</b> ibdiagnet, Module Temperature Alarm Type                              |
|               | Discovered in Version: 31.2010.5108  |
|               | <b>Fixed in Version:</b> 31.2012.1024  |

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|               | <b>Description:</b> Wrap-around of the time_since_last_clear counter caused incorrect reporting of counters on the port.   |
| 3326692       | Keywords: Counters   |
|               | Discovered in Version: 31.2010.3118  |
|               | <b>Fixed in Version:</b> 31.2010.6102  |
| 2200.422      | <b>Description:</b> The flint burning firmware process might take longer than expected, possibly leading to timeouts in SM and logical links drops by the SM, which, in turn, may lead to failure of the flint burn command. |
| 3389432       | <b>Keywords:</b> SM, Timeout, Flint, Failure   |
|               | Discovered in Version: 31.2010.6064  |
|               | <b>Fixed in Version:</b> 31.2010.6102  |
|               | <b>Description:</b> pFRN notification state machine got halted in busywait on all riscs due to inability to free TX credits.   |
| 3339363       | Keywords: pFRN   |
|               | Discovered in Version: 31.2010.3118  |
|               | <b>Fixed in Version:</b> 31.2010.6064  |
| 3393378       | <b>Description:</b> In some cases, pFRN configuration over multi-SWID caused out-of-bound access to an array and overran FLID  |

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|               | configuration.  |
|               | Keywords: pFRN  |
|               | <b>Fixed in Version:</b> 31.2010.6064   |
|               | <b>Description:</b> On rare occasions, the port might get stuck (in all speeds) during the link up flow when using optical modules.   |
| 3342918       | <b>Keywords:</b> Port Link Up, Port Toggling, Optical Modules   |
|               | <b>Fixed in Version:</b> 31.2010.6064   |
|               | <b>Description:</b> Bandwidth is lower than expected on MMS4X00-NL-QP1 cable.   |
| 3395821       | <b>Keywords:</b> MMS4X00-NL-QP1, Bandwidth  |
|               | <b>Fixed in Version:</b> 31.2010.6064   |
|               | <b>Description:</b> After a firmware update failure, the bad image was not erased.  |
| 2824249       | Keywords: Installation, Firmware  |
|               | Discovered in Version: 31.2010.2036   |
|               | <b>Fixed in Version:</b> 31.2010.6064   |
|               | <b>Description:</b> In QM9700 systems, when a transceiver module is plugged in when only one of the optic cables is connected (while the second cable is disconnected), the port LED may be incorrectly displayed on the disconnected side. |
| 3362685       | Keywords: Port LED, Optic Cables  |
|               | Discovered in Version: 31.2010.4102   |
|               | <b>Fixed in Version:</b> 31.2010.5108   |
| 3377608       | <b>Description:</b> When operating in dynamic trees allocation mode, MAD error responses might be received in libsharp.   |
|               | Keywords: sharp_am, libsharp  |
|               | <b>Fixed in Version:</b> 31.2010.5108   |
| 3362200       | <b>Description:</b> In rare cases that involve stress of traffic, unexpected hardware fast path behavior may occur, possibly leading to the   |

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|               | switch firmware hanging when toggling the ports.  |
|               | Keywords: Turbo Path  |
|               | Discovered in Version: 31.2010.5002   |
|               | <b>Fixed in Version:</b> 31.2010.5108   |
|               | <b>Description:</b> The firmware does not return values for the counters "PortSwLifetimeLimitDiscards" and "PortSwHOQLifetimeLimitDiscards". Support has now been added for the counters. |
| 3301825       | Keywords: Counters  |
|               | Discovered in Version: 31.2010.3118   |
|               | <b>Fixed in Version:</b> 31.2010.5042   |
|               | <b>Description:</b> pFRN mirror v1 header pad count showed an invalid padding size.   |
| 3335002       | Keywords: PFRN  |
|               | Discovered in Version: 31.2010.4010   |
|               | <b>Fixed in Version:</b> 31.2010.5042   |
|               | <b>Description:</b> After multiple MSPS (Management System Power Supply register) calls, the switch gets stuck.   |
| 3269531       | Keywords: MSPS  |
|               | Discovered in Version: 27.2010.3118   |
|               | <b>Fixed in Version:</b> 27.2010.5002   |
|               | <b>Description:</b> On NDR devices, when collecting BER data, the peer falls, causing the switch to hang.   |
| 3267152       | Keywords: BER COLLECT   |
|               | Discovered in Version: 31.2010.4102   |
|               | <b>Fixed in Version:</b> 31.2010.5002   |
| 3261861       | <b>Description:</b> Connecting an HDR device to an NDR device with Optical cables longer than 30m causes degradation in the bandwidth.  |

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|               | Keywords: HDR-to-NDR  |
|               | Discovered in Version: 31.2010.4102   |
|               | <b>Fixed in Version:</b> 31.2010.5002   |
|               | <b>Description:</b> Currently, on cables that perform polarity inversion there is no link up.   |
| 2974424       | Keywords: Cables, Polarity Inversion  |
|               | Discovered in Version: 31.2010.3118   |
|               | <b>Fixed in Version:</b> 31.2010.5002   |
| 3199650       | <b>Description:</b> A physical link failure between switches while a SHARP job is running and utilizing the link can cause one of the switches to become invalid for further SHARP jobs. This can result in either a "No resource" response for new SHARP job requests or in jobs getting stuck.  The bug fix requires SHARP version 3.2. |
|               | Keywords: SHARP   |
|               | Discovered in Version: 31.2010.4010   |
|               | <b>Fixed in Version:</b> 31.2010.4102   |
|               | <b>Description:</b> In case of an AR group table set request, the ARN mask is flushed for group that has an active pFRN timer.  |
| 3245821       | Keywords: PFRN  |
|               | Discovered in Version: 31.2010.4010   |
|               | <b>Fixed in Version:</b> 31.2010.4102   |
|               | <b>Description:</b> mask_force_clear_timeout timer in pFRN feature was not functional (the mask was not cleared when the timer expired).  |
| 3253717       | Keywords: PFRN  |
|               | Discovered in Version: 31.2010.4010   |
|               | <b>Fixed in Version:</b> 31.2010.4102   |
| 3242209       | <b>Description:</b> Set PFRN mad did not return error on wrong inputs in mask_clear_timer and mask_force_clear_timer fields.  |

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|               | Keywords: PFRN   |  |  |
|               | Discovered in Version: 31.2010.4010  |  |  |
|               | Fixed in Version: 31.2010.4102   |  |  |
|               | <b>Description:</b> The switch does not return SN or PN when trying to call via mlxlink or ibdiagnet.  |  |  |
| 3143685       | Keywords: SN, PN, mlxlink, ibdiagnet   |  |  |
|               | Discovered in Version: 31.2010.2300  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.4010  |  |  |
|               | <b>Description:</b> On rare occasions, traps were not properly repressed, which caused redundant traps to be sent multiple times.  |  |  |
| 3174239       | Keywords: Traps  |  |  |
|               | Discovered in Version: 31.2010.3118  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.4010  |  |  |
|               | <b>Description:</b> On rare occasion, when port is configured to mloop toggle may cause link to not rise.  |  |  |
| 3002314       | Keywords: Optic in Mloop   |  |  |
|               | Discovered in Version: 31.2010.2110  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.3118  |  |  |
|               | <b>Description:</b> On rare occasion, when egress port is split to two, the egress port may get stuck due to wrong Fast Path configuration.  |  |  |
| 3127727       | <b>Keywords:</b> Switch Hang, Fast Path, Split   |  |  |
|               | Discovered in Version: 31.2010.3004  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.3118  |  |  |
| 3082569       | <b>Description:</b> In some traffic patterns involving small packets, the PortRcvErrors counter may mistakenly count events of local physical errors due to an internal flow in the hardware that involves link packets. |  |  |
|               | Keywords: Counters   |  |  |

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|               | Discovered in Version: 31.2010.2246   |  |  |
|               | Fixed in Version: 31.2010.3004  |  |  |
|               | <b>Description:</b> On rare occasions, SHARP semaphore may remain locked on a port following an event of a port link down or an application crash.                          |  |  |
| 3085427       | Keywords: SHARPv3   |  |  |
|               | Discovered in Version: 31.2010.2036   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.3004   |  |  |
|               | <b>Description:</b> On rare occasions, job failures with SharpError trap may be experienced as a result of previous jobs that have failed.                                  |  |  |
| 3011581       | Keywords: SHARPv3   |  |  |
|               | Discovered in Version: 31.2010.2036   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.3004   |  |  |
|               | <b>Description:</b> After disconnecting MMS4X00-NL* cable and connecting Ultron cable to the same port, ports fails to link up.   |  |  |
| 3000602       | Keywords: Cables  |  |  |
|               | Discovered in Version: 31.2010.2110   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2300   |  |  |
|               | <b>Description:</b> In the event of link fault of a link between root switch and non-root switch during the run of a job, the next job run on the non-root switch may fail. |  |  |
| 3060122       | Keywords: SHARPv3   |  |  |
|               | Discovered in Version: 31.2010.2036   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2300   |  |  |
| 2923464       | <b>Description:</b> When using MMS4X00-NL Optical module, on rare occasions port that is in NDR speed may get stuck and stay in Polling state.                              |  |  |
|               | Keywords: NDR, Optical Module   |  |  |
|               | Discovered in Version: 31.2010.1404   |  |  |

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|               | <b>Fixed in Version:</b> 31.2010.2246   |  |  |
| 2859363       | <b>Description:</b> When using NVIDIA Quantum-2 systems in Auto-Neg mode, NDR speed in one lane (1x) is not supported.                |  |  |
|               | Keywords: Auto-Negotiation  |  |  |
|               | Discovered in Version: 31.2010.1310   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2246   |  |  |
| 3033131       | <b>Description:</b> The number of flows changed from 2 to 1, as intended.   |  |  |
|               | Keywords: SHARPv3   |  |  |
|               | Discovered in Version: 31.2010.2110   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2246   |  |  |
|               | <b>Description:</b> Running of concurrent jobs may lead to states where jobs unexpectedly terminate or get stuck.                     |  |  |
| 2972388       | Keywords: SHARPv3   |  |  |
|               | Discovered in Version: 31.2010.2036   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2110   |  |  |
|               | <b>Description:</b> On rare occasions, job resource cleanup may fail.   |  |  |
| 2002442       | Keywords: SHARPv3   |  |  |
| 2982113       | Discovered in Version: 31.2010.2036   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2110   |  |  |
|               | <b>Description:</b> During high load scenarios, performance degradation may be experienced.   |  |  |
| 2971339       | Keywords: SHARPv3   |  |  |
| 237.1333      | Discovered in Version: 31.2010.2036   |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2110   |  |  |
| 2849215       | <b>Description:</b> On NVIDIA Quantum-2 switches, when working with MFA7U10-H0xx cables, if one of the ports in a cage is disabled at |  |  |

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|               | the time of initialization by user configuration, reenabling the port will require toggling the link (i.e. enable disable enable).                   |  |  |
|               | Keywords: NVIDIA Quantum-2, Cables   |  |  |
|               | Discovered in Version: 31.2010.1310  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2036  |  |  |
|               | <b>Description:</b> On NVIDIA Quantum-2 systems, changing the Optical module rate was not allowed.   |  |  |
| 2890632       | Keywords: Optical Modules  |  |  |
|               | Discovered in Version: 31.2010.1310  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2036  |  |  |
|               | <b>Description:</b> In NVIDIA Quantum-2 systems, effective errors may occur with short Copper cable MCP4Y10-N00B.                                    |  |  |
| 2885798       | Workaround: N/A  |  |  |
|               | Discovered in Version: 31.2010.1310  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2036  |  |  |
|               | <b>Description:</b> In auto-negotiation flow, using copper cables when toggling both port's sides may cause the port to get stuck on rare occasions. |  |  |
| 2910161       | <b>Keywords:</b> Auto-Negotiation, Copper Cables   |  |  |
|               | Discovered in Version: 31.2010.1310  |  |  |
|               | <b>Fixed in Version:</b> 31.2010.2036  |  |  |

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