




**NVIDIA ConnectX-5 Adapter Cards
Firmware Release Notes v16.35.4030
LTS**

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 This is a long-term support (LTS) release. LTS is the practice of maintaining a software product for an extended period of time (up to three years) to help increase product stability. LTS releases include bug fixes and security patches.

1 Release Notes Update History

| Revision | Date | Description |
|------------|---------------|--|
| 16.35.4030 | July 04, 2024 | Initial release of this Release Notes version, This version introduces Bug Fixes . |

2 Overview

Firmware which is added at the time of manufacturing, is used to run user programs on the device and can be thought of as the software that allows hardware to run. Embedded firmware is used to control the functions of various hardware devices and systems, much like a computer's operating system (OS) controls the function of software applications. Firmware may be written into read-only memory (ROM), erasable programmable read-only memory (EPROM) or flash memory.

2.1 Firmware Download

Please visit the [firmware webpage](#).

2.2 Document Revision History

A list of the changes made to this document are provided in [Document Revision History](#).

3 Firmware Compatible Products

The chapter contains the following sections:

These are the release notes for the NVIDIA® ConnectX®-5 adapters firmware. This firmware supports the following protocols:

- InfiniBand - SDR, QDR, FDR10, FDR, EDR
- Ethernet - 1GbE, 10GbE, 25GbE, 40GbE, 50GbE, 100GbE
- PCI Express 4.0/3.0, supporting backwards compatibility for v3.0 v2.0 and v1.1

3.1 Supported Devices

This firmware supports the devices and protocols listed below:

| NVIDIA SKU | Legacy OPN | PSID | Device Name | FlexBoot | UEFI x86 | UEFI ARM | Enable / disable exprom Feature |
|---------------------|---------------|----------------|---|-------------------|--------------------|-------------------|---------------------------------|
| 900-9X5A Z-0053-ST4 | MCX512A -ACUT | MT_0000000 425 | ConnectX®-5 EN network interface card, 10/25GbE dual-port SFP28, PCIe3.0 x8, UEFI Enabled (x86/ ARM), tall bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X5A Z-0053-ST5 | MCX512A -ADAT | MT_0000000 361 | ConnectX®-5 Ex EN network interface card, 25GbE dual-port SFP28, PCIe3.0/4.0 x8, tall bracket | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X523 -0053-SB1 | MCX562A -ACAB | MT_0000000 241 | ConnectX®-5 EN network interface card for OCP 3.0, with host management, 25GbE Dual-port SFP28, PCIe3.0 x16, Thumbscrew (Pull Tab) bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X556 -0056-SB0 | MCX566A -CDAB | MT_0000000 242 | ConnectX®-5 Ex EN network interface card for OCP 3.0, with host management, 100GbE Dual-port QSFP28, PCIe4.0 x16, Internal Lock bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X568 -0016-SN4 | MCX545B -CCUN | MT_0000000 419 | ConnectX®-5 EN network interface card for OCP2.0, Type 1, with host management, 100GbE, single-port QSFP28, PCIe3.0 x16, UEFI Enabled, no bracket | Present (Enabled) | Present (Enabled) | Not Present | Exists |

| NVIDIA SKU | Legacy OPN | PSID | Device Name | FlexBoot | UEFI x86 | UEFI ARM | Enable / disable exprom Feature |
|--------------------|--------------|---------------|--|-------------------|--------------------|-------------------|---------------------------------|
| 900-9X513-0053-SNO | MCX542B-ACUN | MT_0000000427 | ConnectX®-5 EN network interface card for OCP2.0, Type 1, with host management, 25GbE dual-port SFP28, PCIe3.0 x8, UEFI Enabled (x86/ARM), no bracket Halogen free | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X5AZ-0053-ST0 | MCX512F-ACHT | MT_0000000416 | ConnectX®-5 EN network interface card, with host management, 25GbE Dual-port SFP28, PCIe3.0 x16, UEFI Enabled, tall bracket | Present (Enabled) | Present (Enabled) | Not Present | Exists |
| 900-9X568-0016-SN2 | MCX545A-CCUN | MT_0000000418 | ConnectX®-5 EN network interface card for OCP2.0, Type 2, with host management, 100GbE, single-port QSFP28, PCIe3.0 x16, UEFI Enabled, no bracket | Present (Enabled) | Present (Enabled) | Not Present | Exists |
| 900-9X5AD-0056-ST6 | MCX516A-CCHT | MT_0000000417 | ConnectX®-5 EN network interface card, with host management 100GbE dual-port QSFP28, PCIe3.0 x16, UEFI Enabled, tall bracket | Present (Enabled) | Present (Enabled) | Not Present | Exists |
| 900-9X556-0056-SI1 | MCX566A-CCAI | MT_0000000348 | ConnectX®-5 EN network interface card for OCP 3.0, with host management, 100GbE Dual-port QSFP28, PCIe3.0 x16, Internal Lock bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X5AZ-0053-OT3 | MCX512A-ACAT | MT_0000000080 | ConnectX®-5 EN network interface card, 10/25GbE dual-port SFP28, PCIe3.0 x8, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0016-ST1 | MCX515A-CCAT | MT_0000000011 | ConnectX-5 EN network interface card, 100GbE single-port QSFP28, PCIe3.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0015-ST0 | MCX515A-GCAT | MT_0000000087 | ConnectX®-5 EN network interface card, 50GbE single-port QSFP28, PCIe3.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0054-ST0 | MCX516A-BDAT | MT_0000000123 | ConnectX®-5 Ex EN network interface card, 40GbE dual-port QSFP28, PCIe 4.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0056-ST1 | MCX516A-CCAT | MT_0000000012 | ConnectX-5 EN network interface card, 100GbE dual-port QSFP28, PCIe3.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |

| NVIDIA SKU | Legacy OPN | PSID | Device Name | FlexBoot | UEFI x86 | UEFI ARM | Enable / disable exprom Feature |
|---------------------|---------------|----------------|---|-------------------|--------------------|--------------------|---------------------------------|
| 900-9X5A D-0056-ST7 | MCX516A -CDAT | MT_0000000 013 | ConnectX-5 Ex EN network interface card, 100GbE dual-port QSFP28, PCIe4.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Present (Disabled) | Exists |
| 900-9X569 -0054-SN0 | MCX546A -BCAN | MT_0000000 069 | ConnectX®-5 EN network interface card for OCP, 40GbE dual-port QSFP28, PCIe3.0 x16, no bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X569 -0056-SN1 | MCX546A -CDAN | MT_0000000 058 | ConnectX-5 Ex network interface card for OCP; 100GbE dual-port QSFP28; PCIe4.0 x16; no bracket; ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X568 -0016-SN1 | MCX545A -CCAN | MT_0000000 157 | ConnectX-5 EN network interface card for OCP 100GbE; single-port QSFP28; PCIe3.0 x16; no bracket; ROHS R6; | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5A Z-0053-ST6 | MCX512F-ACAT | MT_0000000 183 | ConnectX®-5 EN network interface card, 25GbE Dual-port SFP28, PCIe3.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5A Z-0013-ST0 | MCX511F-ACAT | MT_0000000 182 | ConnectX-5 EN network interface card; 25GbE single-port SFP28; PCIe4.0 x16; ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X513 -0053-SN2 | MCX542B -ACAN | MT_0000000 248 | ConnectX-5 EN network interface card for OCP; with host management; 25GbE dual-port SFP28; PCIe3.0 x8; no bracket; ROHS R6 Halogen free | Present (Enabled) | Present (Enabled) | Not Present | Exists |
| 900-9X513 -0053-SN1 | MCX542A -ACAN | MT_0000000 167 | ConnectX®-5 EN network interface card for OCP, with host management, 25GbE dual-port SFP28, PCIe3.0 x16, no bracket, ROHS R6 Halogen free | Present (Enabled) | Present (Disabled) | Not Present | Not Present |
| 900-9X5A D-0055-ST0 | MCX516A -GCAT | MT_0000000 090 | ConnectX®-5 EN network interface card, 50GbE dual-port QSFP28, PCIe3.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X515 -0016-MS0 | MCX553Q -ECAS | MT_0000000 309 | ConnectX®-5 VPI adapter card with Multi-Host, EDR IB (100Gb/s) and 100GbE, Single-port QSFP28, PCIe3.0 x4 on board, external connectors to 3x auxiliary cards?, Short bracket | Present (Enabled) | Present (Disabled) | Not Present | Exists |

| NVIDIA SKU | Legacy OPN | PSID | Device Name | FlexBoot | UEFI x86 | UEFI ARM | Enable / disable exprom Feature |
|--------------------|------------------|----------------|---|-------------------|--------------------|-------------------|---------------------------------|
| 900-9X568-0016-SN3 | MCX545A-ECAN | MT_0000000077 | ConnectX®-5 VPI network interface card for OCP EDR IB (100Gb/s) and 100GbE, single-port QSFP28, PCIe3.0 x16, no bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X568-0016-SN0 | MCX545B-ECAN | MT_00000000207 | ConnectX-5 VPI network interface card for OCP; with host management; EDR IB (100Gb/s) and 100GbE; single-port QSFP28; PCIe3.0 x16; no bracket; 8mm Heat Sink; ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0016-ST0 | MCX555A-ECAT | MT_0000000010 | ConnectX-5 VPI adapter card, EDR IB (100Gb/s) and 100GbE, single- port QSFP28, PCIe3.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0056-ST8 | MCX556A-ECAT | MT_00000000008 | ConnectX-5 VPI adapter card, EDR IB (100Gb/s) and 100GbE, dual-port QSFP28, PCIe3.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0056-STB | MCX556A-EDAT | MT_00000000009 | ConnectX-5 Ex VPI adapter card, EDR IB (100Gb/s) and 100GbE, dual-port QSFP28, PCIe4.0 x16, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X5AD-0056-DT1 | MCX556M-ECAT-S25 | MT_00000000023 | ConnectX®-5 VPI adapter card with Multi-Host Socket Direct supporting dual-socket server, EDR IB (100Gb/s) and 100GbE, dual-port QSFP28, 2x PCIe3.0 x8, 25cm harness, tall bracket, ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X569-0056-SN0 | MCX546A-EDAN | MT_00000000135 | ConnectX-5 VPI network interface card for OCP; EDR IB (100Gb/s) and 100GbE dual-port QSFP28; PCIe4.0 x16; no bracket; ROHS R6 | Present (Enabled) | Present (Disabled) | Not Present | Exists |
| 900-9X556-0055-MI0 | MCX566M-GDAI | MT_00000000262 | ConnectX®-5 Ex EN network interface card for OCP 3.0 with Multi-Host, with host management, 50GbE Dual-port QSFP28, PCIe 4.0/3.0 x16, Internal Lock bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |

| NVIDIA SKU | Legacy OPN | PSID | Device Name | FlexBoot | UEFI x86 | UEFI ARM | Enable / disable exprom Feature |
|--------------------|--------------|---------------|--|-------------------|-------------------|-------------------|---------------------------------|
| 900-9X5AD-0016-ST2 | MCX515A-CCUT | MT_0000000519 | ConnectX®-5 EN network interface card, 100GbE single-port QSFP28, PCIe3.0 x16, UEFI Enabled (ARM, x86), tall bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X5AD-0056-ST9 | MCX556A-ECUT | MT_0000000504 | ConnectX®-5 VPI adapter card, EDR IB (100Gb/s) and 100GbE, dual-port QSFP28, PCIe3.0 x16, UEFI enabled, tall bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X556-0016-MIO | MCX565M-CDAI | MT_0000000347 | ConnectX®-5 Ex EN network interface card for OCP 3.0, with Multi-Host and host management, 100GbE Single-port QSFP28, PCIe4.0 x16, Internal Lock bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X556-0016-SB0 | MCX565A-CCAB | MT_0000000585 | ConnectX-5 EN network interface card for OCP 3.0; with host management; 100GbE Single-port QSFP28; PCIe3.0 x16; Thumbscrew (Pull Tab) bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X568-0016-MN1 | MCX545M-ECAN | MT_0000000093 | ConnectX-5 VPI network interface card for OCP with Multi-Host; EDR IB (100Gb/s) and 100GbE; single-port QSFP28; PCIe3.0 x16; no bracket; ROHS R6; Halogen free | Present (Enabled) | Present (Enabled) | Not Present | Exists |
| 900-9X568-0015-SN0 | MCX545B-GCUN | MT_0000000681 | ConnectX-5 EN network interface card for OCP2.0, Type 1, with host management, 50GbE, single-port QSFP28, PCIe3.0 x16, UEFI Enabled, no bracket | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X527-0055-ST0 | MCX514A-GCHT | MT_0000000679 | ConnectX-5 EN network interface card; with host management; 40/50GbE Dual-port QSFP28; PCIe3.0 x8 | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |
| 900-9X527-0015-ST0 | MCX513A-GCHT | MT_0000000678 | ConnectX-5 EN network interface card; with host management; 40/50GbE Single-port QSFP28; PCIe3.0 x8; | Present (Enabled) | Present (Enabled) | Present (Enabled) | Exists |

3.2 Driver Software, Tools and Switch Firmware

The following are the drivers' software, tools, switch/HCA firmware versions tested that you can upgrade from or downgrade to when using this firmware version:

| | Supported Version |
|--------------------------------|--|
| ConnectX-5 Firmware | 16.35.4030 / 16.35.3502 / 16.35.3006 |
| MLNX_OFED | 5.8-5.1.1.2 / 5.8-4.1.5.0 / 5.8-3.0.7.0 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes. |
| MLNX_EN (MLNX_OFED based code) | 5.8-5.1.1.2 / 5.8-4.1.5.0 / 5.8-3.0.7.0 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes. |
| WinOF-2 | 3.10.52010 / 3.10.51000 / 3.10.50000 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes. |
| MFT | 4.22.1-417 / 4.22.1-406 / 4.22.1-307 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes. |
| FlexBoot | 3.6.902 Note: Please be aware that not all firmware binaries contain FlexBoot or UEFI, support may vary between cards. |
| UEFI | 14.29.15 Note: Please be aware that not all firmware binaries contain FlexBoot or UEFI, support may vary between cards. |
| MLNX-OS | 3.10.5002 onwards |
| Cumulus | 5.4 onwards |
| NVIDIA Quantum Firmware | 27.2010.5108 onwards |
| SwitchX-IB Firmware | 11.2008.2102 onwards |
| SwitchX-IB 2 Firmware | 15.2008.2102 onwards |


3.3 Supported Cables and Modules

3.3.1 Validated and Supported EDR / 100Gb/s Cables

| Speed | Cable OPN | Description |
|-------|-----------------|--|
| EDR | MCP1600-E001 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1m 30AWG |
| EDR | MCP1600-E001E30 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 1m, Black, 30AWG |
| EDR | MCP1600-E002 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 2m 28AWG |

| Speed | Cable OPN | Description |
|-------|-----------------|---|
| EDR | MCP1600-E002E30 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 2m, Black, 30AWG |
| EDR | MCP1600-E003 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 3m 26AWG |
| EDR | MCP1600-E003E26 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 3m, Black, 26AWG |
| EDR | MCP1600-E004E26 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 4m, Black, 26AWG |
| EDR | MCP1600-E005E26 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 5m, Black, 26AWG |
| EDR | MCP1600-E00A | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 0.5m 30AWG |
| EDR | MCP1600-E00AE30 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 0.5m, Black, 30AWG |
| EDR | MCP1600-E00BE30 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 0.75m, Black, 30AWG |
| EDR | MCP1600-E01A | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1.5m 30AWG |
| EDR | MCP1600-E01AE30 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 1.5m, Black, 30AWG |
| EDR | MCP1600-E01BE30 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 1.25m, Black, 30AWG |
| EDR | MCP1600-E02A | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 2.5m 26AWG |
| EDR | MCP1600-E02AE26 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 2.5m, Black, 26AWG |
| EDR | MFA1A00-E001 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1m |
| EDR | MFA1A00-E003 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 3m |
| EDR | MFA1A00-E005 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 5m |
| EDR | MFA1A00-E010 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 10m |
| EDR | MFA1A00-E015 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 15m |
| EDR | MFA1A00-E020 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 20m |
| EDR | MFA1A00-E030 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 30m |
| EDR | MFA1A00-E050 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 50m |
| EDR | MFA1A00-E100 | NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 100m |
| EDR | MMA1B00-E100 | NVIDIA transceiver, IB EDR, up to 100Gb/s, QSFP28, MPO, 850nm, SR4, up to 100m |
| EDR | MFA1A00-E003-TG | NVIDIA customized active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 3m |
| EDR | MFA1A00-E005-TG | NVIDIA customized active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 5m |
| EDR | MFA1A00-E010-TG | NVIDIA customized active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 10m |

| Speed | Cable OPN | Description |
|-------|-----------------|--|
| EDR | MFA1A00-E015-TG | NVIDIA customized active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 15m |
| EDR | MFA1A00-E020-TG | NVIDIA customized active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 20m |
| EDR | MFA1A00-E030-TG | NVIDIA customized active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 30m |
| EDR | MMA1L10-CR | NVIDIA Optical Transceiver, 100GbE, 100Gb/s, QSFP28, LC-LC, 1310nm, LR4 up to 10km |
| EDR | MMA1L30-CM | NVIDIA Optical Module, 100GbE, 100Gb/s, QSFP28, LC-LC, 1310nm, CWDM4, up to 2km *Supported in cable hardware generations 1 and 2. |
| EDR | MMS1C10-CM | NVIDIA Active Optical Module, 100Gb/s, QSFP, MPO, 1310nm, PSM4, up to 500m |

 EDR links raise with RS-FEC.

3.3.2 Validated and Supported QDR Cables

| Speed | Cable OPN | Description |
|-------|---------------|---|
| QDR | MC2206125-007 | NVIDIA passive copper cable, IB QDR, 40Gb/s, QSFP, 7m |

3.3.3 Validated and Supported FDR10 Cables

| Speed | Cable OPN | Description |
|-------|---------------|--|
| FDR10 | MC2206128-004 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 4m |
| FDR10 | MC2206128-005 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 5m |
| FDR10 | MC2206130-001 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 1m |
| FDR10 | MC2206130-002 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 2m |
| FDR10 | MC2206130-003 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 3m |
| FDR10 | MC2206130-00A | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 0.5m |
| FDR10 | MC2206310-003 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 3m |
| FDR10 | MC2206310-005 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 5m |

| Speed | Cable OPN | Description |
|-------|----------------|---|
| FDR10 | MC2206310-010 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 10m |
| FDR10 | MC2206310-015 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 15m |
| FDR10 | MC2206310-020 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 20m |
| FDR10 | MC2206310-030 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 30m |
| FDR10 | MC2206310-050 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 50m |
| FDR10 | MC2206310-100 | NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 100m |
| FDR10 | MC2210411-SR4E | NVIDIA optical module, 40Gb/s, QSFP, MPO, 850nm, up to 300m |

3.3.4 Validated and Supported FDR Cables

| Speed | Cable OPN | Description |
|-------|---------------|--|
| FDR | MC2207126-004 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 4m |
| FDR | MC2207128-003 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 3m |
| FDR | MC2207128-0A2 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2.5m |
| FDR | MC2207130-001 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1m |
| FDR | MC2207130-002 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2m |
| FDR | MC2207130-00A | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 0.5m |
| FDR | MC2207130-0A1 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1.5m |
| FDR | MC220731V-003 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 3m |
| FDR | MC220731V-005 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 5m |
| FDR | MC220731V-007 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 7m |
| FDR | MC220731V-010 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 10m |
| FDR | MC220731V-012 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 12m |
| FDR | MC220731V-015 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 15m |
| FDR | MC220731V-020 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 20m |

| Speed | Cable OPN | Description |
|-------|---------------|--|
| FDR | MC220731V-025 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 25m |
| FDR | MC220731V-030 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 30m |
| FDR | MC220731V-040 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 40m |
| FDR | MC220731V-050 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 50m |
| FDR | MC220731V-075 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 75m |
| FDR | MC220731V-100 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 100m |
| FDR | MCP1700-F001C | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1m, Red Pulltab |
| FDR | MCP1700-F001D | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1m, Yellow Pulltab |
| FDR | MCP1700-F002C | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2m, Red Pulltab |
| FDR | MCP1700-F002D | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2m, Yellow Pulltab |
| FDR | MCP1700-F003C | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 3m, Red Pulltab |
| FDR | MCP1700-F003D | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 3m, Yellow Pulltab |
| FDR | MCP170L-F001 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 1m |
| FDR | MCP170L-F002 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 2m |
| FDR | MCP170L-F003 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 3m |
| FDR | MCP170L-F00A | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 0.5m |
| FDR | MCP170L-F01A | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 1.5m |
| FDR | MMA1B00-F030D | NVIDIA transceiver, FDR, QSFP+, MPO, 850nm, SR4, up to 30m, DDMI |
| FDR | MC2210511-LR4 | NVIDIA optical module, 40Gb/s, QSFP, LC-LC, 1310nm, LR4 up to 10km |

3.3.5 Validated and Supported 200GbE Cables

| Speed | Cable OPN | Description |
|-------|-----------------|--|
| 200GE | MCP1650-V001E30 | NVIDIA Passive Copper cable, 200GbE, 200Gb/s, QSFP56, LSZH, 1m, black pulltab, 30AWG |

| Speed | Cable OPN | Description |
|-------|-----------------|--|
| 200GE | MCP1650-V002E26 | NVIDIA Passive Copper cable, 200GbE, 200Gb/s, QSFP56, LSZH, 2m, black pulltab, 26AWG |
| 200GE | MCP1650-V003E26 | NVIDIA Passive Copper cable, 200GbE, 200Gb/s, QSFP56, LSZH, 3m, black pulltab, 26AWG |
| 200GE | MCP1650-V00AE30 | NVIDIA Passive Copper cable, 200GbE, 200Gb/s, QSFP56, LSZH, 0.5m, black pulltab, 30AWG |
| 200GE | MCP1650-V01AE30 | NVIDIA Passive Copper cable, 200GbE, 200Gb/s, QSFP56, LSZH, 1.5m, black pulltab, 30AWG |
| 200GE | MCP1650-V02AE26 | NVIDIA Passive Copper cable, 200GbE, 200Gb/s, QSFP56, LSZH, 2.5m, black pulltab, 26AWG |
| 200GE | MCP1650-V00AE30 | NVIDIA Passive Copper cable, 200GbE, 200Gb/s, QSFP56, LSZH, 0.5m, black pulltab, 30AWG |

3.3.6 Validated and Supported 100GbE Cables


| Speed | Cable OPN | Description |
|--------|------------------|---|
| 100GbE | MCP1600-C001 | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP, PVC, 1m 30AWG |
| 100GbE | MCP1600-C001E30N | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 1m, Black, 30AWG, CA-N |
| 100GbE | MCP1600-C002 | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP, PVC, 2m 30AWG |
| 100GbE | MCP1600-C002E30N | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 2m, Black, 30AWG, CA-N |
| 100GbE | MCP1600-C003 | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP, PVC, 3m 28AWG |
| 100GbE | MCP1600-C003E26N | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 3m, Black, 26AWG, CA-N |
| 100GbE | MCP1600-C003E30L | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 3m, Black, 30AWG, CA-L |
| 100GbE | MCP1600-C005E26L | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 5m, Black, 26AWG, CA-L |
| 100GbE | MCP1600-C00A | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP, PVC, 0.5m 30AWG |
| 100GbE | MCP1600-C00AE30N | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 0.5m, Black, 30AWG, CA-N |
| 100GbE | MCP1600-C00BE30N | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 0.75m, Black, 30AWG, CA-N |
| 100GbE | MCP1600-C01A | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP, PVC, 1.5m 30AWG |
| 100GbE | MCP1600-C01AE30N | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 1.5m, Black, 30AWG, CA-N |
| 100GbE | MCP1600-C02A | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP, PVC, 2.5m 30AWG |
| 100GbE | MCP1600-C02AE26N | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 2.5m, Black, 26AWG, CA-N |

| Speed | Cable OPN | Description |
|--------|------------------|--|
| 100GbE | MCP1600-C02AE30L | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP28, 2.5m, Black, 30AWG, CA-L |
| 100GbE | MCP1600-C03A | NVIDIA Passive Copper cable, ETH 100GbE, 100Gb/s, QSFP, PVC, 3.5m 26AWG |
| 100GbE | MCP1600-E001 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1m 30AWG |
| 100GbE | MCP1600-E002 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 2m 28AWG |
| 100GbE | MCP1600-E003 | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 3m 26AWG |
| 100GbE | MCP1600-E01A | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1.5m 30AWG |
| 100GbE | MCP1600-E02A | NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 2.5m 26AWG |
| 100GbE | MCP7F00-A001R | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, colored pulltabs, 1m, 30AWG |
| 100GbE | MCP7F00-A001R30N | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 1m, Colored, 30AWG, CA-N |
| 100GbE | MCP7F00-A002R | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, colored pulltabs, 2m, 30AWG |
| 100GbE | MCP7F00-A002R30N | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 2m, Colored, 30AWG, CA-N |
| 100GbE | MCP7F00-A003R26N | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 3m, Colored, 26AWG, CA-N |
| 100GbE | MCP7F00-A003R30L | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 3m, Colored, 30AWG, CA-L |
| 100GbE | MCP7F00-A005R26L | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 5m, Colored, 26AWG, CA-L |
| 100GbE | MCP7F00-A01AR | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, colored pulltabs, 1.5m, 30AWG |
| 100GbE | MCP7F00-A01AR30N | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 1.5m, Colored, 30AWG, CA-N |
| 100GbE | MCP7F00-A02AR26N | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 2.5m, Colored, 26AWG, CA-N |
| 100GbE | MCP7F00-A02AR30L | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 2.5m, Colored, 30AWG, CA-L |
| 100GbE | MCP7F00-A02ARLZ | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 2.5m, LSZH, Colored, 28AWG |
| 100GbE | MCP7F00-A03AR26L | NVIDIA passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 3.5m, Colored, 26AWG, CA-L |
| 100GbE | MCP7H00-G001 | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 1m, 30AWG |
| 100GbE | MCP7H00-G001R | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, colored pulltabs, 1m, 30AWG |

| Speed | Cable OPN | Description |
|--------|------------------|--|
| 100GbE | MCP7H00-G001R30N | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 1m, Colored, 30AWG, CA-N |
| 100GbE | MCP7H00-G002R | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, colored pulltabs, 2m, 30AWG |
| 100GbE | MCP7H00-G002R30N | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 2m, Colored, 30AWG, CA-N |
| 100GbE | MCP7H00-G003R | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, colored pulltabs, 3m, 28AWG |
| 100GbE | MCP7H00-G003R26N | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 3m, Colored, 26AWG, CA-N |
| 100GbE | MCP7H00-G003R30L | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 3m, Colored, 30AWG, CA-L |
| 100GbE | MCP7H00-G004R26L | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 4m, Colored, 26AWG, CA-L |
| 100GbE | MCP7H00-G01AR | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, colored pulltabs, 1.5m, 30AWG |
| 100GbE | MCP7H00-G01AR30N | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 1.5m, Colored, 30AWG, CA-N |
| 100GbE | MCP7H00-G02AR | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, colored pulltabs, 2.5m, 30AWG |
| 100GbE | MCP7H00-G02AR26N | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 2.5m, Colored, 26AWG, CA-N |
| 100GbE | MCP7H00-G02AR30L | NVIDIA passive copper hybrid cable, ETH 100Gb/s to 2x50Gb/s, QSFP28 to 2xQSFP28, 2.5m, Colored, 30AWG, CA-L |
| 100GbE | MFA1A00-C003 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 3m |
| 100GbE | MFA1A00-C005 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 5m |
| 100GbE | MFA1A00-C010 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 10m |
| 100GbE | MFA1A00-C015 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 15m |
| 100GbE | MFA1A00-C020 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 20m |
| 100GbE | MFA1A00-C030 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 30m |
| 100GbE | MFA1A00-C050 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 50m |
| 100GbE | MFA1A00-C100 | NVIDIA active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 100m |
| 100GbE | MFA7A20-C003 | NVIDIA active fiber hybrid solution, ETH 100GbE to 2x50GbE, QSFP28 to 2xQSFP28, 3m |
| 100GbE | MFA7A20-C005 | NVIDIA active fiber hybrid solution, ETH 100GbE to 2x50GbE, QSFP28 to 2xQSFP28, 5m |
| 100GbE | MFA7A20-C010 | NVIDIA active fiber hybrid solution, ETH 100GbE to 2x50GbE, QSFP28 to 2xQSFP28, 10m |
| 100GbE | MFA7A20-C020 | NVIDIA active fiber hybrid solution, ETH 100GbE to 2x50GbE, QSFP28 to 2xQSFP28, 20m |
| 100GbE | MFA7A50-C003 | NVIDIA active fiber hybrid solution, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 3m |

| Speed | Cable OPN | Description |
|--------|-----------------|---|
| 100GbE | MFA7A50-C005 | NVIDIA active fiber hybrid solution, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 5m |
| 100GbE | MFA7A50-C010 | NVIDIA active fiber hybrid solution, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 10m |
| 100GbE | MFA7A50-C015 | NVIDIA active fiber hybrid solution, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 15m |
| 100GbE | MFA7A50-C020 | NVIDIA active fiber hybrid solution, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 20m |
| 100GbE | MFA7A50-C030 | NVIDIA active fiber hybrid solution, ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 30m |
| 100GbE | MMA1B00-C100D | NVIDIA transceiver, 100GbE, QSFP28, MPO, 850nm, SR4, up to 100m, DDMI |
| 100GbE | MMA1L10-CR | NVIDIA optical transceiver, 100GbE, QSFP28, LC-LC, 1310nm, LR4 up to 10km Note: Only revision A2 and above. |
| 100GbE | MFA1A00-C001-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 1m |
| 100GbE | MFA1A00-C002-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP28, LSZH, 2m |
| 100GbE | MFA1A00-C003-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 3m |
| 100GbE | MFA1A00-C005-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 5m |
| 100GbE | MFA1A00-C007-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP28, LSZH, 7m |
| 100GbE | MFA1A00-C010-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 10m |
| 100GbE | MFA1A00-C015-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 15m |
| 100GbE | MFA1A00-C020-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 20m |
| 100GbE | MFA1A00-C030-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 30m |
| 100GbE | MFA1A00-C050-TG | NVIDIA customized active fiber cable, ETH 100GbE, 100Gb/s, QSFP, LSZH, 50m |
| 100GbE | MMA1L30-CM | NVIDIA optical module, 100GbE, 100Gb/s, QSFP28, LC-LC, 1310nm, CWDM4, up to 2km |
| 100GbE | MMS1C10-CM | NVIDIA active optical module, 100Gb/s, QSFP, MPO, 1310nm, PSM4, up to 500m |
| 100GbE | MMS1V70-CM | NVIDIA transceiver, 100GbE, QSFP28, LC-LC, 1310nm, DR1 |

3.3.7 Validated and Supported 56GbE Cables

 The 56GbE cables are used to raise 40GbE link speed as the 56GbE speed is not supported.

| Speed | Cable OPN | Description |
|-------|---------------|--|
| 56GE | MC2207126-004 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 4m |
| 56GE | MC2207128-003 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 3m |
| 56GE | MC2207128-0A2 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2.5m |
| 56GE | MC2207130-001 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1m |
| 56GE | MC2207130-002 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2m |
| 56GE | MC2207130-00A | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 0.5m |
| 56GE | MC2207130-0A1 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1.5m |
| 56GE | MC220731V-003 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 3m |
| 56GE | MC220731V-005 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 5m |
| 56GE | MC220731V-010 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 10m |
| 56GE | MC220731V-015 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 15m |
| 56GE | MC220731V-020 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 20m |
| 56GE | MC220731V-025 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 25m |
| 56GE | MC220731V-030 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 30m |
| 56GE | MC220731V-040 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 40m |
| 56GE | MC220731V-050 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 50m |
| 56GE | MC220731V-075 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 75m |
| 56GE | MC220731V-100 | NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 100m |
| 56GE | MCP1700-F001C | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1m, Red Pulltab |
| 56GE | MCP1700-F001D | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 1m, Yellow Pulltab |

| Speed | Cable OPN | Description |
|-------|---------------|--|
| 56GE | MCP1700-F002C | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2m, Red Pulltab |
| 56GE | MCP1700-F002D | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2m, Yellow Pulltab |
| 56GE | MCP1700-F003C | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 3m, Red Pulltab |
| 56GE | MCP1700-F003D | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 3m, Yellow Pulltab |
| 56GE | MCP170L-F001 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 1m |
| 56GE | MCP170L-F002 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 2m |
| 56GE | MCP170L-F003 | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 3m |
| 56GE | MCP170L-F00A | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 0.5m |
| 56GE | MCP170L-F01A | NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 1.5m |

3.3.8 Validated and Supported 40GbE Cables

| Speed | Cable OPN | Description |
|-------|---------------|--|
| 40GE | MC2206128-004 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 4m |
| 40GE | MC2206128-005 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 5m |
| 40GE | MC2206130-001 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 1m |
| 40GE | MC2206130-002 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 2m |
| 40GE | MC2206130-003 | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 3m |
| 40GE | MC2206130-00A | NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 0.5m |
| 40GE | MC2210126-004 | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 4m |
| 40GE | MC2210126-005 | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 5m |
| 40GE | MC2210128-003 | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 3m |
| 40GE | MC2210130-001 | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 1m |
| 40GE | MC2210130-002 | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 2m |

| Speed | Cable OPN | Description |
|-------|----------------|---|
| 40GE | MC2210310-003 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 3m |
| 40GE | MC2210310-005 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 5m |
| 40GE | MC2210310-010 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 10m |
| 40GE | MC2210310-015 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 15m |
| 40GE | MC2210310-020 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 20m |
| 40GE | MC2210310-030 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 30m |
| 40GE | MC2210310-050 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 50m |
| 40GE | MC2210310-100 | NVIDIA active fiber cable, ETH 40GbE, 40Gb/s, QSFP, 100m |
| 40GE | MC2210411-SR4E | NVIDIA optical module, 40Gb/s, QSFP, MPO, 850nm, up to 300m |
| 40GE | MC2609125-005 | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 5m |
| 40GE | MC2609130-001 | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 1m |
| 40GE | MC2609130-003 | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 3m |
| 40GE | MCP1700-B001E | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 1m, Black Pulltab |
| 40GE | MCP1700-B002E | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 2m, Black Pulltab |
| 40GE | MCP1700-B003E | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 3m, Black Pulltab |
| 40GE | MCP1700-B01AE | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 1.5m, Black Pulltab |
| 40GE | MCP1700-B02AE | NVIDIA passive copper cable, ETH 40GbE, 40Gb/s, QSFP, 2.5m, Black Pulltab |
| 40GE | MMA1B00-B150D | NVIDIA transceiver, 40GbE, QSFP+, MPO, 850nm, SR4, up to 150m, DDMI |
| 40GE | MCP7900-X01AA | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 1.5m, Blue Pulltab, customized label |
| 40GE | MCP7904-X002A | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 2m, Black Pulltab, customized label |
| 40GE | MCP7904-X003A | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 3m, Black Pulltab, customized label |
| 40GE | MCP7904-X01AA | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 1.5m, Black Pulltab, customized label |

| Speed | Cable OPN | Description |
|-------|---------------|---|
| 40GE | MCP7904-X02AA | NVIDIA passive copper hybrid cable, ETH 40GbE to 4x10GbE, QSFP to 4xSFP+, 2.5m, Black Pulltab, customized label |
| 40GE | MC2210511-LR4 | NVIDIA Optical Module 40Gb/s FDR 10 QSFP LC-LC 1310nm LR4 up to 10km |
| 40GE | MC6709309-005 | NVIDIA passive fiber hybrid cable, MPO to 8xLC, 5m |
| 40GE | MC6709309-010 | NVIDIA passive fiber hybrid cable, MPO to 8xLC, 10m |
| 40GE | MC6709309-020 | NVIDIA passive fiber hybrid cable, MPO to 8xLC, 20m |
| 40GE | MC6709309-030 | NVIDIA passive fiber hybrid cable, MPO to 8xLC, 30m |

3.3.9 Validated and Supported 25GbE Cables



The 25GbE cables can be supported only when connected to the MAM1Q00A-QSA28 module.

| Speed | Cable OPN | Description |
|-------|------------------|---|
| 25GbE | MAM1Q00A-QSA28 | NVIDIA cable module, ETH 25GbE, 100Gb/s to 25Gb/s, QSFP28 to SFP28 |
| 25GbE | MCP2M00-A001 | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 1m, 30AWG |
| 25GbE | MCP2M00-A001E30N | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 1m, Black, 30AWG, CA-N |
| 25GbE | MCP2M00-A002 | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 2m, 30AWG |
| 25GbE | MCP2M00-A002E30N | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 2m, Black, 30AWG, CA-N |
| 25GbE | MCP2M00-A003E26N | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 3m, Black, 26AWG, CA-N |
| 25GbE | MCP2M00-A003E30L | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 3m, Black, 30AWG, CA-L |
| 25GbE | MCP2M00-A004E26L | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 4m, Black, 26AWG, CA-L |
| 25GbE | MCP2M00-A005E26L | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 5m, Black, 26AWG, CA-L |
| 25GbE | MCP2M00-A00A | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 0.5m, 30AWG |
| 25GbE | MCP2M00-A00AE30N | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 0.5m, Black, 30AWG, CA-N |
| 25GbE | MCP2M00-A01AE30N | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 1.5m, Black, 30AWG, CA-N |
| 25GbE | MCP2M00-A02AE26N | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 2.5m, Black, 26AWG, CA-N |
| 25GbE | MCP2M00-A02AE30L | NVIDIA Passive Copper cable, ETH, up to 25Gb/s, SFP28, 2.5m, Black, 30AWG, CA-L |
| 25GbE | MFA2P10-A003 | NVIDIA active optical cable 25GbE, SFP28, 3m |
| 25GbE | MFA2P10-A005 | NVIDIA active optical cable 25GbE, SFP28, 5m |

| Speed | Cable OPN | Description |
|-------|------------------|--|
| 25GbE | MFA2P10-A007 | NVIDIA active optical cable 25GbE, SFP28, 7m |
| 25GbE | MFA2P10-A010 | NVIDIA active optical cable 25GbE, SFP28, 10m |
| 25GbE | MFA2P10-A015 | NVIDIA active optical cable 25GbE, SFP28, 15m |
| 25GbE | MFA2P10-A020 | NVIDIA active optical cable 25GbE, SFP28, 20m |
| 25GbE | MFA2P10-A030 | NVIDIA active optical cable 25GbE, SFP28, 30m |
| 25GbE | MFA2P10-A050 | NVIDIA active optical cable 25GbE, SFP28, 50m |
| 25GbE | MMA2P00-AS | NVIDIA transceiver, 25GbE, SFP28, LC-LC, 850nm, SR, up to 100m |
| 25GbE | SFP25G-AOC10M-TG | NVIDIA customized active optical cable 25GbE, SFP28, 10m, Aqua |
| 25GbE | SFP25G-AOC30M-TG | NVIDIA customized active optical cable 25GbE, SFP28, 30m, Aqua |
| 25GbE | SFP25G-AOC07M-TG | NVIDIA customized active optical cable 25GbE, SFP28, 7m, Aqua |
| 25GbE | SFP25G-AOC05M-TG | NVIDIA customized active optical cable 25GbE, SFP28, 5m, Aqua |
| 25GbE | SFP25G-AOC03M-TG | NVIDIA customized active optical cable 25GbE, SFP28, 3m, Aqua |
| 25GbE | SFP25G-AOC20M-TG | NVIDIA customized active optical cable 25GbE, SFP28, 20m, Aqua |
| 25GbE | MMA2P00-AS_FF | NVIDIA transceiver, 25GbE, SFP28, LC-LC, 850nm, SR, up to 100m |
| 25GbE | MMA2P00-AS-SP | NVIDIA transceiver, 25GbE, SFP28, LC-LC, 850nm, SR, up to 100m, single package |
| 25GbE | MMA2L20-AR | NVIDIA optical transceiver, 25GbE, 25Gb/s, SFP28, LC-LC, 1310nm, LR up to 10km |

3.3.10 Validated and Supported 10GbE Cables

| Speed | Cable OPN | Description |
|-------|---------------|---|
| 10GE | MFM1T02A-LR | NVIDIA SFP+ optical module for 10GBASE-LR |
| 10GE | MFM1T02A-SR | NVIDIA SFP+ optical module for 10GBASE-SR |
| 10GE | MAM1Q00A-QSA | NVIDIA cable module, ETH 10GbE, 40Gb/s to 10Gb/s, QSFP to SFP+ |
| 10GE | MC2309124-005 | NVIDIA passive copper hybrid cable, ETH 10GbE, 10Gb/s, QSFP to SFP+, 5m |
| 10GE | MC2309124-007 | NVIDIA passive copper hybrid cable, ETH 10GbE, 10Gb/s, QSFP to SFP+, 7m |
| 10GE | MC2309130-001 | NVIDIA passive copper hybrid cable, ETH 10GbE, 10Gb/s, QSFP to SFP+, 1m |
| 10GE | MC2309130-002 | NVIDIA passive copper hybrid cable, ETH 10GbE, 10Gb/s, QSFP to SFP+, 2m |
| 10GE | MC2309130-003 | NVIDIA passive copper hybrid cable, ETH 10GbE, 10Gb/s, QSFP to SFP+, 3m |
| 10GE | MC2309130-00A | NVIDIA passive copper hybrid cable, ETH 10GbE, 10Gb/s, QSFP to SFP+, 0.5m |
| 10GE | MC3309124-004 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 4m |

| Speed | Cable OPN | Description |
|-------|---------------|--|
| 10GE | MC3309124-005 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 5m |
| 10GE | MC3309124-006 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 6m |
| 10GE | MC3309124-007 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 7m |
| 10GE | MC3309130-001 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 1m |
| 10GE | MC3309130-002 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 2m |
| 10GE | MC3309130-003 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 3m |
| 10GE | MC3309130-00A | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 0.5m |
| 10GE | MC3309130-0A1 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 1.5m |
| 10GE | MC3309130-0A2 | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 2.5m |
| 10GE | MCP2100-X001B | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 1m, Blue Pulltab, Connector Label |
| 10GE | MCP2100-X002B | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 2m, Blue Pulltab, Connector Label |
| 10GE | MCP2100-X003B | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 3m, Blue Pulltab, Connector Label |
| 10GE | MCP2101-X001B | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 1m, Green Pulltab, Connector Label |
| 10GE | MCP2104-X001B | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 1m, Black Pulltab, Connector Label |
| 10GE | MCP2104-X002B | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 2m, Black Pulltab, Connector Label |
| 10GE | MCP2104-X003B | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 3m, Black Pulltab, Connector Label |
| 10GE | MCP2104-X01AB | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 1.5m, Black Pulltab, Connector Label |
| 10GE | MCP2104-X02AB | NVIDIA passive copper cable, ETH 10GbE, 10Gb/s, SFP+, 2.5m, Black Pulltab, Connector Label |

3.3.11 Validated and Supported 1GbE Cables

| Speed | Cable OPN | Description |
|-------|--------------|--|
| 1GbE | MC3208011-SX | NVIDIA Optical module, ETH 1GbE, 1Gb/s, SFP, LC-LC, SX 850nm, up to 500m |
| 1GbE | MC3208411-T | NVIDIA module, ETH 1GbE, 1Gb/s, SFP, Base-T, up to 100m |

3.4 Supported 3rd Party Cables and Modules

| Speed | Cable OPN | Description |
|-------|-------------------|--|
| 10GbE | BN-QS-SP-CBL-5M | 40G QSFP+ to 4xSFP+ DAC Breakout Direct Attach Cable 5m |
| 10GbE | BN-QS-SP-CBL-5M | 40G QSFP+ to 4xSFP+ DAC Breakout Direct Attach Cable 5m |
| 10GbE | CAB-SFP-SFP-1M | Arista 10GBASE-CR SFP+ Cable 1 Meter |
| 10GbE | CAB-SFP-SFP-3M | Arista 10GBASE-CR SFP+ Cable 3 Meter |
| 10GbE | CAB-SFP-SFP-5M | Arista 10GBASE-CR SFP+ Cable 5 Meter |
| 10GbE | FTLX1471D3BCL-ME | 10GBASE-LR SFP+ 1310nm 10km DOM Transceiver Module |
| 10GbE | FTLX8571D3BCL-ME | 10gb SFP 850nm Optic Transceiver |
| 10GbE | L45593-D178-B50 | QSFP-4SFP10G-CU5M |
| 10GbE | SFP-10G-SR | Cisco 10GBASE-SR SFP+ transceiver module for MMF, 850-nm wavelength, LC duplex connector |
| 10GbE | SFP-H10GB-ACU10M | Cisco 10GBASE-CR1 Active Copper Cable 10-meter |
| 10GbE | SFP-H10GB-ACU7M | Cisco 10GBASE-CR1 Active Copper Cable 7-meter |
| 10GbE | SFP-H10GB-CU1M | Cisco 1-m 10G SFP+ Twinax cable assembly, passive |
| 10GbE | SFP-H10GB-CU3M | Cisco 3-m 10G SFP+ Twinax cable assembly, passive |
| 10GbE | SFP-H10GB-CU4M | Cisco 10GBASE-CR1 Copper Cable 4-meter |
| 10GbE | SFP-H10GB-CU5M | Cisco 5-m 10G SFP+ Twinax cable assembly, passive |
| 25GbE | SFP-25G-AOC5M | Cisco 25GBASE-AOC Active Optical Cable 5-meter |
| 25GbE | SFP-25G-AOC7M | Cisco 25GBASE-AOC Active Optical Cable 7-meter |
| 25GbE | SFP-H25G-CU1M | 25GBASE-CR1 Copper Cable 1-meter |
| 25GbE | SFP-H25G-CU2.5M | Cisco 25GBASE-CR1 Copper Cable 2.5-meter |
| 25GbE | SFP-H25G-CU2M | 25GBASE-CR1 Copper Cable 2-meter |
| 25GbE | SFP-H25G-CU3M | Cisco 25GBASE-CR1 Copper Cable 3-meter |
| 25GbE | SFP-H25G-CU4M | Cisco 25GBASE-CR1 Copper Cable 4-meter |
| 40GbE | 2231254-2 | PASSIVE COPPER CABLE ETH 40GBE QSFP 3M |
| 40GbE | QSFP-40G-SR4 | Cisco 40GBASE-SR4, 4 lanes, 850 nm MMF |
| 40GbE | QSFP-40G-SR-BD | Cisco 40GBASE-SR-BiDi, duplex MMF |
| 40GbE | QSFP-4SFP10G-CU5M | PASSIVE COPPER SPLITTER CABLE ETH 40GBE TO 4X10GBE 5M |
| 40GbE | QSFP-H40G-ACU10M | Cisco 40GBASE-CR4 QSFP direct-attach copper cable, 10-meter, active |
| 40GbE | QSFP-H40G-AOC10M | Cisco 40GBase-AOC QSFP direct-attach Active Optical Cable, 10-meter |
| 40GbE | QSFP-H40G-CU5M | PASSIVE COPPER CABLE ETH 40GBE QSFP 5M |

| Speed | Cable OPN | Description |
|--------|-------------------|---|
| 56GbE | FTL414QB2N-E5 | Finisar FTL414QB2N-E5 56Gb 850nm 100m QSFP+ Transceiver Module ARK |
| 100GbE | CAB-Q-Q-100GbE-3M | Passive 3 meter , QSFP+ to QSFP+ QSFP100 TWINAX 103.125Gbps-CR4 |
| 100GbE | FTLF8519P3BTL-N1 | 100GBASE-SX and 2G Fibre Channel (2GFC) 500m Industrial Temperature SFP Optical Transceiver |
| 100GbE | QSFP-100G-AOC5M | Cisco 100GBASE QSFP Active Optical Cables 5-meter |
| 100GbE | QSFP-100G-AOC7M | Cisco 100GBASE QSFP Active Optical Cables 7-meter |
| 100GbE | QSFP-100G-CU3M | Cisco 100GBASE-CR4 Passive Copper Cable 3-meter |
| 100GbE | QSFP-100G-CU5M | Cisco 100GBASE-CR4 Passive Copper Cable 5-meter |
| 100GbE | QSFP-100G-SR4-S | Cisco 100GBASE SR4 QSFP Transceiver, MPO, 100m over OM4 MMF |
| 100GbE | QSFP-40/100-SRBD | Cisco 100G and 40GBASE SR-BiDi QSFP Transceiver, LC, 100m OM4 MMF |
| 100GbE | SO-QSFP28-LR4 | QSFP28, 100GBase, 1310nm, SM, DDM, 10km, LC |
| 100GbE | TR-FC13L-N00 | 100G QSFP28 Optical Transceivers, QSFP28 LR4 (10km) |
| 100GbE | FTLC9152RGPL | 100G 100M QSFP28 SWDM4 OPT TRANS |

3.5 Tested Switches

3.5.1 Tested EDR / 100Gb/s Switches

| Speed | Switch Silicon | OPN # / Name | Description | Vendor |
|-------|----------------|--------------|---|--------|
| EDR | Switch-IB | MSB7790-XXX | 36-port Unmanaged EDR 100Gb/s InfiniBand Switch Systems | NVIDIA |
| EDR | Switch-IB | MSB7700-XXX | 36-port Managed EDR 100Gb/s InfiniBand Switch Systems | NVIDIA |
| EDR | Switch-IB 2 | MSB7800-XXX | 36-port Managed EDR 100Gb/s InfiniBand Switch Systems | NVIDIA |

3.5.2 Tested FDR Switches

| Speed | Switch Silicon | OPN # / Name | Description | Vendor |
|-------|----------------|---------------|---|----------|
| FDR | SwitchX-2 | MSX6036F-1SFS | 36 QSFP+ port Unmanaged FDR InfiniBand Switch Systems | Mellanox |

3.5.3 Tested 100GbE Switches

| Speed | Switch Silicon | OPN # / Name | Description | Vendor |
|--------|----------------|--------------------|--|---------|
| 100GbE | Spectrum-3 | MSN4600-XXXX | 64-port Non-blocking 100GbE Open Ethernet Switch System | NVIDIA |
| 100GbE | Spectrum-2 | MSN3700C-XXXX | 32-port Non-blocking 100GbE Open Ethernet Switch System | NVIDIA |
| 100GbE | Spectrum-2 | MSN3420-XXXX | 48 SFP + 12 QSFP ports Non-blocking 100GbE Open Ethernet Switch System | NVIDIA |
| 100GbE | Spectrum | MSN2410-XXXX | 48-port 25GbE + 8-port 100GbE Open Ethernet Switch System | NVIDIA |
| 100GbE | Spectrum | MSN2700-XXXX | 32-port Non-blocking 100GbE Open Ethernet Switch System | NVIDIA |
| 100GbE | N/A | QFX5200-32C-32 | 32-port 100GbE Ethernet Switch System | Juniper |
| 100GbE | N/A | S6820-56HF | 48 SFP+ + 8 QSFP Ports 100GbE Switch Ethernet | H3C |
| 100GbE | N/A | CE6860-1-48S8CQ-EI | Huawei 100GbE Ethernet switch | Huawei |
| 100GbE | N/A | 7060CX-32S | 32-port 100GbE Ethernet Switch System | Arista |
| 100GbE | N/A | 3232C | 32-port 100GbE Ethernet Switch System | Cisco |
| 100GbE | N/A | N9K-C9236C | 36-port 100GbE Ethernet Switch System | Cisco |
| 100GbE | N/A | 93180YC-EX | 48-port 25GbE + 6-port 100GbE Ethernet Switch System | Cisco |
| 100GbE | N/A | T7032-IX7 | 32-port 100GbE Ethernet Switch System | Quanta |

3.5.4 Tested 10/40GbE Switches

| Speed | Switch Silicon | OPN # / Name | Description | Vendor |
|----------|----------------|--------------|--|---------|
| 10GbE | N/A | 5548UP | 32x 10GbE SFP+ Switch System | Cisco |
| 10/40GbE | N/A | 7050Q | 16 x 40GbE QSFP+ Switch System | Arista |
| 10/40GbE | N/A | 7050S | 48x 10GbE SFP+ and 4 x 40GbE QSFP+ Switch System | Arista |
| 10/40GbE | N/A | G8264 | 48x 10GbE SFP+ and 4 x 40GbE QSFP+ Switch System | Lenovo |
| 10/40GbE | N/A | QFX3500 | 48x 10GbE SFP+ and 4 x 40GbE QSFP+ Switch System | Juniper |
| 10/40GbE | N/A | S4810P-AC | 48x 10GbE SFP+ and 4 x 40GbE QSFP+ Switch System | Force10 |
| 10/40GbE | N/A | 3064 | 48x 10GbE SFP+ and 4 x 40GbE QSFP+ Switch System | Cisco |

| Speed | Switch Silicon | OPN # / Name | Description | Vendor |
|----------|----------------|--------------|--|--------|
| 10/40GbE | N/A | 8164F | 48x 10GbE SFP+ and 2 x 40GbE QSFP+ Switch System | Dell |
| 10/40GbE | N/A | S5000 | 48x 10GbE SFP+ and 4 x 40GbE QSFP+ Switch System | Dell |
| 10/40GbE | N/A | 3132Q | 4x 10GbE SFP+ and 32 x 40GbE QSFP+ Switch System | Cisco |
| 40GbE | N/A | 7050QX | 32x 40GbE QSFP+ Switch System | Arista |
| 40GbE | N/A | G8316 | 16x 40GbE QSFP+ Switch System | Lenovo |
| 40GbE | N/A | S6000 | 32x 40GbE QSFP+ Switch System | Dell |

3.6 PRM Revision Compatibility


This firmware version complies with the following Programmer's Reference Manual:


- Adapters Programmer's Reference Manual (PRM), Rev 0.53 or later, which has Command Interface Revision 0x5. The command interface revision can be retrieved by means of the QUERY_FW command and is indicated by the field cmd_interface_rev.


4 Changes and New Features

4.1 Important Notes

 SR-IOV - Virtual Functions (VF) per Port - The maximum Virtual Functions (VF) per port is 127. For further information, see [Known Issues](#).

 It is recommended to enable the "above 4G decoding" BIOS setting for features that require a large amount of PCIe resources (e.g., SR-IOV with numerous VFs, PCIe Emulated Switch, Large BAR Requests).

 Security Hardening Enhancements: This release contains important reliability improvements and security hardening enhancements. NVIDIA recommends upgrading your devices' firmware to this release to improve the devices' firmware security and reliability.

 When upgrading or changing the configuration on multi-host adapter cards, for the changes to take effect, PCIe restart must be simultaneously sent from both hosts (servers).

To do so, perform the following:

1. Shut down the server with the auxiliary card.
2. Shut down the server with the primary card.
3. Bring back the server with the primary card.
4. Bring back the server with the auxiliary card.

4.2 Changes and New Feature in this Firmware Version

| Feature/Change | Description |
|-------------------|--|
| 16.35.4030 | |
| Bug Fixes | See <i>Bug Fixes in this Firmware Version</i> section. |

4.3 Unsupported Features and Commands

4.3.1 Unsupported Features

The following advanced feature are unsupported in the current firmware version:

- The following service types:
 - SyncUMR

- Mellanox transport
- RAW IPv6
- INT-A not supported for EQs only MSI-X
- PCI VPD write flow (RO flow supported)
- Streaming Receive Queue (STRQ) and collapsed CQ
- Subnet Manager (SM) on VFs
- RoCE LAG in Multi-Host/Socket-Direct

4.3.2 Unsupported Commands

- QUERY_MAD_DEMUX
- SET_MAD_DEMUX
- CREATE_RQ - MEMORY_RQ_RMP
- MODIFY_LAG_ASYNC_EVENT

5 Bug Fixes in this Firmware Version

For a list of old Bug Fixes, please see [Bug Fixes History](#).

| Internal Ref. | Issue |
|---------------|--|
| 3878507 | <p>Description: Fixed an issue that led to unexpected source UDP port in RoCE packets when the firmware ignored a non-PF driver configuration for RoCE source UDP port control (as set in SET_HCA_CAP.sw_r_roce_src_udp_port).</p> <p>Keywords: UDP, RoCE</p> <p>Discovered in Version: 16.35.3006</p> <p>Fixed in Release: 16.35.4030</p> |

6 Known Issues

Ethernet Rate Limit per VF in RoCE Mode Limitations

| Dual Port Device | | | | Single Port Device | |
|------------------------|----------|-------------------------|----------|--------------------|----------|
| w/o LAG (TOTAL_VFS>32) | | With LAG (TOTAL_VFS<32) | | w/o LAG | |
| w/o QoS | Full QoS | w/o QoS | Full QoS | w/o QoS | Full QoS |
| 127 | 127 | 64 | 64 | 127 | 127 |

Ethernet Rate Limit per VF in InfiniBand Mode Limitations

| Dual Port Device | | Single Port Device | |
|------------------|----------|--------------------|----------|
| w/o LAG | | w/o LAG | |
| w/o QoS | Full QoS | w/o QoS | Full QoS |
| 127 | 127 | 127 | 127 |

Known Issues

| Internal Ref. | Issue |
|---------------|---|
| 3209624 | Description: To configure Adaptive Routing in RoCE through ROCE_ACCL access register or through cmdif mlxconfig, ROCE_ADAPTIVE_ROUTING_EN nvconfig parameter must be set. |
| | Workaround: N/A |
| | Keywords: Adaptive Routing in RoCE |
| | Discovered in Version: 16.35.1012 |
| 3200779 | Description: Changing dynamic PCIe link width is not supported. |
| | Workaround: N/A |
| | Keywords: PCIe |
| | Discovered in Version: 16.34.1002 |
| 2864238 | Description: VPD cannot be accessed after firmware upgrade or reset when the following sequence is performed: <ol style="list-style-type: none"> 1. Upgrade to a new firmware and perform a cold reboot 2. Downgrade to an old firmware 3. Run fwreset 4. Upgrade to a new firmware 5. Run fwreset |
| | Workaround: Run the upgrade or reset sequence as follow: <ol style="list-style-type: none"> 1. Upgrade to a new firmware and perform a cold reboot 2. Downgrade to an old firmware 3. Run fwreset 4. Upgrade to a new firmware 5. Perform a cold reboot |
| | Keywords: VDP |
| | Discovered in Version: 16.32.1010 |

| Internal Ref. | Issue |
|---------------|---|
| 2850374 | <p>Description: When using the Fast Linkup flow, once in 40 iterations the linkup time may take up to ~800 sec.</p> <p>Workaround: N/A</p> <p>Keywords: Fast linkup flow</p> <p>Discovered in Version: 16.32.1010</p> |
| 2616755 | <p>Description: Forward action for IPoIB is not supported on RX RDMA Flow Table.</p> <p>Workaround: N/A</p> <p>Keywords: Steering, IPoIB</p> <p>Discovered in Version: 16.32.1010</p> |
| 2622688 | <p>Description: Software steering on multi-port devices requires performing cfg. on top of the multi-port function and not the affiliated single-port function.</p> <p>Workaround: N/A</p> <p>Keywords: Software steering, multi-port devices</p> <p>Discovered in Version: 16.29.2002</p> |
| 2378593 | <p>Description: Sub 1sec firmware update (fast reset flow) is not supported when updating from previous releases to the current one. Doing so may cause network disconnection events.</p> <p>Workaround: Use full reset flow for firmware upgrade/downgrade.</p> <p>Keywords: Sub 1sec firmware update</p> <p>Discovered in Version: 16.29.1016</p> |
| 2213356 | <p>Description: The following are the Steering Dump limitations:</p> <ul style="list-style-type: none"> • Requires passing the version (FW/Stelib/MFT) and device type to stelib • Re-format is not supported • Advanced multi-port feature is not supported - LAG/ROCE_AFFILIATION/MPFS_LB/ESW_LB (only traffic vhca <-> wire) • Packet types supported: <ul style="list-style-type: none"> • Layer 2 Eth • Layer 3 IPv4/Ipv6/Grh • Layer 4 TCP/UDP/Bth/GreV0/GreV1 • Tunneling VXLAN/Geneve/GREv0/Mpls • FlexParser protocols are not supported (e.g AliVxlan/VxlanGpe etc..). • Compiles only on x86 <p>Workaround: N/A</p> <p>Keywords: Steering Bump</p> <p>Discovered in Version: 16.29.1016</p> |
| 2365322 | <p>Description: When configuring adapter card's Level Scheduling, a QoS tree leaf (QUEUE_GROUP) configured with default rate_limit and default bw_share, may not obey the QoS restrictions imposed by any of the leaf's ancestors.</p> |

| Internal Ref. | Issue |
|---------------|---|
| | <p>Workaround: To prevent such a case, configure at least one of the following QoS attributes of a leaf: <code>max_average_bw</code> or <code>bw_share</code></p> <p>Keywords: QoS</p> <p>Discovered in Version: 16.29.1016</p> |
| 2109187 | <p>Description: CRC errors are observed when connecting between FPGA and ConnectX-5 using 3rd party cables.</p> <p>Workaround: N/A</p> <p>Keywords: CRC</p> <p>Discovered in Version: 16.27.2008</p> |
| 2064538 | <p>Description: When working with an NVME offload QP that is created with a unaligned page size (<code>page_offset != 0</code>), the QP moves to an error state on the first posted WQE.</p> <p>Workaround: Create an NVME offload QP with page an aligned size (<code>page_offset = 0</code>).</p> <p>Keywords: NVMF offload, unaligned page size</p> <p>Discovered in Version: 16.27.2008</p> |
| 2080512 | <p>Description: Running VF lag with TTL WA (<code>ESWITCH_IPV4_TTL_MODIFY_ENABLE = 1</code>) may cause performance degradation.</p> <p>Workaround: To bypass this issue, configure the following using <code>mlxconfig</code>:</p> <ul style="list-style-type: none"> • <code>ESWITCH_HAIRPIN_DESCRIPTOR[0..7]=11</code> • <code>ESWITCH_HAIRPIN_TOT_BUFFER_SIZE[0..7]=17</code> <p>Keywords: <code>mlxconfig</code>, VF Lag</p> <p>Discovered in Version: 16.27.1016</p> |
| 2071210 | <p>Description: <code>mlxconfig</code> query for the <code>BOOT_INTERRUPT_DIS</code> TLV shows a wrong value in the "current value" field.</p> <p>Workaround: Use "next boot" indication to see the right value.</p> <p>Keywords: <code>mlxconfig</code></p> <p>Discovered in Version: 16.27.1016</p> |
| 1930619 | <p>Description: <code>PF_BAR2</code> and <code>ATS</code> cannot be enabled together, i.e. when <code>PF_BAR2</code> is enabled, <code>ATS</code> cannot be enabled too.</p> <p>Workaround: N/A</p> <p>Keywords: <code>ATS</code>, <code>SF</code>, <code>BAR2</code>, Multi GVMI</p> <p>Discovered in Version: 16.26.1040</p> |
| - | <p>Description: In rare cases, following a server powerup, a fatal error (device's health compromised) message might appear with <code>ext_synd 0x8d1d</code>. The error will be accompanied by a failure to use <code>mlxconfig</code> and in some cases flash burning tools.</p> <p>Workaround: N/A</p> <p>Keywords: <code>mlxconfig</code>, flash tool, <code>ext_synd 0x8d1d</code></p> |

| Internal Ref. | Issue |
|-----------------|--|
| | Discovered in Version: 16.26.1040 |
| 1836465 | <p>Description: When using the hairpin feature, and using VLAN strip or using the “modify esw vport context” command, the packets can have an incorrect VLAN header. Meaning, using VLAN push/pop may not work properly when using vport context VLAN. The features that may be affected by this and not work properly are:</p> <ul style="list-style-type: none"> • Host chaining • Mirroring in FDB • TTL modify in FDB • VGT+ <p>Workaround: N/A</p> <p>Keywords: E-switch vport context, VLAN</p> <p>Discovered in Version: 16.26.1040</p> |
| 1842278 | <p>Description: DC LAG can function only in case there is a single PF per port without any active VFs.</p> <p>Workaround: N/A</p> <p>Keywords: DC LAG</p> <p>Discovered in Version: 16.26.1040</p> |
| 1796628 | <p>Description: Due to performance considerations, unicast loopback traffic will go through the NIC SX tables, and multicast loopback traffic will skip the NIC SX tables.</p> <p>Workaround: N/A</p> <p>Keywords: Performance, unicast loopback traffic, multicast loopback traffic</p> <p>Discovered in Version: 16.26.1040</p> |
| 1797493 | <p>Description: Firmware asserts may occur when setting the PF_BAR2_SIZE value higher than the maximum supported size.</p> <p>Workaround: Configure within limits (NIC PF_BAR_SIZE <= 4).</p> <p>Keywords: Multi-GVMI, Sub-Function, SFs, BAR2</p> <p>Discovered in Version: 16.26.1040</p> |
| 1768814/1772474 | <p>Description: Due to hardware limitation, REG_C cannot be passed over loopback when the FDB action is forwarded to multiple destinations.</p> <p>Workaround: N/A</p> <p>Keywords: Connection-Tracking</p> <p>Discovered in Version: 16.25.1020</p> |
| 1770736 | <p>Description: When a PF or ECPF with many VFs (SR-IOV), and/or SFs (Multi-GVMI) triggers an FLR, PCIe completion timeout might occur.</p> <p>Workaround: Increase the PCIe completion timeout.</p> <p>Keywords: Multi-GVMI, SR-IOV, Sub-Function, Virtual Function, PF FLR</p> <p>Discovered in Version: 16.25.1020</p> |
| 1716334 | <p>Description: When mlxconfig.PF_BAR2_EN is enabled, configuring more than 255 PCI functions will raise an assert.</p> |

| Internal Ref. | Issue |
|---------------|---|
| | <p>Workaround: When working with BAR2, configure SR-IOV to align to the 255 PCI functions limitation. mlxconfig.NUM_OF_VFS controls the number of configured SR-IOV VFs. e.g.:</p> <ul style="list-style-type: none"> • Smart NICs: 2 External Host PFs, 2 ARM ECPFs, 125 VFs per PF. • Non-smart NICs: 2 External Host PFs, 126 VFs per PF <p>Keywords: Multi-GVMI, PF_BAR2_EN, Sub-Functions, SR-IOV, VFs</p> <p>Discovered in Version: 16.25.1020</p> |
| 1699214 | <p>Description: NODNIC VF is partially tested. It is fully tested only in ConnectX-5 adapter cards.</p> <p>Workaround: N/A</p> <p>Keywords: NODNIC VF</p> <p>Discovered in Version: 16.25.1020</p> |
| 1749691 | <p>Description: On rare occasions, when using Socket-Direct devices, inband burning through the external port might fail.</p> <p>Workaround: N/A</p> <p>Keywords: Socket-Direct, inband burning</p> <p>Discovered in Version: 16.25.1020</p> |
| 1689186 | <p>Description: Changing priority to TC map during traffic might cause packet drops.</p> <p>Workaround: N/A</p> <p>Keywords: QoS</p> <p>Discovered in Version: 16.25.1020</p> |
| 1604699 | <p>Description: Ethernet RFC 2819 counter ether_stats_oversize_pkts and Ethernet IEEE 802.3 counter a_frame_too_long_errors share the same resource. Clearing each of them will affect the other.</p> <p>Workaround: N/A</p> <p>Keywords: Counters</p> <p>Discovered in Version: 16.25.1020</p> |
| 1558250 | <p>Description: eSwitch owner may receive NIC_VPORT_CONTEXT events from vPorts that are not necessarily armed using the nic vport context arm_change_even tbit.</p> <p>Workaround: N/A</p> <p>Keywords: Port event, NODNIC</p> |
| - | <p>Description: In Ethernet mode, at 10/40GbE speeds, only NO-FEC in Force mode is supported. Other user configurations are overridden.</p> <p>Workaround: N/A</p> <p>Keywords: Ethernet, 10GbE, 40GbE, RS-FEC</p> <p>Discovered in Version: 16.25.1020</p> |
| 1574876 | <p>Description: DC RoCE LAG is functional only if the router posts VRRP address as the source MAC.</p> |

| Internal Ref. | Issue |
|-----------------|--|
| | <p>Workaround: N/A</p> <p>Keywords: DC RoCE LAG</p> <p>Discovered in Version: 16.25.1020</p> |
| 1498399 | <p>Description: If the XRC switches between SRQ/RMPs while there is an outstanding ODP on the responder XRC QP, a CQE with an error might be generated (that is not a PFAULT abort).</p> <p>Workaround: N/A</p> <p>Keywords: XRC SRQ/RMP ODP</p> <p>Discovered in Version: 16.25.1020</p> |
| 1546492 | <p>Description: Executing the update_lid command while the IB port sniffer utility is active can stop the utility.</p> <p>Workaround: N/A</p> <p>Keywords: IB Sniffer</p> <p>Discovered in Version: 16.24.1000</p> |
| 1537898 | <p>Description: Initializing a function while the IB port sniffer utility is active can stop the utility.</p> <p>Workaround: N/A</p> <p>Keywords: IB Sniffer</p> <p>Discovered in Version: 16.24.1000</p> |
| 1523577 | <p>Description: When modifying the TTL in the NIC RX, the CQE checksum is not recalculated automatically. The limitation is indicated by the ttl_checksum_correction bit. If the ttl_checksum_correction=0, the capability is not functioning properly.</p> <p>Workaround: N/A</p> <p>Keywords: multi_prio_sq, VF</p> <p>Discovered in Version: 16.24.1000</p> |
| 1414290 | <p>Description: When getting an inline scatter CQE on IB striding RQ, the stride index in the CQE will be zero.</p> <p>Workaround: N/A</p> <p>Keywords: Scatter CQE</p> <p>Discovered in Version: 16.24.1000</p> |
| 1475490 | <p>Description: Reboot is not supported on any host during the PLDM firmware burning process.</p> <p>Workaround: N/A</p> <p>Keywords: PLDM</p> <p>Discovered in Version: 16.23.1020</p> |
| 1332714/1345824 | <p>Description: The maximum “read” size of MTRC_STDB is limited to 272 Bytes.</p> <p>Workaround: Set the MTRC_STDB.read_size to the maximum value of 0x110=272 Bytes</p> |

| Internal Ref. | Issue | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|---------------|---|-----|-----|--------|-----|--|------------|-------|----|-----|--------|-------|----|----|--|---------------|-------|----|-----|--------|-------|----|-----|--|------------|-------|-----|-----|--|-------|-----|-----|--|
| | <p>Keywords: Access register, MTRC_STDB, tracer to dmesg, fwtrace to dmesg</p> <p>Discovered in Version: 16.23.1020</p> | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 1408994 | <p>Description: FTE with both forward (FWD) and encapsulation (ENCAP) actions is not supported in the SX NIC Flow Table.</p> <p>Workaround: N/A</p> <p>Keywords: SX NIC Flow Table</p> <p>Discovered in Version: 16.23.1020</p> | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 1350794 | <p>Description: Encapsulation / Decapsulation support in steering has the following limitations:</p> <ul style="list-style-type: none"> Encapsulation / Decapsulation can be open on the FDB only if all VFs are non active. Encapsulation / Decapsulation supports single mode only: FDB / NIC. Opening tables of both types is not supported. Encapsulation / Decapsulation per device support: <table border="1"> <thead> <tr> <th></th> <th></th> <th>NIC</th> <th>FDB</th> <th></th> </tr> </thead> <tbody> <tr> <td rowspan="2">ConnectX-4</td> <td>encap</td> <td>NO</td> <td>YES</td> <td>non-MH</td> </tr> <tr> <td>decap</td> <td>NO</td> <td>NO</td> <td></td> </tr> <tr> <td rowspan="2">ConnectX-4 Lx</td> <td>encap</td> <td>NO</td> <td>YES</td> <td>non-MH</td> </tr> <tr> <td>decap</td> <td>NO</td> <td>YES</td> <td></td> </tr> <tr> <td rowspan="2">ConnectX-5</td> <td>encap</td> <td>YES</td> <td>YES</td> <td></td> </tr> <tr> <td>decap</td> <td>YES</td> <td>YES</td> <td></td> </tr> </tbody> </table> <p>Workaround: N/A</p> <p>Keywords: Steering Encapsulation / Decapsulation</p> <p>Discovered in Version: 16.23.1020</p> | | | NIC | FDB | | ConnectX-4 | encap | NO | YES | non-MH | decap | NO | NO | | ConnectX-4 Lx | encap | NO | YES | non-MH | decap | NO | YES | | ConnectX-5 | encap | YES | YES | | decap | YES | YES | |
| | | NIC | FDB | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| ConnectX-4 | encap | NO | YES | non-MH | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | decap | NO | NO | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| ConnectX-4 Lx | encap | NO | YES | non-MH | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | decap | NO | YES | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| ConnectX-5 | encap | YES | YES | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | decap | YES | YES | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 1027553 | <p>Description: While using e-switch vport sVLAN stripping, the RX steering values on the sVLAN might not be accurate.</p> <p>Workaround: N/A</p> <p>Keywords: e-sw vport sVLAN stripping, RX steering</p> <p>Discovered in Version: 16.24.1000</p> | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 1799917 | <p>Description: Untagged CVLAN packets in the Steering Flow Tables do not match the sVLAN tagged packets.</p> <p>Workaround: N/A</p> <p>Keywords: Steering Flow Tables, CVLAN/sVLAN packets</p> <p>Discovered in Version: 16.23.1020</p> | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 1504073 | <p>Description: When using ConnectX-5 with LRO over PPC systems there might be backpressure to the NIC due to delayed PCI writes operations. In this case bandwidth might drop from line-rate to ~35Gb/s. Packet loss or pause frames might also be observed.</p> | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| Internal Ref. | Issue |
|---------------|---|
| | <p>Workaround: Look for an indication of PCI back pressure (“outbound_pci_stalled_wr” counter in ethtools advancing). Disabling LRO helps reduce the back pressure and its effects.</p> <p>Keywords: Flow Control, LRO</p> <p>Discovered in Version: 16.23.1020</p> |
| 1178792 | <p>Description: Host Chaining Limitations:</p> <ul style="list-style-type: none"> • Single MAC address per port is supported • Both ports should be configured to Ethernet when host chaining is enabled • The following capabilities cannot function when host chaining is enabled: <ul style="list-style-type: none"> • SR-IOV • DSCP • NODNIC • Load balancing • LAG • Dual Port RoCE (multi port vHCA) <p>Workaround: N/A</p> <p>Keywords: Host Chaining</p> <p>Discovered in Version: 16.22.1002</p> |
| 1277762 | <p>Description: An Ethernet multicast loopback packet is not counted (even if it is not a local loopback packet) when running the nic_receive_steering_discard command.</p> <p>Workaround: N/A</p> <p>Keywords: Ethernet multicast loopback packet</p> <p>Discovered in Version: 16.22.1002</p> |
| 1190753 | <p>Description: When a dual-port VHCA sends a RoCE packet on its non-native port. and the packet arrives to its affiliated vport FDB, a mismatch might happen on the rules that match the packet source vport.</p> <p>Workaround: N/A</p> <p>Keywords: RoCE, vport FDB</p> <p>Discovered in Version: 16.22.1002</p> |
| 1306342 | <p>Description: Signature-accessing WQEs sent locally to the NVMeF target QPs that encounter signature errors, will not send a SIGERR CQE.</p> <p>Workaround: N/A</p> <p>Keywords: Signature-accessing WQEs, NVMeF target</p> <p>Discovered in Version: 16.22.1002</p> |
| 1059975 | <p>Description: NVMeF limitation:</p> <ul style="list-style-type: none"> • Transaction size - up to 128KB per IO (non-inline) • Support up to 16K connections • Support single namespace per drive • Staging buffer size must be at least 16MB in order to allow SRQ size of 64 entries <p>Workaround: N/A</p> |

| Internal Ref. | Issue |
|---------------|--|
| | Keywords: NVMeF Discovered in Version: 16.22.1010 |
| 1168594 | Description: RoCE Dual Port Mode (a.k.a Multi-Port vHCA: MPV) is not supported in Multi-Host setups. Workaround: N/A Keywords: Multi-Port vHCA, Multi-Host Discovered in Version: 16.21.1000 |
| 1072337 | Description: If a packet is modified in e-sw flow steering, the SX sniffer Flow Table (of the VF) will see the sniffed packet after the modification. Workaround: N/A Keywords: SX sniffer Flow Table Discovered in Version: 16.21.1000 |
| 1171013 | Description: Signature Handover Operations is not supported when FPP (Function-Per-Port) mode is disabled. Workaround: N/A Keywords: Signature Handover Operations, FPP Discovered in Version: 16.21.1000 |

7 PreBoot Drivers (FlexBoot/UEFI)

7.1 FlexBoot Changes and New Features

For further information, please refer to the [FlexBoot Release Notes](#).

7.2 UEFI Changes and Major New Features

For further information, please refer to the [UEFI Release Notes](#).

8 Supported Non-Volatile Configurations


| Configuration | mlxconfig Parameter Name | Class | TLV ID |
|---------------------------|--|------------|--------|
| NV_MEMIC_CONF | MEMIC_BAR_SIZE | GLOBAL (0) | 0x6 |
| | MEMIC_SIZE_LIMIT | | |
| NV_HOST_CHAINING_CONF | HOST_CHAINING_MODE | | 0x8 |
| | HOST_CHAINING_DESCRIPTOR | | |
| | HOST_CHAINING_TOTAL_BUFFER_SIZE | | |
| NV_FLEX_PARS_CONF | FLEX_PARSER_PROFILE_ENABLE | | 0xe |
| | FLEX_IPV4_OVER_VXLAN_PORT | | |
| NV_ROCE_1_5_CONF | ROCE_NEXT_PROTOCOL | | 0x10 |
| NV_INTERNAL_RESOURCE_CONF | ESWITCH_HAIRPIN_DESCRIPTOR | | 0x13 |
| | ESWITCH_HAIRPIN_TOT_BUFFER_SIZE | | |
| NV_GLOBAL_PCI_CONF | NON_PREFETCHABLE_PF_BAR | 0x80 | |
| | NUM_OF_VFS | | |
| | SRIOV_EN | | |
| | PF_LOG_BAR_SIZE | | |
| | VF_LOG_BAR_SIZE | | |
| | NUM_PF_MSIX | | |
| | NUM_VF_MSIX | | |
| NV_TPT_CONF | INT_LOG_MAX_PAYLOAD_SIZE | 0x82 | |
| NV_POWER_CONF | SW_RECOVERY_ON_ERRORS | 0x88 | |
| | RESET_WITH_HOST_ON_ERRORS | | |
| | ADVANCED_POWER_SETTINGS | | |
| NV_GLOBAL_MASK | ece_disable_mask | 0x116 | |
| NV_SW_OFFLOAD_CONFIG | CQE_COMPRESSION | 0x10a | |
| | IP_OVER_VXLAN_EN | | |
| | PCI_ATOMIC_MODE | | |
| | LRO_LOG_TIMEOUT0 | | |
| | LRO_LOG_TIMEOUT1 | | |
| | LRO_LOG_TIMEOUT2 | | |
| | LRO_LOG_TIMEOUT3 | | |
| | log_max_outstandng_wqe | | |
| | NV_config.sr_enable (ConnectX-6 Dx and above) | | |
| | | | |
| NV_IB_DC_CONF | LOG_DCR_HASH_TABLE_SIZE | 0x190 | |

| Configuration | mlxconfig Parameter Name | Class | TLV ID |
|-----------------------|-------------------------------|-------------------|--------|
| | DCR_LIFO_SIZE | | |
| NV_VPI_LINK_TYPE | LINK_TYPE | PHYSICAL_PORT (2) | 0x12 |
| NV_ROCE_CC | ROCE_CC_PRIO_MASK | | 0x107 |
| | ROCE_CC_ALGORITHM | | |
| NV_ROCE_CC_ECN | CLAMP_TGT_RATE_AFTER_TIME_INC | | 0x108 |
| | CLAMP_TGT_RATE | | |
| | RPG_TIME_RESET | | |
| | RPG_BYTE_RESET | | |
| | RPG_THRESHOLD | | |
| | RPG_MAX_RATE | | |
| | RPG_AI_RATE | | |
| | RPG_HAI_RATE | | |
| | RPG_GD | | |
| | RPG_MIN_DEC_FAC | | |
| | RPG_MIN_RATE | | |
| | RATE_TO_SET_ON_FIRST_CNP | | |
| | DCE_TCP_G | | |
| | DCE_TCP_RTT | | |
| | RATE_REDUCE_MONITOR_PERIOD | | |
| INITIAL_ALPHA_VALUE | | | |
| MIN_TIME_BETWEEN_CNPS | | | |
| CNP_802P_PRIO | | | |
| CNP_DSCP | | | |
| NV_LLDP_NB_CONF | LLDP_NB_DCBX | 0x10a | |
| | LLDP_NB_RX_MODE | | |
| | LLDP_NB_TX_MODE | | |
| NV_LLDP_NB_DCBX | DCBX_IEEE | 0x18e | |
| | DCBX_CEE | | |
| | DCBX_WILLING | | |
| NV_KEEP_LINK_UP | KEEP_ETH_LINK_UP | 0x190 | |
| | KEEP_IB_LINK_UP | | |
| | KEEP_LINK_UP_ON_BOOT | | |
| | KEEP_LINK_UP_ON_STANDBY | | |
| NV_QOS_CONF | NUM_OF_VL | 0x192 | |

| Configuration | mlxconfig Parameter Name | Class | TLV ID |
|-------------------------|--------------------------|-------------------|--------|
| | NUM_OF_TC | | |
| | NUM_OF_PFC | | |
| NV_MPFS_CONF | DUP_MAC_ACTION | | 0x196 |
| | SRIOV_IB_ROUTING_MODE | | |
| | IB_ROUTING_MODE | | |
| NV_HCA_CONF | PCI_WR_ORDERING | HOST-FUNCTION (3) | 0x112 |
| | MULTI_PORT_VHCA_EN | | |
| NV_EXTERNAL_PORT_CTRL | PORT_OWNER | | 0x192 |
| | ALLOW_RD_COUNTERS | | |
| | RENEG_ON_CHANGE | | |
| | TRACER_ENABLE | | |
| NV_ROM_BOOT_CONF2 | IP_VER | | 0x195 |
| | BOOT_UNDI_NETWORK_WAIT | | |
| NV_ROM_UEFI_CONF | UEFI_HII_EN | | 0x196 |
| NV_ROM_UEFI_DEBUG_LEVEL | BOOT_DBG_LOG | | 0x206 |
| | UEFI_LOGS | | |
| NV_ROM_BOOT_CONF1 | BOOT_VLAN | | 0x221 |
| | LEGACY_BOOT_PROTOCOL | | |
| | BOOT_RETRY_CNT | | |
| | BOOT_LACP_DIS | | |
| | BOOT_VLAN_EN | | |
| NV_ROM_IB_BOOT_CONF | BOOT_PKEY | | 0x222 |
| NV_PCI_CONF | ADVANCED_PCI_SETTINGS | HOST (7) | 0x80 |
| SAFE_MODE_CONF | SAFE_MODE_THRESHOLD | | 0x82 |
| | SAFE_MODE_ENABLE | | |

9 Release Notes History

9.1 Changes and New Feature History

 This section includes history of changes and new feature of 3 major releases back. For older releases history, please refer to the relevant firmware versions.

| Feature/Change | Description |
|-------------------|--|
| 16.35.3502 | |
| Bug Fixes | See <i>Bug Fixes in this Firmware Version</i> section. |

| Feature/Change | Description |
|-------------------|--|
| 16.35.3006 | |
| Bug Fixes | See <i>Bug Fixes in this Firmware Version</i> section. |


| Feature/Change | Description |
|-------------------|--|
| 16.35.2000 | |
| Bug Fixes | See <i>Bug Fixes in this Firmware Version</i> section. |

| Feature/Change | Description |
|--|--|
| 16.35.1012 | |
| UDP | Added support for copy modify header steering action to/from the UDP field. |
| RoCE: Adaptive Timer | Enabled ADP timer to allow the user to configure RC or DC qp_timeout values lower than 16. |
| QoS Priority Trust Default State | QoS priority trust default state can now be changed using the new nvconfig below: <ul style="list-style-type: none"> • QOS_TRUST_STATE_P1 • QOS_TRUST_STATE_P2 The values that can be used to set the default state are: <ul style="list-style-type: none"> • TRUST_PORT • TRUST_PCP • TRUST_DSCP • TRUST_DSCP_PCP |
| Bug Fixes | See Bug Fixes section. |
| 16.34.1002 | |
| LLDP Properties Implementation on RDE | Added LLDPEnable, LLDPTransmit and LLDPReceive properties to the RDE Port schema implementation. |
| Bug Fixes | See Bug Fixes section. |

16.33.1048

| | |
|--|--|
| NV Configurations via the Relevant Reset Flow | Added <code>pci_rescan_needed</code> field to the MFRL access register to indicate whether a PCI rescan is needed based on the NV configurations issued by the software. Note: If the Keep Link Up NV configuration is changed, phyless reset will be blocked. |
| MADs | Added a new MAD of class SMP that has the attributes <code>hierarchy_Info</code> as defined in the IB Specification and is used to query the hierarchy information stored on the node and the physical port. |
| ICM Pages | Added a new register (<code>vhca_icm_ctrl_access_reg</code>) to enable querying and limiting the ICM pages in use. |
| XRQ QP Errors Enhancements | Enhanced the XRQ QP error information provided to the user in case QP goes into an error state. In such case, QUERY_QP will provide information on the syndrome type and which side caused the error. |
| NetworkPort Schema Replacement | Replaced the deprecated NetworkPort schema with Port schema in NIC RDE implementation. |
| ibstat | Updated the ibstat status reported when the phy link is down. Now <code>QUERY_VPORT_STATE.max_tx_speed</code> of UPLINK will not be reported as 0 anymore. |
| SMPs | Disabled the option to send SMPs from unauthorized hosts. |
| Firmware Steering | Enabled the option to modify the <code>ip_ecn</code> field in the packet header in firmware steering. |
| SW Steering Cache | Modified the TX or RX cache invalidation behavior. TX or RX cache invalidation now does not occur automatically but only when the software performs the sync operation using the <code>sync_steering</code> command. |
| Mega Allocations in Bulk Allocator Mechanism | Modified the maximum bulk size per single allocation from <code>"log_table_size - log_num_unisizes"</code> , to allocate any range size, to remove limitations that HWS objects such as counters and modify arguments might encounter. |
| Bug Fixes | See Bug Fixes section. |

9.2 Bug Fixes History

 This section includes history of fixed bugs of 3 major releases back. For older releases history, please refer to the relevant firmware versions.

| Internal Ref. | Issue |
|---------------|---|
| 3673153 | <p>Description: Modified the TCP IPv4 flows so that the steering TIR <code>rx_hash_symmetric</code> field is now valid only when both the SRC and DST fields are not set to zero.</p> <p>Keywords: TCP IPv4 flows</p> |

| Internal Ref. | Issue |
|---------------|--|
| | Discovered in Version: 16.35.2000 |
| | Fixed in Release: 16.35.3502 |

| Internal Ref. | Issue |
|---------------|--|
| 3333959 | Description: Enabled ACS for single port cards. |
| | Keywords: ACS |
| | Discovered in Version: 16.35.2000 |
| | Fixed in Release: 16.35.3006 |
| 3303728 | Description: Fixed packet loss that occurred when restarting the transmit. |
| | Keywords: Packet loss |
| | Discovered in Version: 16.35.2000 |
| | Fixed in Release: 16.35.3006 |
| 3336571 | Description: Fixed an issue that prevented RoCE malformed packets (UDP packet with dest_port equal to RoCE well known udp_dport (0x4791)) from being counted on the vport_counter when the function disables RoCE (through MODIFY_NIC_VPORT_CONTEXT command). |
| | Keywords: RoCE, vPort, counters |
| | Discovered in Version: 16.35.2000 |
| | Fixed in Release: 16.35.3006 |
| 3492648 | Description: Fixed a memory leakage that occurred when closing connected QPs (Type RC/UC/XRC/DC). |
| | Keywords: Memory leakage |
| | Discovered in Version: 16.35.2000 |
| | Fixed in Release: 16.35.3006 |
| 3498600 | Description: Added a missing VLAN strip. |
| | Keywords: VLAN |
| | Discovered in Version: 16.35.2000 |
| | Fixed in Release: 16.35.3006 |
| 3331009 | Description: Added vPort counters after creating the LAG demux table to count kernel packets reaching all the PFs participating in the LAG. |
| | Keywords: LAG, counters, vPort |
| | Discovered in Version: 16.35.2000 |
| | Fixed in Release: 16.35.3006 |

| Internal Ref. | Issue |
|---------------|--|
| 32178 96 | Description: Fixed RDE PATCH operation status code reported in case the property is "read-only". |
| | Keywords: RDE |
| | Discovered in Version: 16.35.1012 |
| | Fixed in Release: 16.35.2000 |
| 32413 57 | Description: Fixed an issue in MCTP-over-PCIe, where the VDM message with the type Route-to-Root Complex, the target ID was not set as 0x0. |
| | Keywords: MCTP-over-PCIe, VDM message |
| | Discovered in Version: 16.35.1012 |
| | Fixed in Release: 16.35.2000 |

| Internal Ref. | Issue |
|---------------|--|
| 32278 73 | Description: Fixed an issue that caused RDE (Redfish) PATCH operation to <code>LLDPTransmit</code> properties <code>"ManagementAddressIPv4"</code> , <code>"ManagementAddressIPv6"</code> and <code>"ManagementAddressMAC"</code> to be applied only in the first attempt but failed in the next. |
| | Keywords: RDE (Redfish) PATCH operation |
| | Discovered in Version: 16.34.1002 |
| | Fixed in Release: 16.35.1012 |
| 31846 25 | Description: Fixed an issue that caused PLDM AEN event receiver media to be changed unexpectedly and destination BDF to be overridden with garbage when some PLDM packet were received from the SMBus layer. |
| | Keywords: PLDM AEN event receiver media |
| | Discovered in Version: 16.34.1002 |
| | Fixed in Release: 16.35.1012 |
| 30481 62 | Description: Fixed the reduction flows behavior to ensure the configuration does not exceed the total number of supported functions. Bad configuration of number of VFs and SFs may lead to consume too many functions and trigger a FW assert 0x888E. |
| | Keywords: VFs, SFs, FW assert |
| | Discovered in Version: 16.34.1002 |
| | Fixed in Release: 16.35.1012 |
| 31476 48 | Description: Fixed an issue that prevented InfiniBand L2 QP from receiving RDMA traffic. |
| | Keywords: RDMA traffic |
| | Discovered in Version: 16.34.1002 |
| | Fixed in Release: 16.35.1012 |
| 28244 27 | Description: Running with a debug firmware reduces security as if token was applied. |
| | Keywords: Debug Firmware |
| | Discovered in Version: 16.33.1048 |

| Internal Ref. | Issue |
|---------------|-------------------------------------|
| | Fixed in Release: 16.35.1012 |

| Internal Ref. | Issue |
|---------------|---|
| 3134894 | Description: Fixed an issue where <code>set_flow_table_entry</code> failed when <code>aso_flow_meter</code> action was used. |
| | Keywords: ASO Flow Meter, FW Steering |
| | Discovered in Version: 16.30.1004 |
| | Fixed in Release: 16.34.1002 |
| 3059379 | Description: Added "Command Unsupported" response code in cases when running the MCTP control command "Get Vendor Defined Messages Supported", and there were no supported VDMs. |
| | Keywords: MCTP control command |
| | Discovered in Version: 16.30.1004 |
| | Fixed in Release: 16.34.1002 |
| 2994292 | Description: Fixed a race condition occurred between the duplicate read and QP commands (2RST, 2ERR and Destroy) in the signature that caused the command to hang. |
| | Keywords: Race condition |
| | Discovered in Version: 16.30.1004 |
| | Fixed in Release: 16.34.1002 |
| 3059082 | Description: When all traffic applications sharing the same combination of <function, priority, side> are rate limited (for example by congestion control), this limit is enforced on other applications with different combinations of <function, priority, side> under the same VL. For example, requestor flows (RDMA-write) are limited to rate X, however, this rate is also enforced on a QP sending RDMA-read responses. This firmware version prevents rate limit enforcement on traffic applications which should not be limited. |
| | Keywords: Rate limit enforcement |
| | Discovered in Version: 16.32.1010 |
| | Fixed in Release: 16.34.1002 |

| Internal Ref. | Issue |
|---------------|---|
| 2785026 | Description: Fixed a rare case that caused the QP not to receive a completion. |
| | Keywords: QP |
| | Discovered in Version: 16.32.1010 |
| | Fixed in Release: 16.33.1048 |

| Internal Ref. | Issue |
|---------------|--|
| 2513453 | <p>Description: Fixed rare lanes skew issue that caused CPU to timeout in Rec.idle.</p> <p>Keywords: PCIe</p> <p>Discovered in Version: 16.32.1010</p> <p>Fixed in Release: 16.33.1048</p> |
| 2961149 | <p>Description: Fixed an issue that caused the card to mask some PCIe AER reporting.</p> <p>Keywords: AER</p> <p>Discovered in Version: 16.32.1010</p> <p>Fixed in Release: 16.33.1048</p> |
| 2860816 | <p>Description: Fixed a wrong flow of credits blockage that prevented booting during DC cycle test.</p> <p>Keywords: DC cycle test</p> <p>Discovered in Version: 16.32.1010</p> <p>Fixed in Release: 16.33.1048</p> |
| 2882943 | <p>Description: Fixed an issue with BMC medium migration from SMBUS to PCIe, and increased FIFOs to pass large packets in case of the migration.</p> <p>Keywords: BMC medium migration</p> <p>Discovered in Version: 16.32.1010</p> <p>Fixed in Release: 16.33.1048</p> |
| 2860409 | <p>Description: Enabled delay drop for hairpin packets. If a hairpin QP is created with delay_drop_en enabled, the feature will be enabled across all GVMs, based on the delay drop status.</p> <p>Keywords: Hairpin delay drop</p> <p>Discovered in Version: 16.32.1010</p> <p>Fixed in Release: 16.33.1048</p> |

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