

NVIDIA ConnectX-6 DE Adapter Cards Firmware Release Notes v22.41.1000

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1 Release Notes Update History

Version	Date	Description
22.41.1000	May 05, 2024	Initial release of this Release Notes version, This version introduces <u>Changes and New</u> <u>Features</u> and <u>Bug Fixes</u> .

2 Overview

Firmware which is added at the time of manufacturing, is used to run user programs on the device and can be thought of as the software that allows hardware to run. Embedded firmware is used to control the functions of various hardware devices and systems, much like a computer's operating system (OS) controls the function of software applications. Firmware may be written into read-only memory (ROM), erasable programmable read-only memory (EPROM) or flash memory.

2.1 Firmware Download

Please visit the firmware webpage.

2.2 Document Revision History

A list of the changes made to this document are provided in **Document Revision History**.

3 Firmware Compatible Products

The chapter contains the following sections:

These are the release notes for the NVIDIA® ConnectX®-6 DE adapters firmware. This firmware supports the following protocols:

- InfiniBand SDR, QDR, FDR, EDR, HDR100, HDR
- PCI Express 4.0, supporting backwards compatibility for v3.0, v2.0 and v1.1
- 1. Speed that supports both NRZ and PAM4 modes in Force mode and Auto-Negotiation mode.



When connecting an NVIDIA-to-NVIDIA adapter card in ETH PAM4 speeds, Auto-Neg should always be enabled.



Please make sure to use a PCIe slot that can supply the required power to the ConnectX-6 DE adapter card as stated in section Specifications in the adapter card's User Manual.

3.1 Supported Devices

This firmware supports the devices and protocols listed below:

NVIDIA SKU	Legacy OPN	PSID	Device Name
900-9X0BC-001H-	MCX683105AN-	MT_00000009	Nvidia ConnectX-6 DE InfiniBand adapter, HDR, single-port QSFP, PCIe 4.0 x16, No Crypto, Tall Bracket
ST1	HDAT	03	

3.2 Driver Software, Tools and Switch Firmware

The following are the drivers' software, tools, switch/HCA firmware versions tested that you can upgrade from or downgrade to when using this firmware version:

	Supported Version
ConnectX-6 DE Firmware	22.41.1000 / 22.40.1000 / 22.39.2048
MLNX_OFED	24.04-0.6.6.0 / 24.01-0.3.3.1 / 23.10-1.1.9.0 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes.
MLNX_EN (MLNX_OFED based code)	24.04-0.6.6.0 / 24.01-0.3.3.1 / 23.10-1.1.9.0 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes.
WinOF-2	24.4.50000 / 24.1.50000 / 23.10.50000 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes.

	Supported Version	
MFT	4.28.0-92 / 4.27.0 / 4.26.1 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes.	
mstflint	4.28.0-92 / 4.27.0 / 4.26.1 Note: For the list of the supported Operating Systems, please refer to the driver's Release Notes.	
FlexBoot	3.7.400	
UEFI	14.34.12	
MLNX-OS	3.10.5002 onwards	
Cumulus	5.4 onwards	
NVIDIA Quantum Firmware	27.2012.1010 onwards	
SwitchX-IB 2 Firmware	15.2010.5108 onwards	
SwitchX-IB Firmware	11.2008.3328 onwards	

4 Changes and New Features

4.1 Important Notes

▲ SR-IOV - Virtual Functions (VF) per Port - The maximum Virtual Functions (VF) per port is 127. For further information, see Known Issues.



It is recommended to enable the "above 4G decoding" BIOS setting for features that require a large amount of PCIe resources (e.g., SR-IOV with numerous VFs, PCIe Emulated Switch, Large BAR Requests).



Security Hardening Enhancements: This release contains important reliability improvements and security hardening enhancements. NVIDIA recommends upgrading your devices' firmware to this release to improve the devices' firmware security and reliability.

Feature/Change	Description	
	22.41.1000	
Unify Rx/Tx Table Domains in FDB	The new unified_fdb subdomain simplifies the FDB model by eliminating the need to duplicate rules for RX and TX tables. This domain is directionless, meaning no RX/TX specific actions are allowed. Firmware now handlea packet transitions IN and OUT of the unified domain, allowing for a more streamlined packet flow management. Software can now transition between unified_fdb and FDB_RX/FDB_TX domains as long as the packet maintains the same direction, without the risk of dropping the packet when crossing between RX and TX.	
TRNG FIPS Compliance	Implemented Deterministic Random Bit Generator (DRBG) algorithm on top of firmware TRNG (the source for raw data input) in accordance with NIST SP800-90A.	
vDPA Live Migration	Added support for vDPA virtual queue state change from suspend to ready, and discrete mkey for descriptor. vDPA Live Migration uses these two new capabilities to reduce downtime since vq can go back to ready state for traffic and descriptor-only-mkey can help reduce mkey mapping time.	
64M Active Connections	Added the ability to generate up to 2^30 STE objects through the general object creation command.	
NVConfig	Added a new NVConfig option to copy AR bit from the BTH header to the DHCP header.	
Steering	Added the option provide field's offset and length in Steering add_action option.	
Steering Match	Added support for steering match on packet l4_type through FTG/FTE.	
Flex Parser Merge Mechanism	Extended Flex Parser merge mechanism to support hardware capabilities.	
Flex Parser	Enabled the option to disable the native parser when the parse graph node is configured with the same conditions.	

Feature/Change	Description
22.41.1000	
Flex Parser Added support for father/son headers parsing.	
LRO Added support for tunnel_offload in LRO.	
Bug Fixes See Bug Fixes in this Firmware Version section.	

4.2 Unsupported Features and Commands

4.2.1 Unsupported Features

The following advanced feature are unsupported in the current firmware version:

- The following service types:
 - SyncUMR
 - Mellanox transport
 - RAW IPv6
- INT-A not supported for EQs only MSI-X
- PCI VPD write flow (RO flow supported)
- Streaming Receive Queue (STRQ) and collapsed CQ
- Subnet Manager (SM) on VFs
- RoCE LAG in Multi-Host/Socket-Direct

4.2.2 Unsupported Commands

- QUERY_MAD_DEMUX
- SET_MAD_DEMUX
- CREATE_RQ MEMORY_RQ_RMP
- MODIFY_LAG_ASYNC_EVENT

5 Bug Fixes in this Firmware Version

For a list of old Bug Fixes, please see <u>Bug Fixes History</u>.

Internal Ref.	Issue
3677596	Description: Fixed an issue that caused the GTP length field to automatically be modified at the TX side when an incorrect or invalid length was detected.
	Keywords: GTP length field, Tx
	Discovered in Version: 22.39.1002
	Fixed in Release: 22.41.1000
3751258	Description: Fixed an issue that caused the switch port to go down due to MC packets mirrored back to the switch when in promiscuous mode.
	Keywords: Promiscuous mode, switch, MC packets
	Discovered in Version: 22.39.1002
	Fixed in Release: 22.41.1000
3555832	Description: Fixed an issue that caused traffic failure when modifying the VIRTIO_NET_F_MRG_RXBUF bit for the VDPA device during traffic.
	Keywords: VDPA, MRG_RXBUF
	Discovered in Version: 22.39.1002
	Fixed in Release: 22.41.1000
3771100	Description: Fixed an issue that resulted in the second mkey index returning even if it was not set in the creation of the virtio q when querying virtio q object.
	Keywords: VDPA, virtio, query object
	Discovered in Version: 22.39.1002
	Fixed in Release: 22.41.1000
3691774	Description: Fixed an issue that resulted in traffic loss after performing Live Migration with virtio vq "frozen-ready" feature. Note: When the traffic load is high, and the vq frozen-ready cap is on, traffic loss might still be experienced after modifying the vq from suspend to ready mode.
	Keywords: VDPA, live migration, virtio, resume
	Discovered in Version: 22.39.1002
	Fixed in Release: 22.41.1000

6 Known Issues

VF Network Function Limitations in SRIOV Legacy Mode

Dual Port Device	Single Port Device
127 VF per PF (254 functions)	127

VF Network Function Limitations in Switchdev Mode

Dual Port Device	Single Port Device
127 VF per PF (254 functions)	127

VF+SF Network Function Limitations in Switchdev Mode

Dual Port Device	Single Port Device
127 VF per PF (254 functions)512 PF+VF+SF per PF (1024 functions)	127 VF (127 functions)512 PF+VF+SF per PF (512 functions)

Known Issues

ConnectX-6 DE has the same feature set and limitations as ConnectX-6 adapter card. For the list of ConnectX-6 Known Issues, please go to https://docs.nvidia.com/networking/category/connectx6fw.

The below are limitations related to ConnectX-6 DE only.

	etow are timitations retated to connective DE only.						
Inte rnal Ref.	Issue						
35258 65	Description: Unexpected system behavior might be observed if the driver is loaded while reset is in progress.						
	Workaround: N/A						
	Keywords: Sync 1 reset, firmware reset						
	Discovered in Version: 22.39.1002						
34574 72	Description: Disabling the Relaxed Ordered (RO) capability (relaxed_ordering_read_pci_enabled=0) using the vhca_resource_manager is currently not functional.						
	Workaround: N/A						
	Keywords: Relaxed Ordered						
	Discovered in Version: 22.37.1014						
28788 41	Description: Firmware rollback fails for the signature retransmit flow if the QPN field is configured in the mkey (as it only allows the given QP to use this Mkey) as the firmware rollback flow relies on an internal QP that uses the mkey.						
	Workaround: N/A						
	Keywords: Signature retransmit flow						
	Discovered in Version: 22.37.1014						

Inte rnal Ref.	Issue						
33291	Description: MFS1S50-H003E cable supports only HDR rate when used as a split cable.						
09	Workaround: N/A						
	Keywords: HDR, split cable, MFS1S50-H003E						
	Discovered in Version: 22.36.1010						
27450 23	Description: RDMA statistics for sent packets are not updated when RoCE traffic is running in a loopback on the same uplink.						
	Workaround: N/A						
	Keywords: RoCE						
	Discovered in Version: 22.35.2302						
32007	Description: Changing dynamic PCIe link width is not supported.						
79	Workaround: N/A						
	Keywords: PCIe						
	Discovered in Version: 20.34.1002						
-	Description: A wrong device ID is presented When running the "dev_id" command for ConnectX-6 DE. The device ID shown is the ConnectX-6 Dx instead.						
	Workaround: To be able to identify the ConnectX-6 DE ID, run one of the commands below: • mlxfwmanager • mlxvpd (or mlxburn -vpd)						
	Keywords: Device ID						
	Discovered in Version: 22.32.2306						
28500	Description: Occasionally, when rising a logical link, the link recovery counter is increase by 1.						
03	Workaround: N/A						
	Keywords: Link recovery counter						
	Discovered in Version: 22.32.2306						

7 PreBoot Drivers (FlexBoot/UEFI)

7.1 FlexBoot Changes and New Features

For further information, please refer to the FlexBoot Release Notes.

7.2 UEFI Changes and Major New Features

For further information, please refer to the UEFI Release Notes.

8 Validated and Supported Cables and Switches

8.1 Validated and Supported Cables and Modules

8.1.1 Cables Lifecycle Legend

Lifecycle Phase	Definition
EOL	End of Life
LTB	Last Time Buy
HVM	GA level
MP	GA level
P-Rel	GA level
Preliminary	Engineering Sample
Prototype	Engineering Sample



AVIDIA does not support InfiniBand cables or modules not qualified or approved by NVIDIA.

8.1.2 HDR / 200GbE Cables

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
HDR	N/A	980-9186N-00H0 03*	MCA1J00- H003E*	NVIDIA Active Copper cable, IB HDR, up to 200Gb/s, QSFP56, 3m, yellow pulltab	EOL [MP]
HDR	N/A	980-9186O-00H0 04*	MCA1J00- H004E*	NVIDIA Active Copper cable, IB HDR, up to 200Gb/s, QSFP56, 4m, yellow pulltab	EOL [MP]
HDR	N/A	980-9186P-00H0 05	MCA1J00- H005E	NVIDIA Active Copper cable, IB HDR, up to 200Gb/s, QSFP56, 5m, yellow pulltab	EOL [Prototype]
HDR	N/A	980-91977-00H0 03*	MCA7J50- H003R*	NVIDIA Active copper hybrid cable, IB HDR 200Gb/s to 2xHDR100 100Gb/s, QSFP56 to 2xQSFP56, 3m, colored	EOL [MP]
HDR	N/A	980-91978-00H0 04*	MCA7J50- H004R*	NVIDIA Active copper hybrid cable, IB HDR 200Gb/s to 2xHDR100 100Gb/s, QSFP56 to 2xQSFP56, 4m, colored	EOL [MP]

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
HDR	N/A	980-91979-00H0 05	MCA7J50- H005R	NVIDIA Active copper hybrid cable, IB HDR 200Gb/s to 2xHDR100 100Gb/s, QSFP56 to 2xQSFP56, 5m, colored	EOL [Prototype]
HDR	200GE	980-9I548-00H0 01	MCP1650- H001E30	Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 1m	HVM
HDR	200GE	980-91549-00H0 02	MCP1650- H002E26	Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 2m	HVM
HDR	200GE	980-9I54A-00H0 0A	MCP1650- H00AE30	Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 0.5m	HVM
HDR	200GE	980-9I54B-00H0 1A	MCP1650- H01AE30	Nvidia Passive Copper cable, up to 200Gbps, QSFP56 to QSFP56, 1.5 m	HVM
HDR	200GE	980-9I39E-00H0 01	MCP7H50- H001R30	Nvidia Passive copper splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2xQSFP56, 1m	HVM
HDR	200GE	980-9199F-00H0 02	MCP7H50- H002R26	Nvidia Passive copper splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2xQSFP56, 2m	HVM
HDR	200GE	980-9198G-00H0 1A	MCP7H50- H01AR30	Nvidia Passive copper splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2xQSFP56, 1.5m	HVM
HDR	200GE	980-9146K-00H0 01	MCP7Y60-H001	NVIDIA passive copper splitter cable, 400(2x200)Gbps to 2x200Gbps, OSFP to 2xQSFP56, 1m, fin to flat	MP
HDR	200GE	980-9146L-00H0 02	MCP7Y60-H002	NVIDIA passive copper splitter cable, 400(2x200)Gbps to 2x200Gbps, OSFP to 2xQSFP56, 2m, fin to flat	MP
HDR	200GE	980-9193M-00H0 1A	MCP7Y60-H01A	NVIDIA passive copper splitter cable, 400(2x200)Gbps to 2x200Gbps, OSFP to 2xQSFP56, 1.5m, fin to flat	MP
HDR	200GE	980-9193N-00H0 01	MCP7Y70-H001	NVIDIA passive copper splitter cable, 400(2x200)Gbps to 4x100Gbps, OSFP to 4xQSFP56, 1m, fin to flat	MP

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
HDR	200GE	980-9193O-00H0 02	MCP7Y70-H002	NVIDIA passive copper splitter cable, 400(2x200)Gbps to 4x100Gbps, OSFP to 4xQSFP56, 2m, fin to flat	MP
HDR	200GE	980-9I47P-00H0 1A	MCP7Y70-H01A	NVIDIA passive copper splitter cable, 400(2x200)Gbps to 4x100Gbps, OSFP to 4xQSFP56, 1.5m, fin to flat	MP
HDR	N/A	980-9I123-00H0 03	MFS1S00-H003- LL	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 3m	EOL [P-Rel]
HDR	N/A	980-9I124-00H0 03	MFS1S00- H003E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 3m	EOL [HVM]
HDR	200GE	980-9I457-00H0 03	MFS1S00- H003V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 3m	MP
HDR	N/A	980-91449-00H0 05	MFS1S00-H005- LL	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 5m	EOL [P-Rel]
HDR	N/A	980-9145A-00H0 05	MFS1S00- H005E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 5m	EOL [HVM]
HDR	200GE	980-9I45D-00H0 05	MFS1S00- H005V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 5m	MP
HDR	N/A	980-9I44F-00H0 10	MFS1S00-H010- LL	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 10m	EOL [P-Rel]
HDR	N/A	980-9I45G-00H0 10	MFS1S00- H010E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 10m	EOL [HVM]
HDR	200GE	980-9I45J-00H0 10	MFS1S00- H010V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 10m	MP
HDR	N/A	980-9I44L-00H0 15	MFS1S00-H015- LL	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 15m	EOL [P-Rel]

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
HDR	N/A	980-9I45M-00H0 15	MFS1S00- H015E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 15m	EOL [HVM]
HDR	200GE	980-9I45O-00H0 15	MFS1S00- H015V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 15m	MP
HDR	N/A	980-9144Q-00H0 20	MFS1S00-H020- LL	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 20m	EOL [P-Rel]
HDR	N/A	980-9145R-00H0 20	MFS1S00- H020E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 20m	EOL [HVM]
HDR	200GE	980-9I45T-00H0 20	MFS1S00- H020V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 20m	MP
HDR	N/A	980-9145X-00H0 30	MFS1S00-H030- LL	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, low latency, 30m	EOL [P-Rel]
HDR	N/A	980-9145Y-00H0 30	MFS1S00- H030E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 30m	EOL [HVM]
HDR	200GE	980-91440-00H0 30	MFS1S00- H030V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 30m	MP
HDR	N/A	980-91455-00H0 50	MFS1S00- H050E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 50m	EOL [HVM]
HDR	200GE	980-9I447-00H0 50	MFS1S00- H050V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 50m	MP
HDR	N/A	980-9I44G-00H1 00	MFS1S00- H100E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 100m	EOL [HVM]
HDR	200GE	980-9I44H-00H1 00	MFS1S00- H100V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 100m	MP
HDR	N/A	980-9I44I-00H13 0	MFS1S00- H130E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 130m	EOL [HVM]
HDR	200GE	980-9I44K-00H1 30	MFS1S00- H130V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 130m	MP

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
HDR	N/A	980-9I45L-00H1 50	MFS1S00- H150E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 150m	EOL [HVM]
HDR	200GE	980-9I44N-00H1 50	MFS1S00- H150V	Nvidia active optical cable, up to 200Gbps , QSFP56 to QSFP56, 150m	MP
HDR	N/A	980-9145O-00H2 00	MFS1S00- H200E	NVIDIA active fiber cable, IB HDR, up to 200Gb/s, QSFP56, LSZH, black pulltab, 200m	EOL [EVT]
HDR	N/A	980-91452-00H0 03	MFS1S50- H003E	NVIDIA active fiber splitter cable, IB HDR, 200Gb/s to 2x100Gb/s, QSFP56 to 2xQSFP56, LSZH, 3m	EOL [HVM]
HDR	200GE	980-91445-00H0 03	MFS1S50- H003V	Nvidia active optical splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2x QSFP56, 3m	HVM
HDR	N/A	980-91956-00H0 05	MFS1S50- H005E	NVIDIA active fiber splitter cable, IB HDR, 200Gb/s to 2x100Gb/s, QSFP56 to 2xQSFP56, LSZH, 5m	EOL [HVM]
HDR	200GE	980-91969-00H0 05	MFS1S50- H005V	Nvidia active optical splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2x QSFP56, 5m	HVM
HDR	N/A	980-9195A-00H0 10	MFS1S50- H010E	NVIDIA active fiber splitter cable, IB HDR, 200Gb/s to 2x100Gb/s, QSFP56 to 2xQSFP56, LSZH, 10m	EOL [HVM]
HDR	200GE	980-9I96D-00H0 10	MFS1S50- H010V	Nvidia active optical splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2x QSFP56, 10m	HVM
HDR	N/A	980-9I95E-00H0 15	MFS1S50- H015E	NVIDIA active fiber splitter cable, IB HDR, 200Gb/s to 2x100Gb/s, QSFP56 to 2xQSFP56, LSZH, 15m	EOL [HVM]
HDR	200GE	980-9I96H-00H0 15	MFS1S50- H015V	Nvidia active optical splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2x QSFP56, 15m	HVM
HDR	N/A	980-91951-00H02 0	MFS1S50- H020E	NVIDIA active fiber splitter cable, IB HDR, 200Gb/s to 2x100Gb/s, QSFP56 to 2xQSFP56, LSZH, 20m	EOL [HVM]

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
HDR	200GE	980-9I96L-00H0 20	MFS1S50- H020V	Nvidia active optical splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2x QSFP56, 20m	HVM
HDR	N/A	980-9195M-00H0 30	MFS1S50- H030E	NVIDIA active fiber splitter cable, IB HDR, 200Gb/s to 2x100Gb/s, QSFP56 to 2xQSFP56, LSZH, 30m	EOL [HVM]
HDR	200GE	980-9I96P-00H0 30	MFS1S50- H030V	Nvidia active optical splitter cable, 200Gbps to 2x100Gbps, QSFP56 to 2x QSFP56, 30m	HVM
HDR	N/A	980-9I95Z-00H0 03	MFS1S90- H003E	NVIDIA active fiber splitter cable, IB HDR, 2x200Gb/s to 2x200Gb/s, 2xQSFP56 to 2xQSFP56, LSZH, 3m	EOL [HVM]
HDR	N/A	980-91960-00H0 05	MFS1S90- H005E	NVIDIA active fiber splitter cable, IB HDR, 2x200Gb/s to 2x200Gb/s, 2xQSFP56 to 2xQSFP56, LSZH, 5m	EOL [HVM]
HDR	N/A	980-9I961-00H0 10	MFS1S90- H010E	NVIDIA active fiber splitter cable, IB HDR, 2x200Gb/s to 2x200Gb/s, 2xQSFP56 to 2xQSFP56, LSZH, 10m	LTB [HVM]
HDR	N/A	980-9I962-00H0 15	MFS1S90- H015E	NVIDIA active fiber splitter cable, IB HDR, 2x200Gb/s to 2x200Gb/s, 2xQSFP56 to 2xQSFP56, LSZH, 15m	EOL [HVM]
HDR	N/A	980-91423-00H0 20	MFS1S90- H020E	NVIDIA active fiber splitter cable, IB HDR, 2x200Gb/s to 2x200Gb/s, 2xQSFP56 to 2xQSFP56, LSZH, 20m	LTB [HVM]
HDR	N/A	980-91424-00H0 30	MFS1S90- H030E	NVIDIA active fiber splitter cable, IB HDR, 2x200Gb/s to 2x200Gb/s, 2xQSFP56 to 2xQSFP56, LSZH, 30m	EOL [HVM]
HDR	N/A	980-9I17S-00HS 00	MMA1T00-HS	NVIDIA transceiver, HDR, QSFP56, MPO, 850nm, SR4, up to 100m	HVM
HDR	NA	980-9I45E-09H0 70	MFS1S00- H070V	NVIDIA active optical cable, up to 200Gb/s IB HDR, QSFP56, LSZH, 70m	MP



*These cables were approved for switch-to-switch connectivity. For switch-to-host connectivity there may be some issues. See Known Issue 1959529 in the Known Issues section.

8.1.3 EDR / 100GbEs Cables

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
EDR	100GE	980-9I62P-00C 001	MCP1600-E001	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1m 30AWG	EOL [HVM]
EDR	N/A	980-9162Q-00E 001	MCP1600- E001E30	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 1m, Black, 30AWG	HVM
EDR	100GE	980-9162S-00C0 02	MCP1600-E002	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 2m 28AWG	EOL [HVM]
EDR	N/A	980-9162T-00E0 02	MCP1600- E002E26	NVIDIA® Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 2m, Black, 26AWG	Preliminary
EDR	N/A	980-9162U-00E 002	MCP1600- E002E30	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 2m, Black, 30AWG	HVM
EDR	100GE	980-9162V-00C 003	MCP1600-E003	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 3m 26AWG	EOL [HVM]
EDR	N/A	980-9162W-00E 003	MCP1600- E003E26	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 3m, Black, 26AWG	HVM
EDR	N/A	980-9162Y-00E0 04	MCP1600- E004E26	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 4m, Black, 26AWG	EOL [HVM]
EDR	N/A	980-9162Z-00E0 05	MCP1600- E005E26	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 5m, Black, 26AWG	HVM
EDR	N/A	980-91620-00E0 0A	MCP1600-E00A	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 0.5m 30AWG	EOL [HVM]
EDR	N/A	980-91621-00E0 0A	MCP1600- E00AE30	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 0.5m, Black, 30AWG	EOL [HVM]

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
EDR	N/A	980-91622-00E0 0B	MCP1600- E00BE30	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 0.75m, Black, 30AWG	EOL [HVM] [HIBERN/ATE]
EDR	100GE	980-91623-00C 01A	MCP1600-E01A	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1.5m 30AWG	EOL [HVM]
EDR	N/A	980-91624-00E0 1A	MCP1600- E01AE30	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 1.5m, Black, 30AWG	HVM
EDR	N/A	980-91625-00E0 1C	MCP1600- E01BE30	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 1.25m, Black, 30AWG	EOL [HVM] [HIBERN/ATE]
EDR	100GE	980-91626-00C 02A	MCP1600-E02A	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 2.5m 26AWG	EOL [HVM]
EDR	N/A	980-91627-00E0 2A	MCP1600- E02AE26	NVIDIA Passive Copper cable, IB EDR, up to 100Gb/s, QSFP28, 2.5m, Black, 26AWG	HVM
EDR	N/A	980-9I13D-00E 001	MFA1A00-E001	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 1m	HVM
EDR	N/A	980-9I13F-00E0 03	MFA1A00-E003	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 3m	HVM
EDR	N/A	980-9I13J-00E0 05	MFA1A00-E005	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 5m	HVM
EDR	N/A	980-9I13M-00E 007	MFA1A00-E007	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 7m	LTB [HVM]
EDR	N/A	980-9I13O-00E 010	MFA1A00-E010	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 10m	HVM
EDR	N/A	980-9I13S-00E0 15	MFA1A00-E015	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 15m	HVM
EDR	N/A	980-9I13V-00E0 20	MFA1A00-E020	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 20m	HVM
EDR	N/A	980-9I13Y-00E0 30	MFA1A00-E030	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 30m	HVM

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
EDR	N/A	980-91133-00E0 50	MFA1A00-E050	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 50m	HVM
EDR	N/A	980-9I135-00E1 00	MFA1A00-E100	NVIDIA active fiber cable, IB EDR, up to 100Gb/s, QSFP, LSZH, 100m	LTB [HVM]
EDR	N/A	980-9117L-00E0 00	MMA1B00-E100	NVIDIA transceiver, IB EDR, up to 100Gb/s, QSFP28, MPO, 850nm, SR4, up to 100m	HVM

▲ EDR links raise with RS-FEC.

8.1.4 FDR Cables

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy OPN	Description	LifeCycle Phase
FDR	56GE	980-91679-00L0 04	MC2207126-00 4	NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 4m	EOL [HVM]
FDR	56GE	980-9I67A-00L0 03	MC2207128-00 3	NVIDIA passive copper cable, VPI, up to 56Gb/ s, QSFP, 3m	EOL [HVM]
FDR	56GE	980-9I67C-00L0 2A	MC2207128-0A 2	NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, 2.5m	EOL [MP]
FDR	56GE	980-9I67D-00L0 01	MC2207130-00 1	NVIDIA passive copper cable, VPI, up to 56Gb/ s, QSFP, 1m	EOL [HVM]
FDR	56GE	980-9I67E-00L0 02	MC2207130-00 2	NVIDIA passive copper cable, VPI, up to 56Gb/ s, QSFP, 2m	EOL [HVM]
FDR	56GE	980-9I67F-00L0 0A	MC2207130-00 A	NVIDIA passive copper cable, VPI, up to 56Gb/ s, QSFP, 0.5m	EOL [HVM]
FDR	56GE	980-9I67G-00L0 1A	MC2207130-0A 1	NVIDIA passive copper cable, VPI, up to 56Gb/ s, QSFP, 1.5m	EOL [HVM]
FDR	56GE	980-9I15U-00L0 03	MC220731V-00 3	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 3m	EOL [HVM]
FDR	56GE	980-9115V-00L0 05	MC220731V-00 5	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 5m	EOL [HVM]

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy OPN	Description	LifeCycle Phase
FDR	56GE	980-9I15W-00L 010	MC220731V-01 0	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 10m	EOL [HVM]
FDR	56GE	980-9I15X-00L0 15	MC220731V-01 5	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 15m	EOL [HVM]
FDR	56GE	980-9I15Y-00L0 20	MC220731V-02 0	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 20m	EOL [HVM]
FDR	56GE	980-9I15Z-00L0 25	MC220731V-02 5	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 25m	EOL [HVM]
FDR	56GE	980-9I150-00L0 30	MC220731V-03 0	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 30m	EOL [HVM]
FDR	56GE	980-9I151-00L0 40	MC220731V-04 0	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 40m	EOL [HVM] [HIBERN/ATE]
FDR	56GE	980-9I152-00L0 50	MC220731V-05 0	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 50m	EOL [HVM]
FDR	56GE	980-9I153-00L0 75	MC220731V-07 5	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 75m	EOL [HVM]
FDR	56GE	980-9I154-00L1 00	MC220731V-10 0	NVIDIA active fiber cable, VPI, up to 56Gb/s, QSFP, 100m	EOL [HVM]
FDR	56GE	980-91675-00L0 01	MCP170L-F001	NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 1m	EOL [P-Rel]
FDR	56GE	980-91676-00L0 02	MCP170L-F002	NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 2m	EOL [P-Rel]
FDR	56GE	980-91677-00L0 03	MCP170L-F003	NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 3m	EOL [P-Rel] [HIBERN/ATE]
FDR	56GE	980-91678-00L0 0A	MCP170L-F00A	NVIDIA passive copper cable, VPI, up to 56Gb/ s, QSFP, LSZH, 0.5m	EOL [P-Rel]
FDR	56GE	980-91679-00L0 1A	MCP170L-F01A	NVIDIA passive copper cable, VPI, up to 56Gb/s, QSFP, LSZH, 1.5m	EOL [P-Rel] [HIBERN/ATE]
FDR	N/A	980-9I17M-00FS 00	MMA1B00- F030D	NVIDIA transceiver, FDR, QSFP+, MPO, 850nm, SR4, up to 30m, DDMI	LTB [HVM]

8.1.5 FDR10 Cables

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
FDR10	40GE	980-9I66U-00B0 04	MC2206128-004	NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 4m	EOL [HVM] [HIBERN/ATE]
FDR10	40GE	980-9166V-00B0 05	MC2206128-005	NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 5m	EOL [HVM]
FDR10	40GE	980-9I66W-00B 001	MC2206130-001	NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 1m	EOL [HVM]
FDR10	40GE	980-9I66X-00B0 02	MC2206130-002	NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 2m	EOL [HVM]
FDR10	40GE	980-9I66Y-00B0 03	MC2206130-003	NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 3m	EOL [HVM]
FDR10	40GE	980-9I66Z-00B0 0A	MC2206130-00 A	NVIDIA passive copper cable, VPI, up to 40Gb/s, QSFP, 0.5m	EOL [HVM]
FDR10	N/A	980-9I140-00T0 03	MC2206310-003	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 3m	EOL [HVM]
FDR10	N/A	980-9I141-00T0 05	MC2206310-005	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 5m	EOL [HVM]
FDR10	N/A	980-9I142-00T0 10	MC2206310-010	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 10m	EOL [HVM]
FDR10	N/A	980-9I143-00T0 15	MC2206310-015	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 15m	EOL [HVM]
FDR10	N/A	980-9I144-00T0 20	MC2206310-020	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 20m	EOL [HVM]
FDR10	N/A	980-9I145-00T0 30	MC2206310-030	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 30m	EOL [HVM]
FDR10	N/A	980-9I147-00T0 50	MC2206310-050	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 50m	EOL [HVM]
FDR10	N/A	980-9I148-00T1 00	MC2206310-100	NVIDIA active fiber cable, IB QDR/FDR10, 40Gb/s, QSFP, 100m	EOL [HVM]
FDR10	40GE	980-9I170-00B M00	MC2210411- SR4E	NVIDIA optical module, 40Gb/s, QSFP, MPO, 850nm, up to 300m	EOL [HVM]

IB Data Rate	Eth Data Rate	NVIDIA P/N	Legacy P/N	Description	LifeCycle Phase
FDR10	N/A	980-9121O-00T R00	MC2210511-LR4	NVIDIA optical module, 40Gb/s, QSFP, LC-LC, 1310nm, LR4 up to 10km	EOL [MP]

8.2 Tested Switches

8.2.1 HDR / 200Gb/s Switches

Speed	Switch Silicon	OPN # / Name	Description	Vendor
HDR	Quantum	MQM8700-xxx	40-port Managed Non-blocking HDR 200Gb/s InfiniBand Smart Switch	NVIDIA
HDR	Quantum	MQM8790-xxx	40-port Unmanaged, Non-blocking HDR 200Gb/s InfiniBand Smart Switch	NVIDIA

8.2.2 EDR / 100Gb/s Switches

Speed	Switch Silicon	OPN # / Name	Description	Vendor
EDR	Switch-IB	MSB7790-XXX	36-port Unmanaged EDR 100Gb/s InfiniBand Switch Systems	NVIDIA
EDR	Switch-IB	MSB7700-XXX	36-port Managed EDR 100Gb/s InfiniBand Switch Systems	NVIDIA
EDR	Switch-IB 2	MSB7800-XXX	36-port Managed EDR 100Gb/s InfiniBand Switch Systems	NVIDIA

8.3 PRM Revision Compatibility

This firmware version complies with the following Programmer's Reference Manual:

 Adapters Programmer's Reference Manual (PRM), Rev 0.53 or later, which has Command Interface Revision 0x5. The command interface revision can be retrieved by means of the QUERY_FW command and is indicated by the field cmd_interface_rev.

9 Supported Non-Volatile Configurations

	Tion voluence co		
Configuration	mlxconfig Parameter Name	Class	TLV ID
NV_MEMIC_CONF	MEMIC_BAR_SIZE	GLOBAL (0)	0x6
	MEMIC_SIZE_LIMIT		
NV_HOST_CHAINING_CONF	HOST_CHAINING_MODE		0x8
	HOST_CHAINING_DESCRIPTORS		
	HOST_CHAINING_TOTAL_BUFFER _SIZE		
NV_FLEX_PARS_CONF	FLEX_PARSER_PROFILE_ENABLE		0xe
	FLEX_IPV4_OVER_VXLAN_PORT		
NV_ROCE_1_5_CONF	ROCE_NEXT_PROTOCOL		0x10
NV_INTERNAL_RESOURCE_ CONF	ESWITCH_HAIRPIN_DESCRIPTOR S		0x13
	ESWITCH_HAIRPIN_TOT_BUFFER _SIZE		
NV_GLOBAL_PCI_CONF	NON_PREFETCHABLE_PF_BAR		0x80
	NUM_OF_VFS		
	SRIOV_EN	_	
	PF_LOG_BAR_SIZE		
	VF_LOG_BAR_SIZE		
	NUM_PF_MSIX		
	NUM_VF_MSIX		
NV_TPT_CONF	INT_LOG_MAX_PAYLOAD_SIZE		0x82
NV_POWER_CONF	SW_RECOVERY_ON_ERRORS		0x88
	RESET_WITH_HOST_ON_ERRORS		
	ADVANCED_POWER_SETTINGS		
NV_GLOBAL_MASK	ece_disable_mask		0x116
NV_SW_OFFLOAD_CONFIG	CQE_COMPRESSION		0x10a
	IP_OVER_VXLAN_EN		
	PCI_ATOMIC_MODE		
	LRO_LOG_TIMEOUT0		
	LRO_LOG_TIMEOUT1		
	LRO_LOG_TIMEOUT2		
	LRO_LOG_TIMEOUT3		
	log_max_outstandng_wqe		
	NV_config.sr_enable (ConnectX-6 Dx and above)		
NV_IB_DC_CONF	LOG_DCR_HASH_TABLE_SIZE		0x190

Configuration	mlxconfig Parameter Name	Class	TLV ID
	DCR_LIFO_SIZE		
NV_VPI_LINK_TYPE	LINK_TYPE	PHYSICAL_PORT (2)	0x12
NV_ROCE_CC	ROCE_CC_PRIO_MASK	_	0x107
	ROCE_CC_ALGORITHM	_	
NV_ROCE_CC_ECN	CLAMP_TGT_RATE_AFTER_TIME _INC	_	0x108
	CLAMP_TGT_RATE	_	
	RPG_TIME_RESET	_	
	RPG_BYTE_RESET	-	
	RPG_THRESHOLD	-	
	RPG_MAX_RATE	-	
	RPG_AI_RATE	-	
	RPG_HAI_RATE	_	
	RPG_GD	-	
	RPG_MIN_DEC_FAC	-	
	RPG_MIN_RATE		
	RATE_TO_SET_ON_FIRST_CNP		
	DCE_TCP_G		
	DCE_TCP_RTT		
	RATE_REDUCE_MONITOR_PERIO D		
	INITIAL_ALPHA_VALUE	-	
	MIN_TIME_BETWEEN_CNPS	-	
	CNP_802P_PRIO	_	
	CNP_DSCP	_	
NV_LLDP_NB_CONF	LLDP_NB_DCBX	_	0x10a
	LLDP_NB_RX_MODE	-	
	LLDP_NB_TX_MODE	-	
NV_LLDP_NB_DCBX	DCBX_IEEE	-	0x18e
	DCBX_CEE	_	
	DCBX_WILLING	_	
NV_KEEP_LINK_UP	KEEP_ETH_LINK_UP		0x190
	KEEP_IB_LINK_UP	-	
	KEEP_LINK_UP_ON_BOOT		
	KEEP_LINK_UP_ON_STANDBY	-	
NV_QOS_CONF	NUM_OF_VL	1	0x192

Configuration	mlxconfig Parameter Name	Class	TLV ID
	NUM_OF_TC		
	NUM_OF_PFC		
NV_MPFS_CONF	DUP_MAC_ACTION		0x196
	SRIOV_IB_ROUTING_MODE		
	IB_ROUTING_MODE		
NV_HCA_CONF	PCI_WR_ORDERING	HOST-FUNCTION (3)	0x112
	MULTI_PORT_VHCA_EN		
NV_EXTERNAL_PORT_CTRL	PORT_OWNER		0x192
	ALLOW_RD_COUNTERS		
	RENEG_ON_CHANGE		
	TRACER_ENABLE		
NV_ROM_BOOT_CONF2	IP_VER		0x195
	BOOT_UNDI_NETWORK_WAIT		
NV_ROM_UEFI_CONF	UEFI_HII_EN		0x196
NV_ROM_UEFI_DEBUG_LEV	BOOT_DBG_LOG		0x206
EL	UEFI_LOGS		
NV_ROM_BOOT_CONF1	BOOT_VLAN		0x221
	LEGACY_BOOT_PROTOCOL		
	BOOT_RETRY_CNT		
	BOOT_LACP_DIS		
	BOOT_VLAN_EN		
NV_ROM_IB_BOOT_CONF	BOOT_PKEY		0x222
NV_PCI_CONF	ADVANCED_PCI_SETTINGS	HOST (7)	0x80
SAFE_MODE_CONF	SAFE_MODE_THRESHOLD		0x82
	SAFE_MODE_ENABLE		

10 Release Notes History

10.1 Changes and New Feature History

⚠ This section includes history of changes and new feature of 3 major releases back. For older releases history, please refer to the relevant firmware versions.

Feature/Change	Description
	22.40.1000
Socket Direct Single netdev Mapped to Two PCIe Devices	Enabled Single Netdev mapping to two PCIe devices (Socket Direct). Now multiple devices (PFs) of the same port can be combined under a single netdev instance. Traffic is passed through different devices belonging to different NUMA sockets, thus saving cross-NUMA traffic and allowing apps running on the same netdev from different NUMAs to still feel a sense of proximity to the device and achieve improved performance. The netdev is destroyed once any of the PFs is removed. A proper configuration would utilize the correct close NUMA when working on a certain app/CPU. Currently, this capability is limited to PFs only, and up to two devices (sockets). To enable the feature, one must configure the same Socket Direct group (non zero) for both PFs through mlxconfig SD_GROUP.
ACL	Added support for egress ACL to the uplink by adding a new bit to the Set Flow Table Entry: allow_fdb_uplink_hairpin.
Bug Fixes	See Bug Fixes in this Firmware Version section.

Feature/Change	Description		
	22.39.2048		
Bug Fixes	See Bug Fixes in this Firmware Version section.		

Feature/Change	Description		
	22.39.1002		
Expansion ROM	Added a caching mechanism to improved expansion ROM performance and to avoid any slow boot occurrences when loading the expansion ROM driver.		
Live Migration Support for Image Size above 4GB	Added support for image size above 4GB when performing a live migration by splitting the image to chunks.		
Crypto Algorithms	Extended the role-based authentication to cover all crypto algorithms. Now the TLS. IPsec. MACsec. GCM, mem2mem, and NISP work when		
	<pre>nv_crypto_conf.crypto_policy = CRYPTO_POLICY_FIPS_LEVEL_2, meaning all cryptographic engines can also work in wrapped mode and not only in plaintext mode.</pre>		
Programmable Congestion Control	Programmable Congestion Control is now the default CC mechanism. ZTR_RTTCC is the default CC algorithm when ECE is enabled and the CC algorithm negotiation succeeds, otherwise PCC DCQCN will be used.		

Feature/Change	Description		
22.39.1002			
Reserved mkey	Added new support for reserved mkey index range. When enabled, a range of mkey indexes is reserved for mkey by name use.		
Bug Fixes	See Bug Fixes in this Firmware Version section.		

Feature/Change	Description			
22.38.1900				
QKEY Mitigation in the Kernel	QKEY creation with the MSB set is available now for non-privileged users as well. To allow non-privileged users to create QKEY with MSB set, the below new module parameter was added to ib_uverbs module: • Module Parameter: enforce_qkey_check • Description: Force QKEY MSB check for non-privileged user on UD QP creation • Default: 0 (disabled) Note: In this release, this module parameter is disabled by default to ensure backward compatibility and give customers the opportunity to update their applications accordingly. In the upcoming release, it will be enabled by default, and later on deprecated.			

Feature/Change	Description		
22.38.1002			
INT Packets	Added support for forwarding INT packets to the user application for monitoring purposes by matching the BTH acknowledge request bit (bth_a).		
IPsec CPS Bulk Allocation	Improved the IPsec CPS by using bulk allocation. For cases in which <code>log_obj_range == 0</code> , single IPSEC object will be allocated and initialized as before keeping backward compatibility. For better performance, it is recommended to work with IPsec bulk allocation and to initialize IPsec ASO context not via the firmware but via the hardware using ASO WQE.		
QKEY Mitigation in the Kernel	Non-privileged users are now blocked by default from setting controlled/privileged QKEYs (QKEY with MSB set).		
Bug Fixes	See Bug Fixes in this Firmware Version section.		

10.2 Bug Fixes History



⚠ This section includes history of bug fixes of 3 major releases back. For older releases history, please refer to the relevant firmware versions Release Notes in https:// docs.mellanox.com/category/adapterfw.

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